

Figure 57

36/58

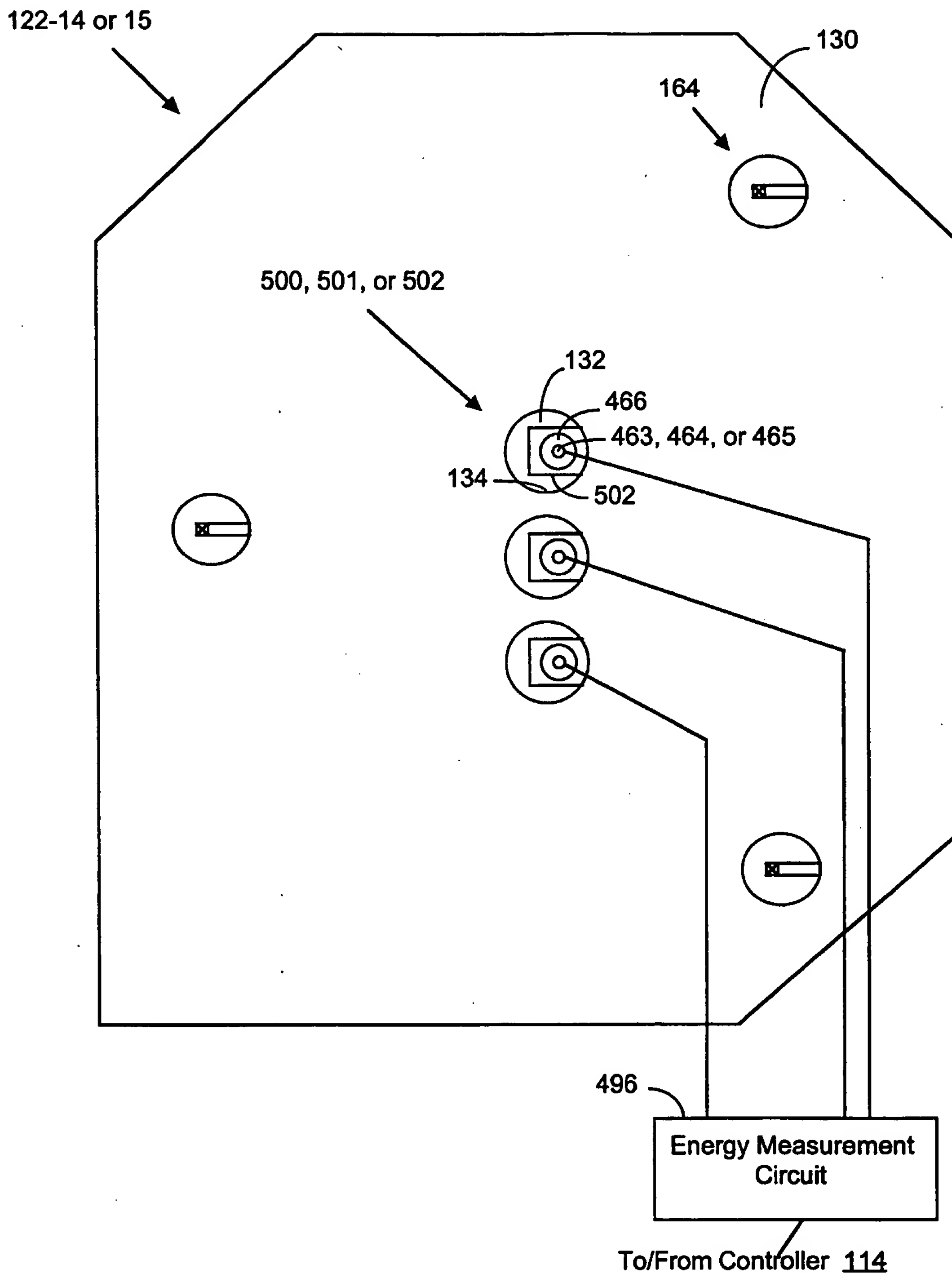


Figure 58

37/58

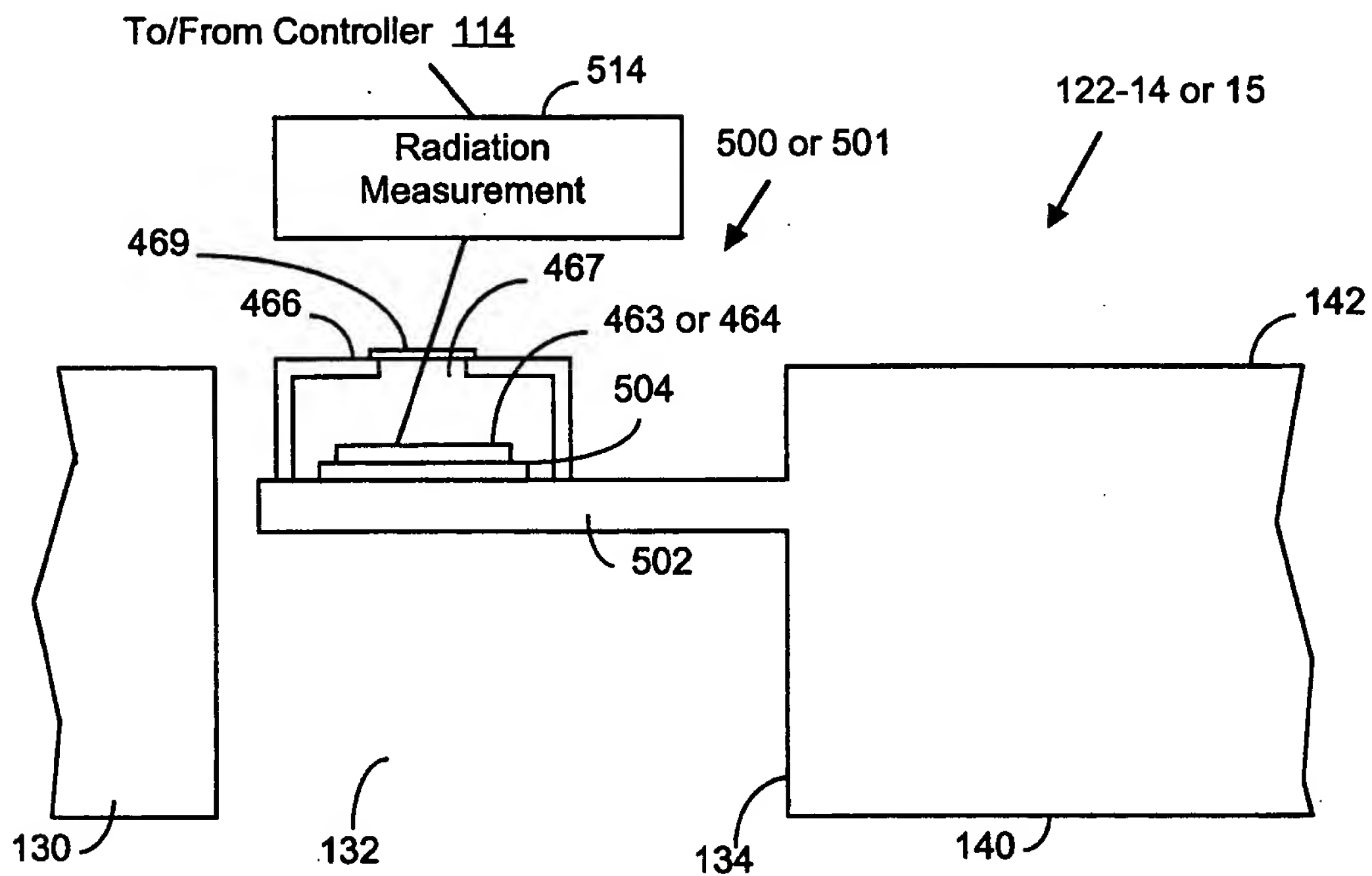


Figure 59

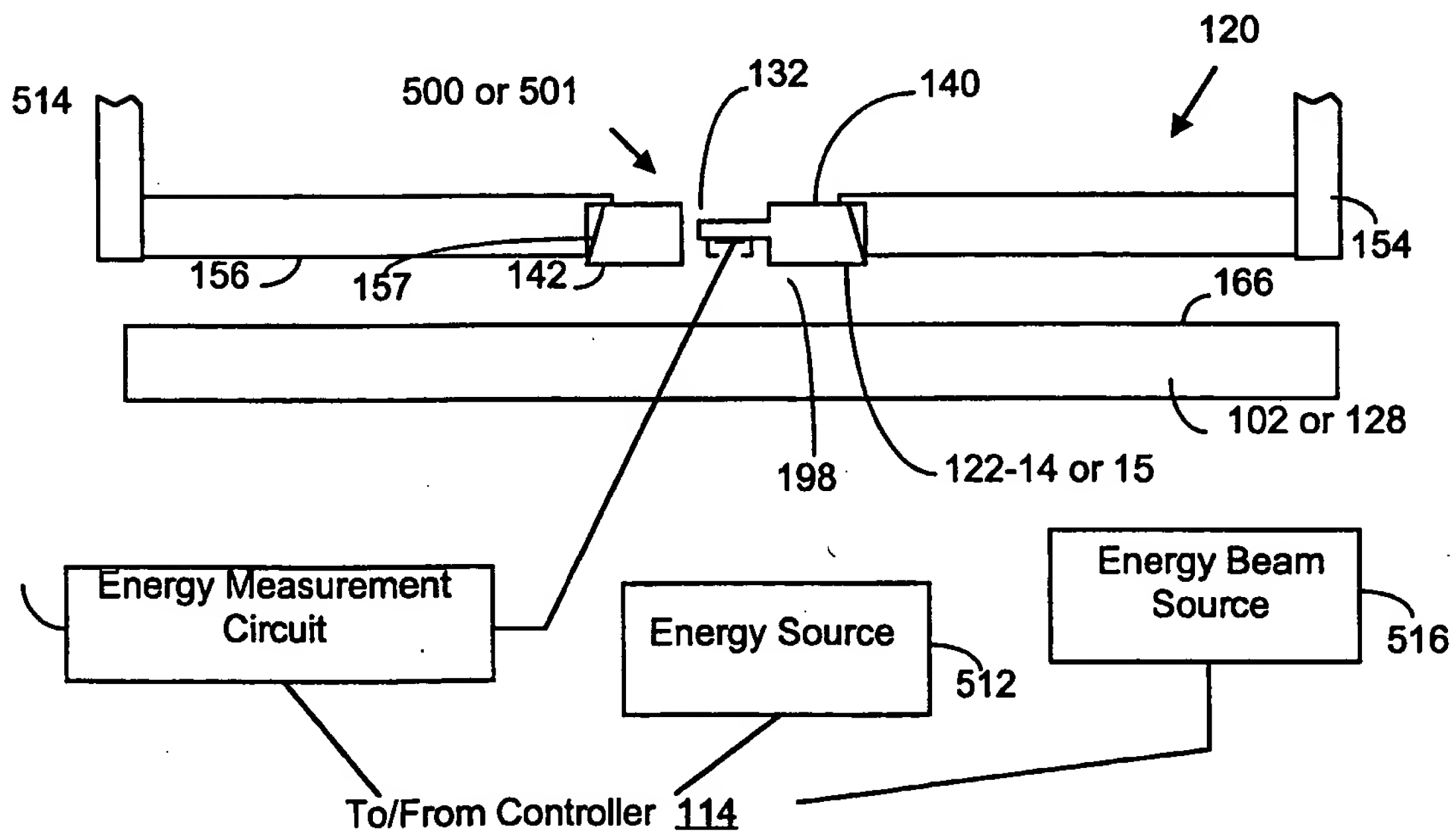


Figure 60

38/58

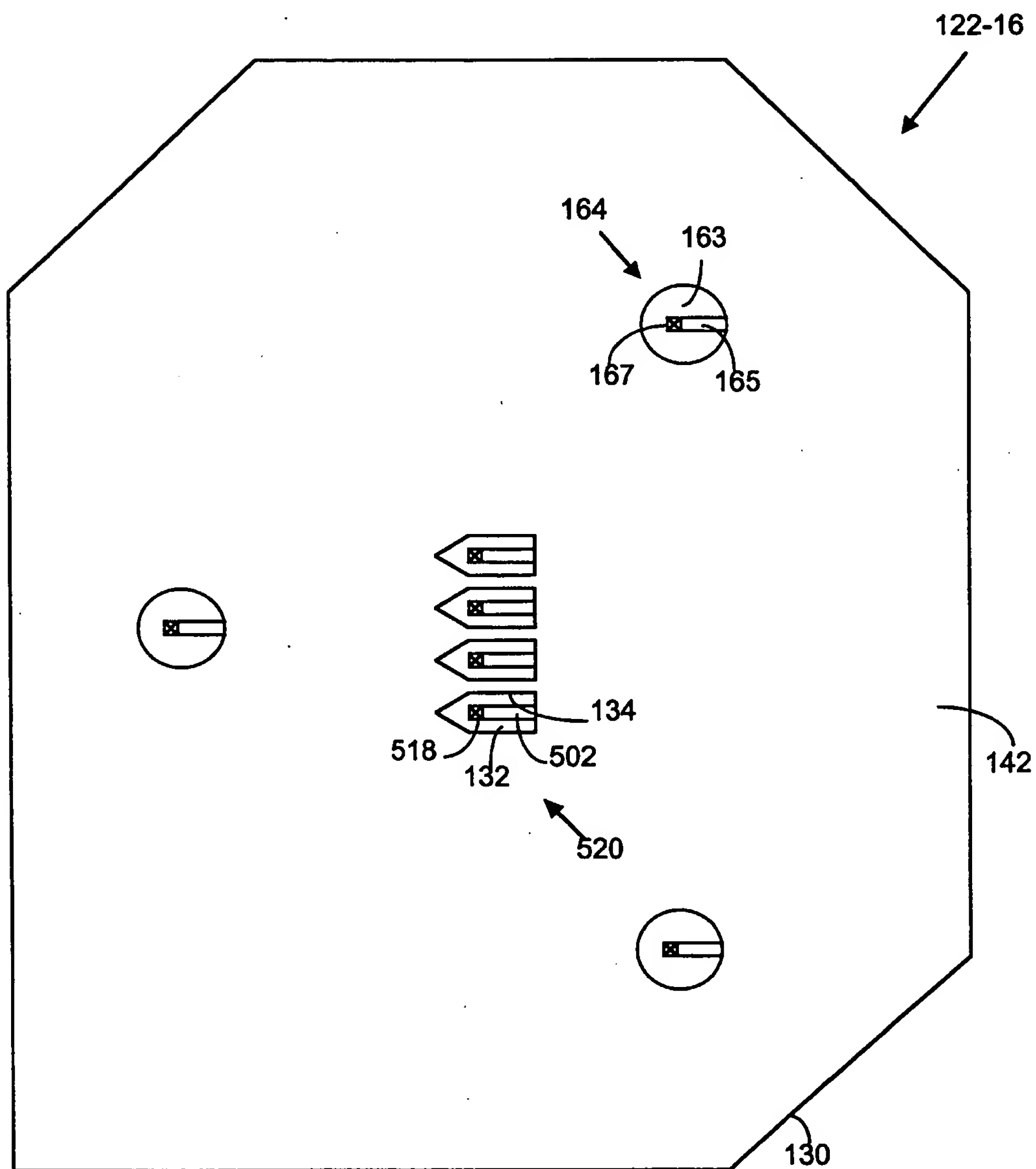


Figure 61

39/58

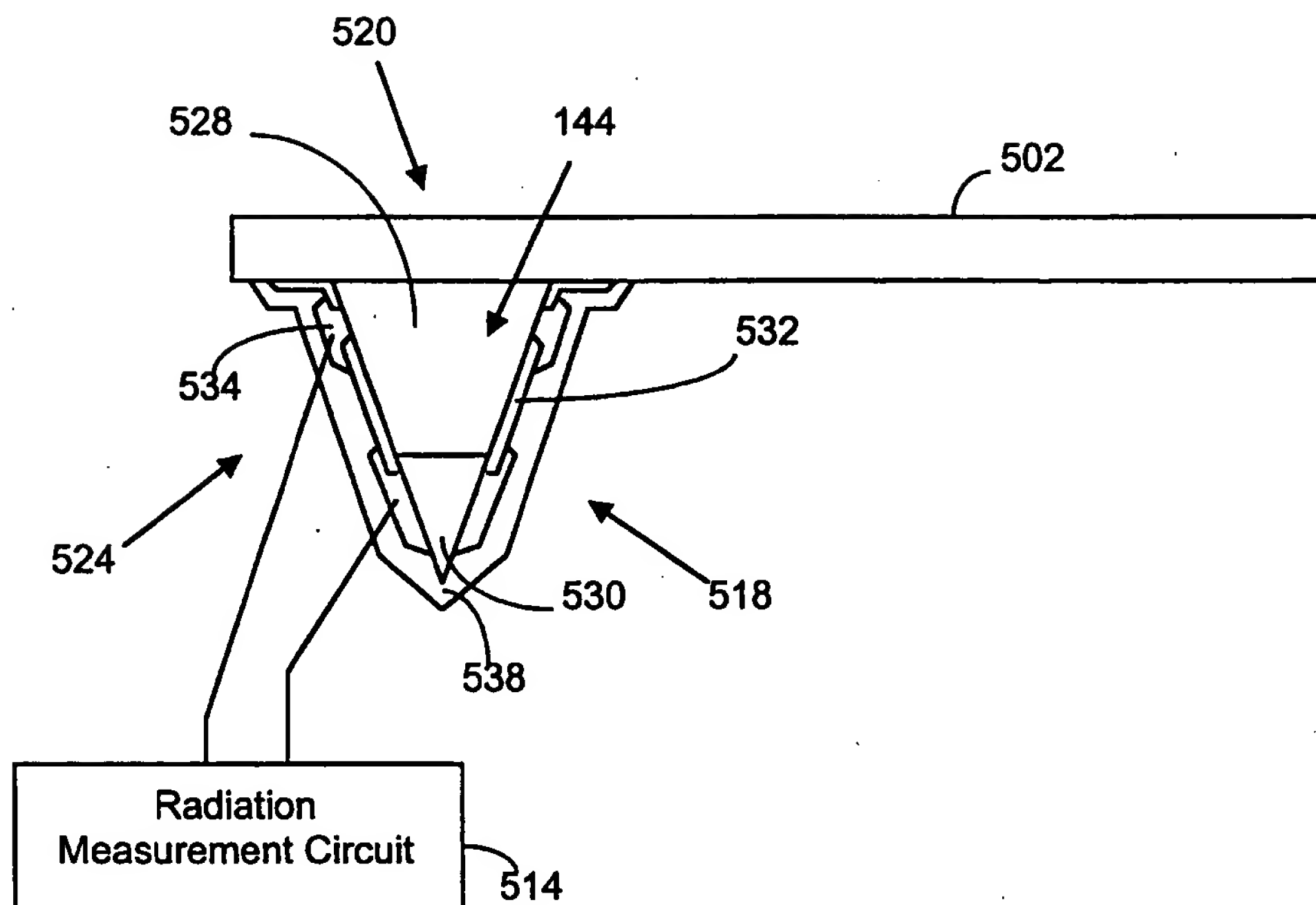


Figure 62

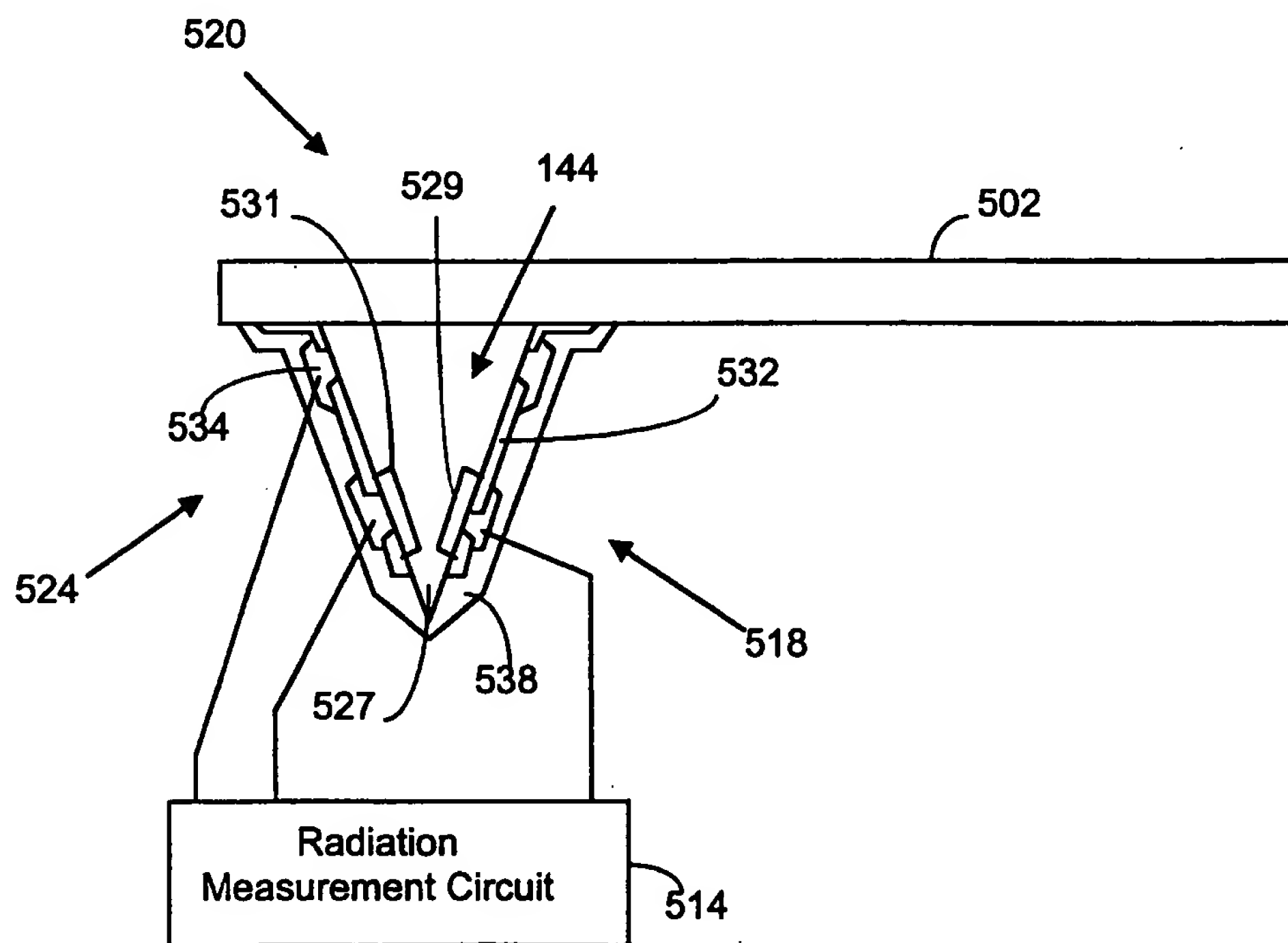


Figure 63

40/58

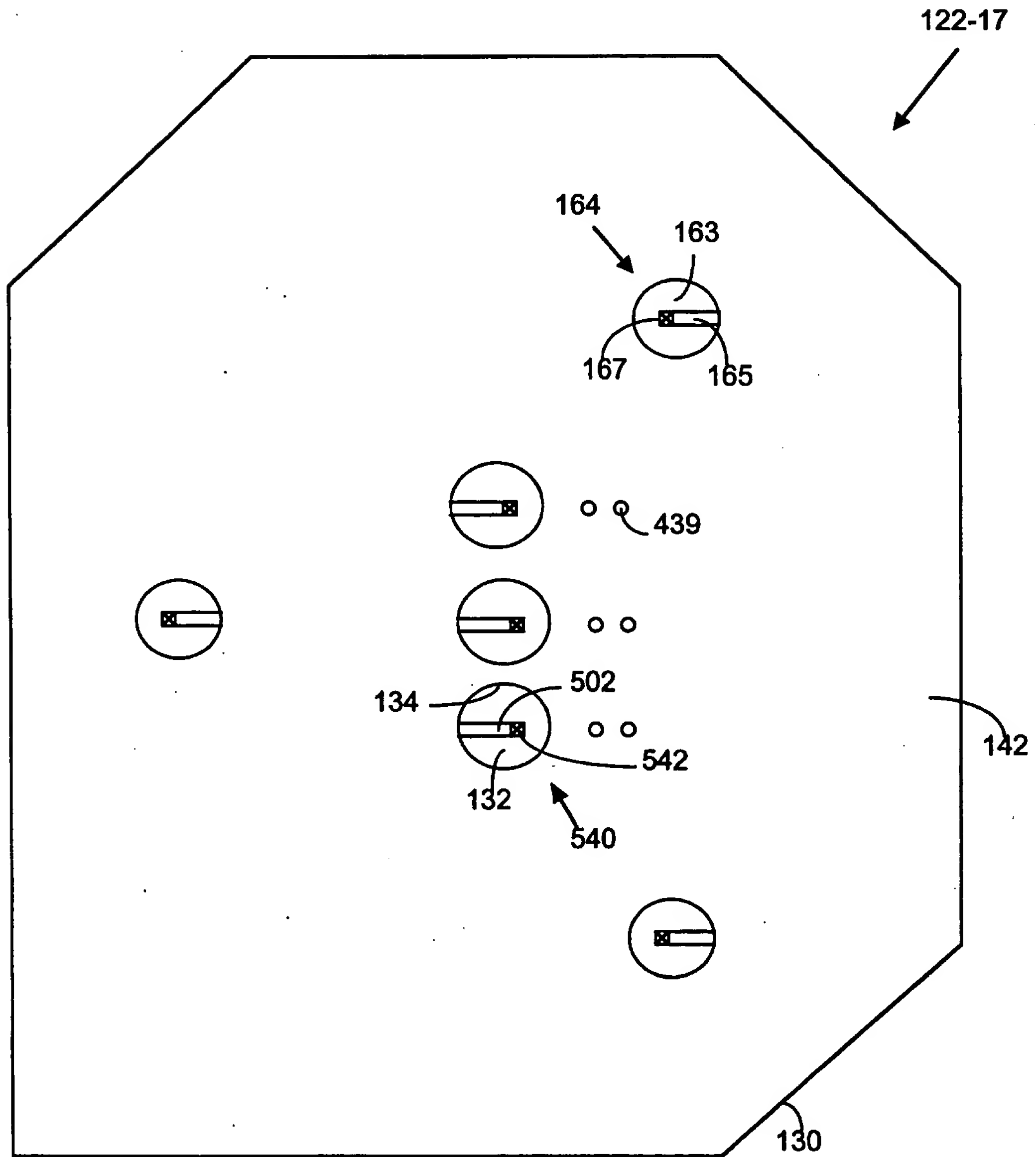


Figure 64

41/58

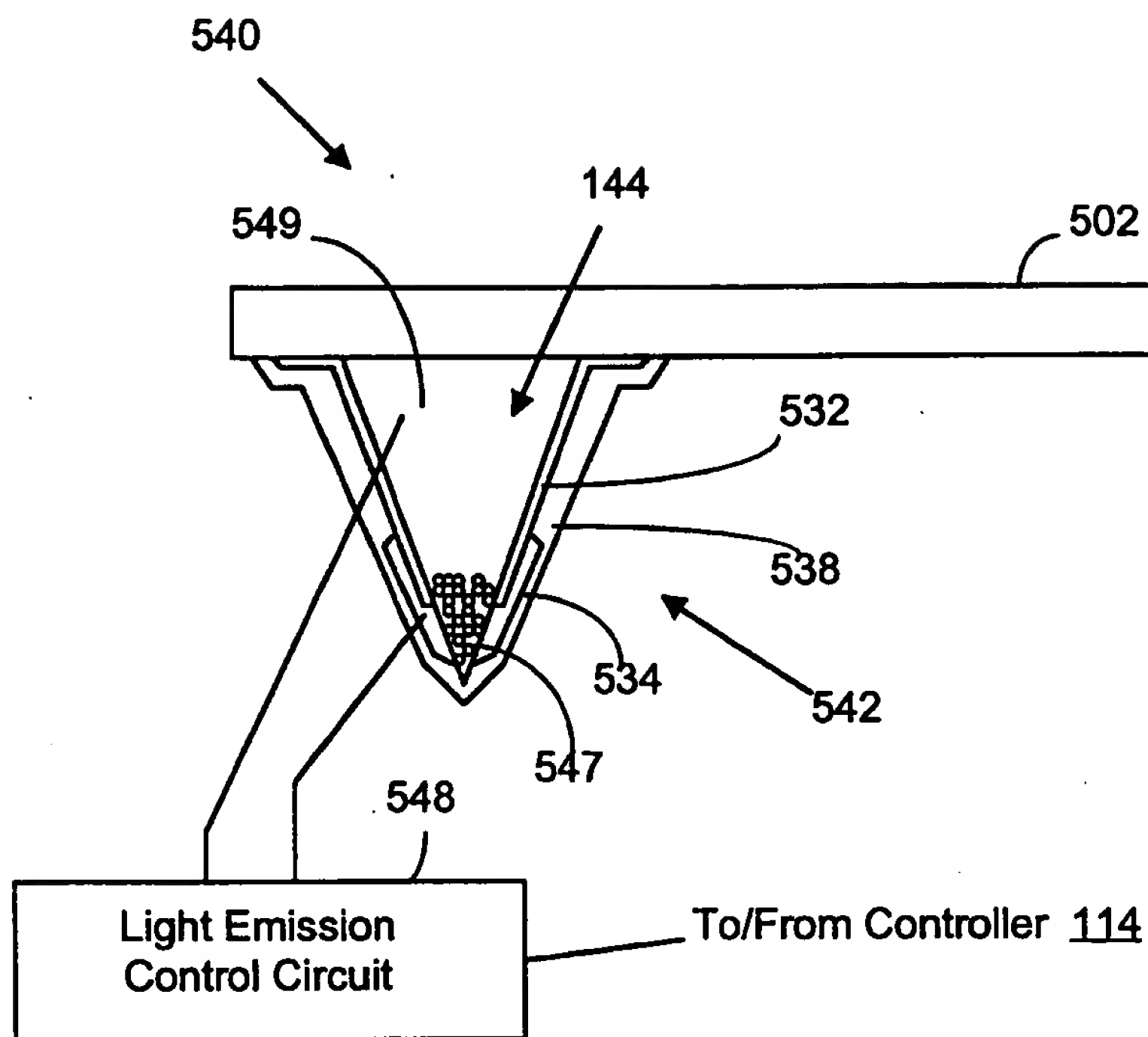


Figure 66

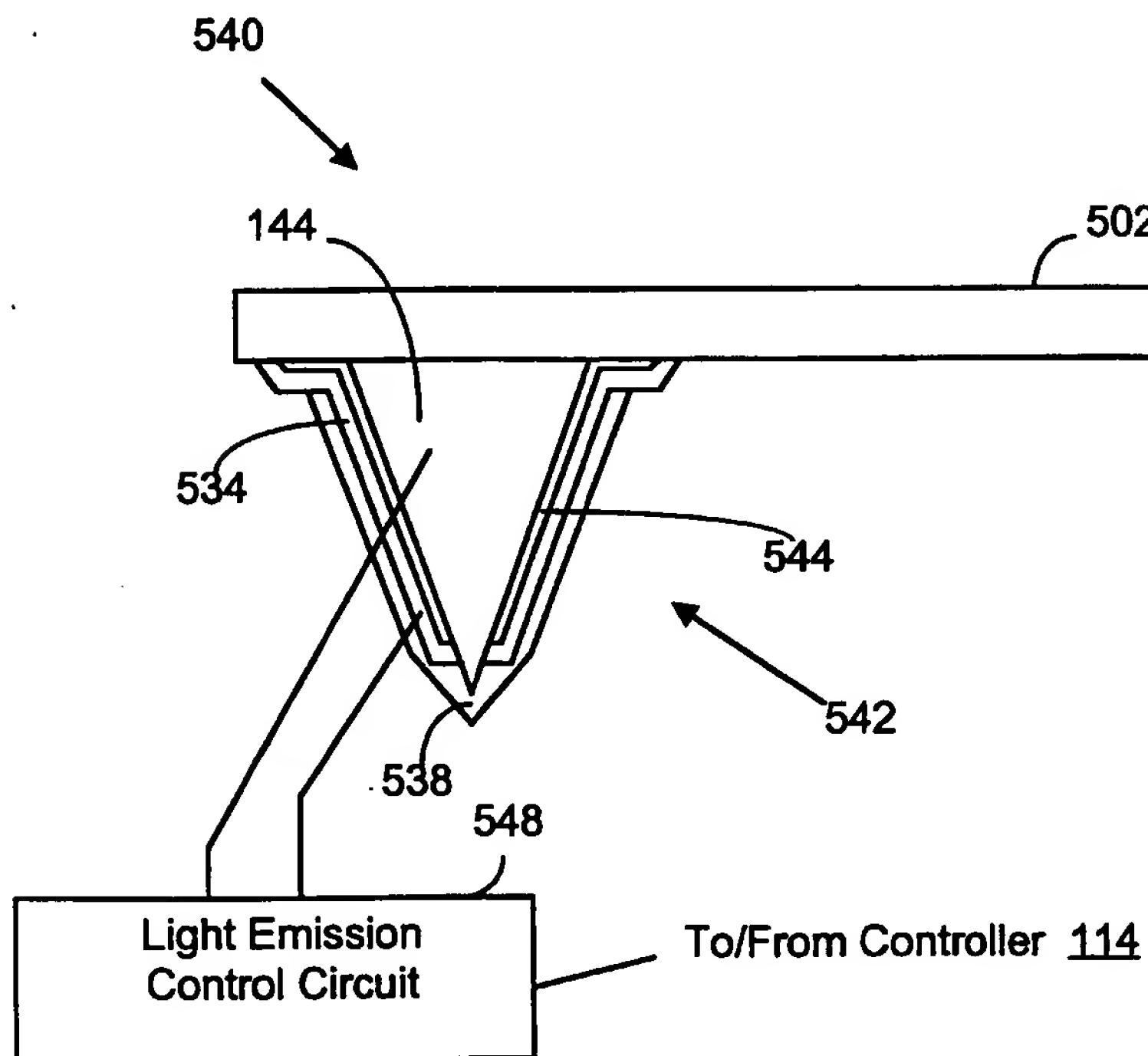


Figure 65

42/58

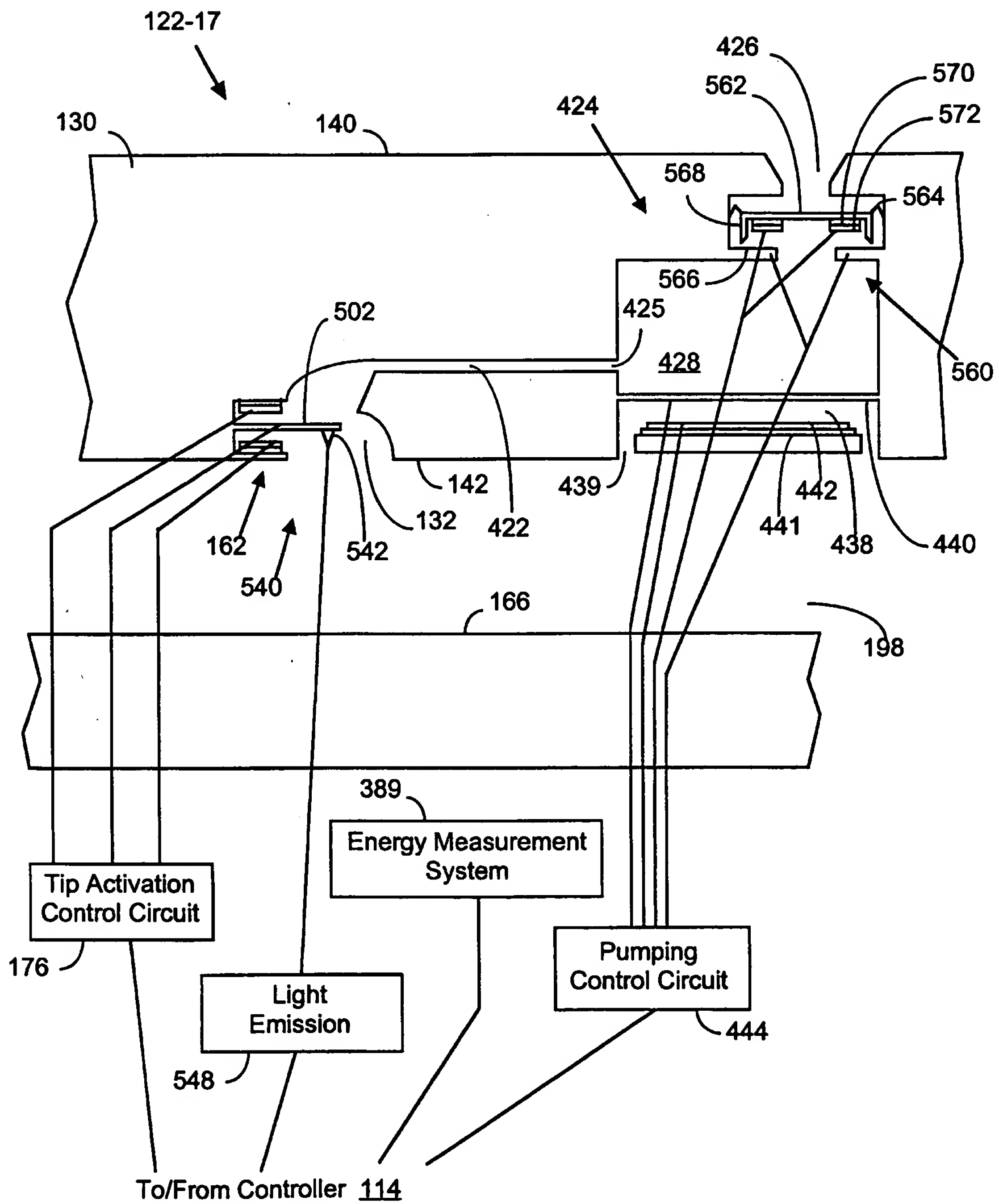


Figure 67

43/58

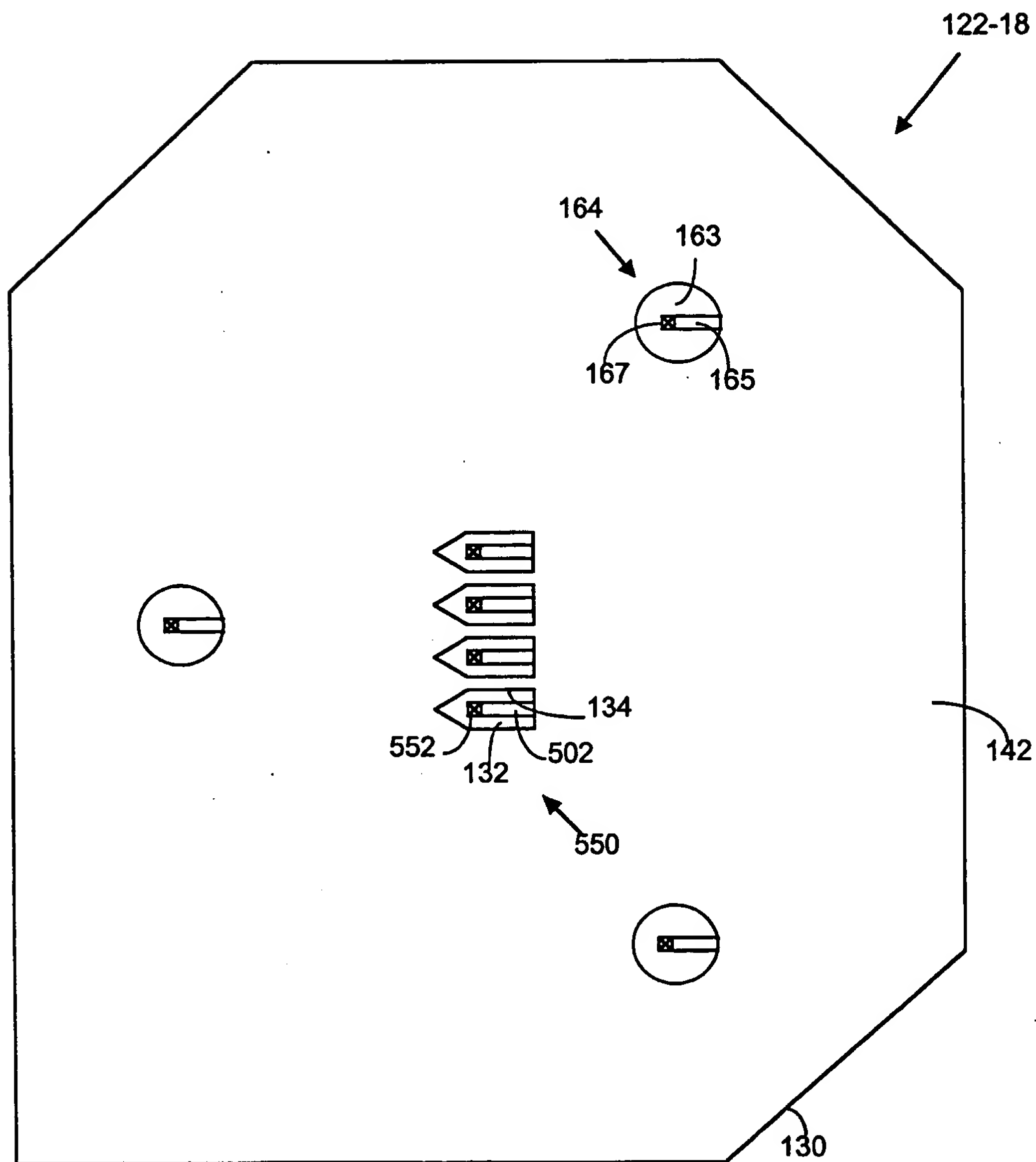


Figure 68

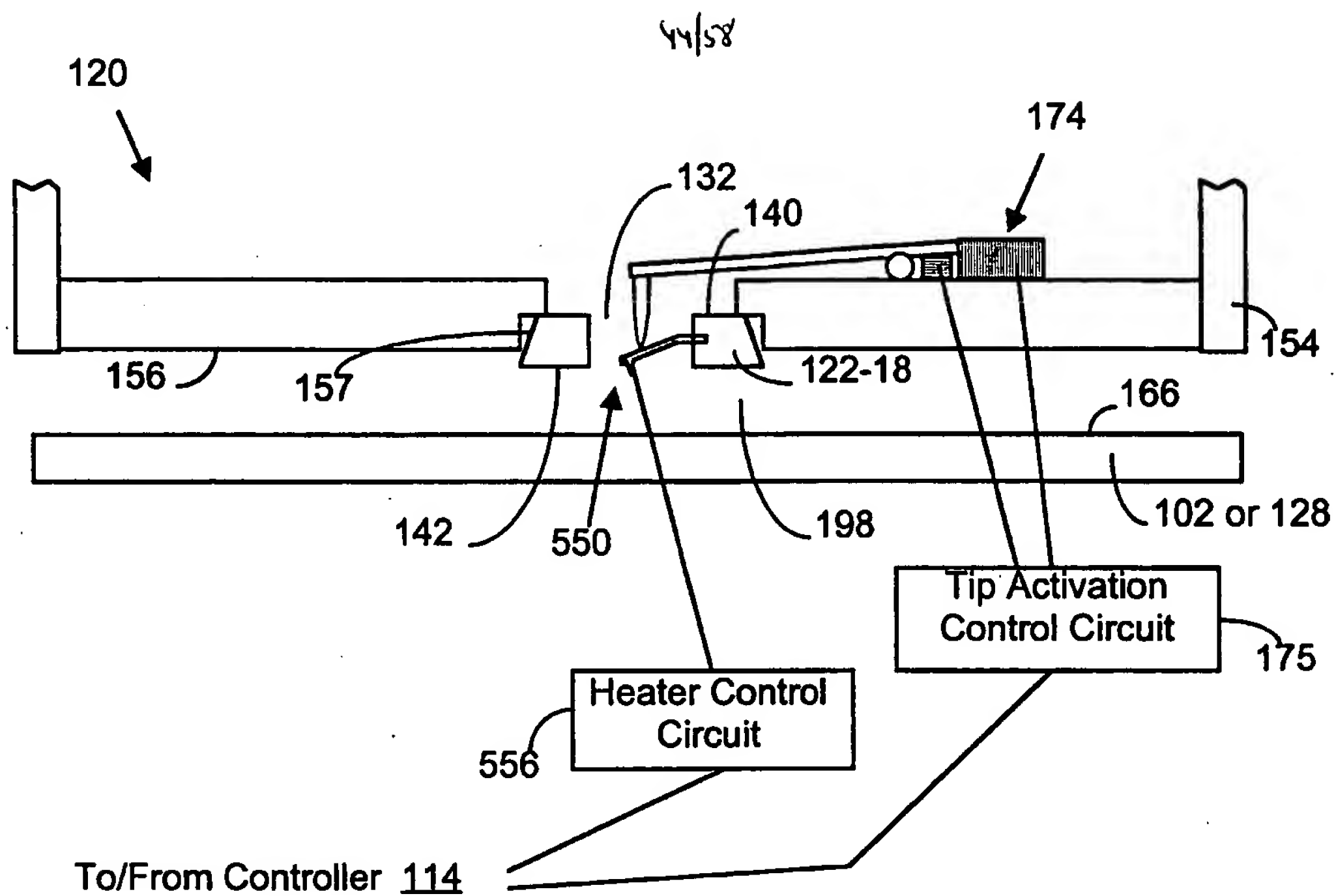


Figure 70

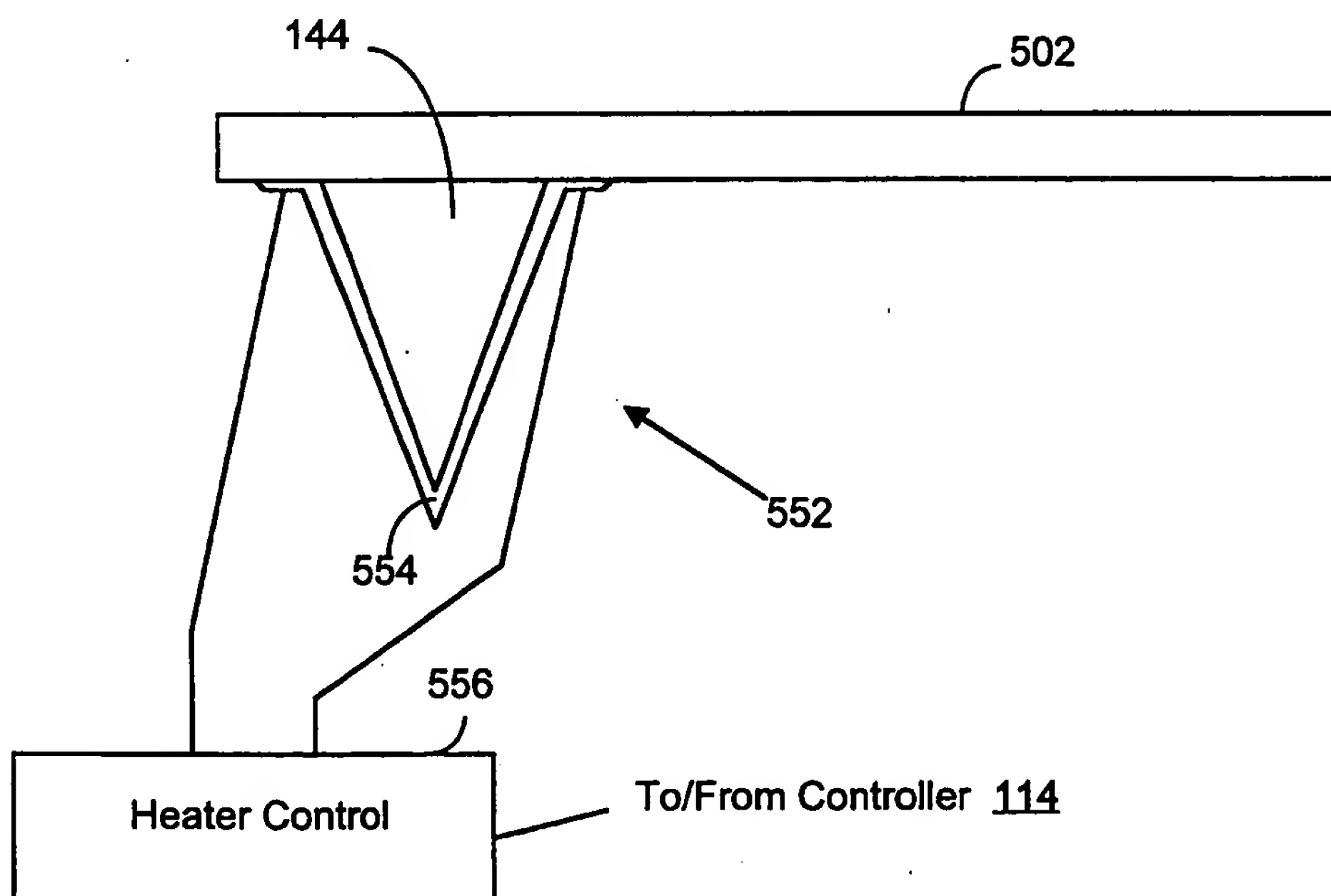


Figure 69

45/58

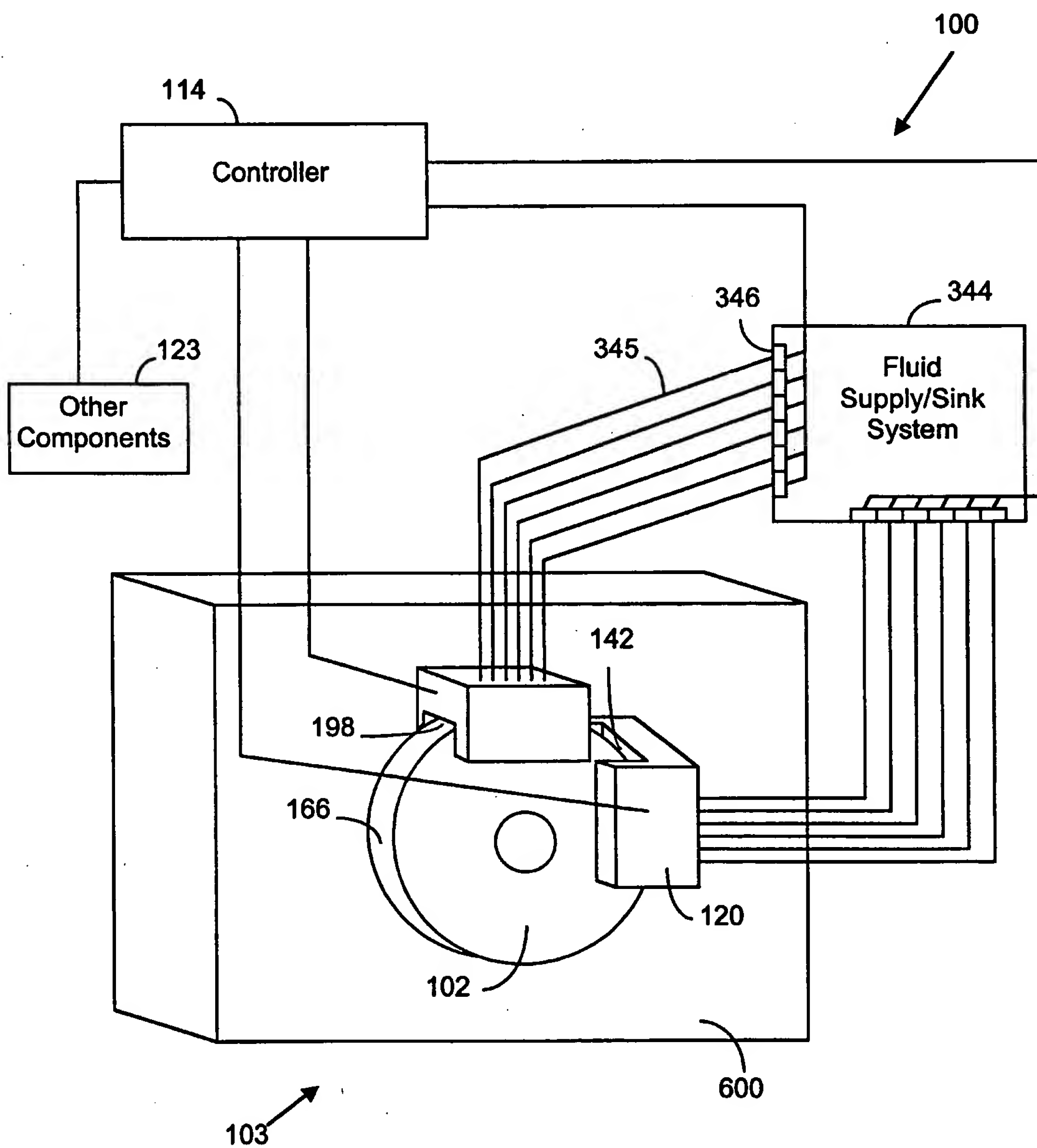


Figure 71

46/58

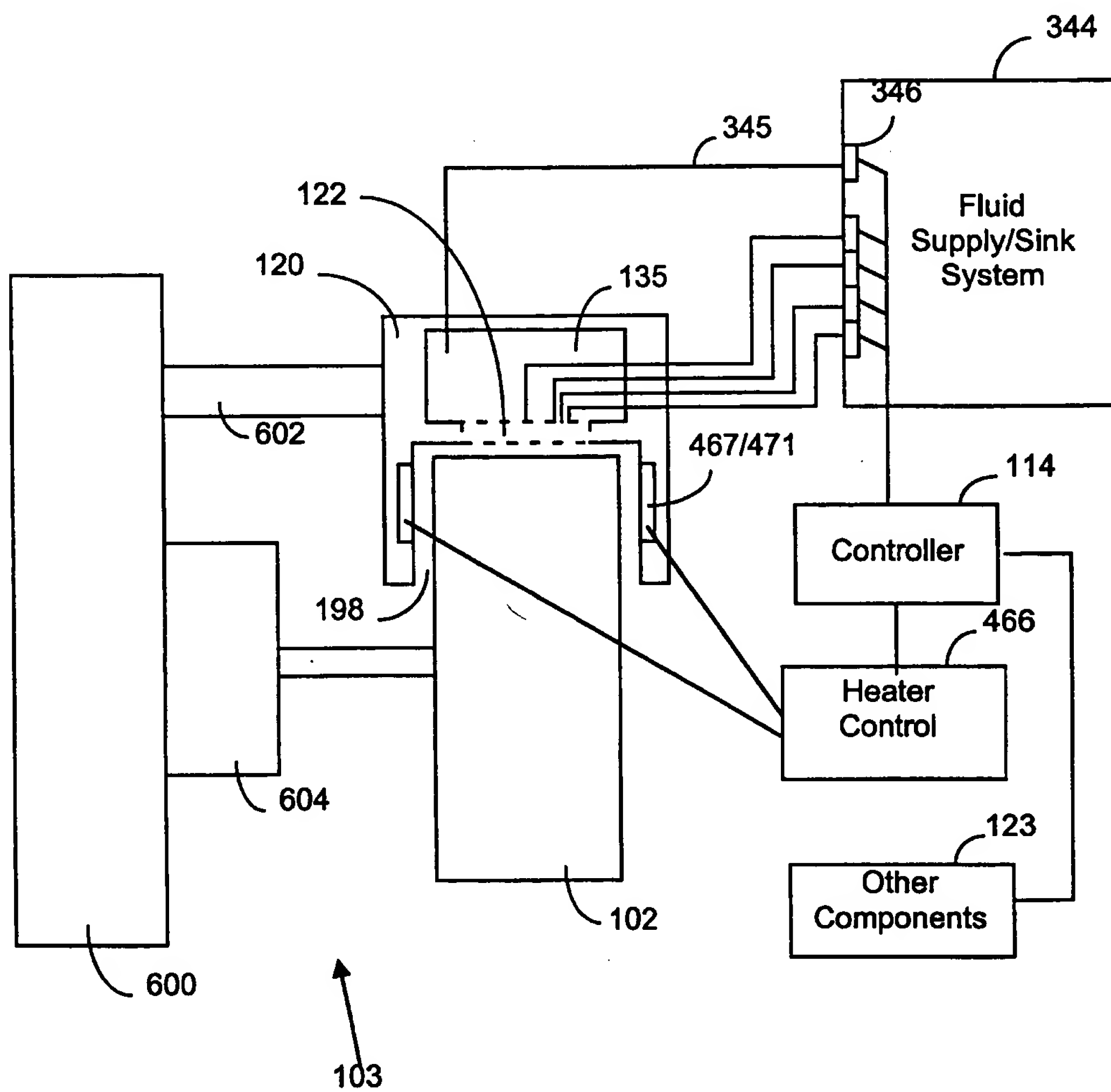


Figure 72

47/58

FIG. 73

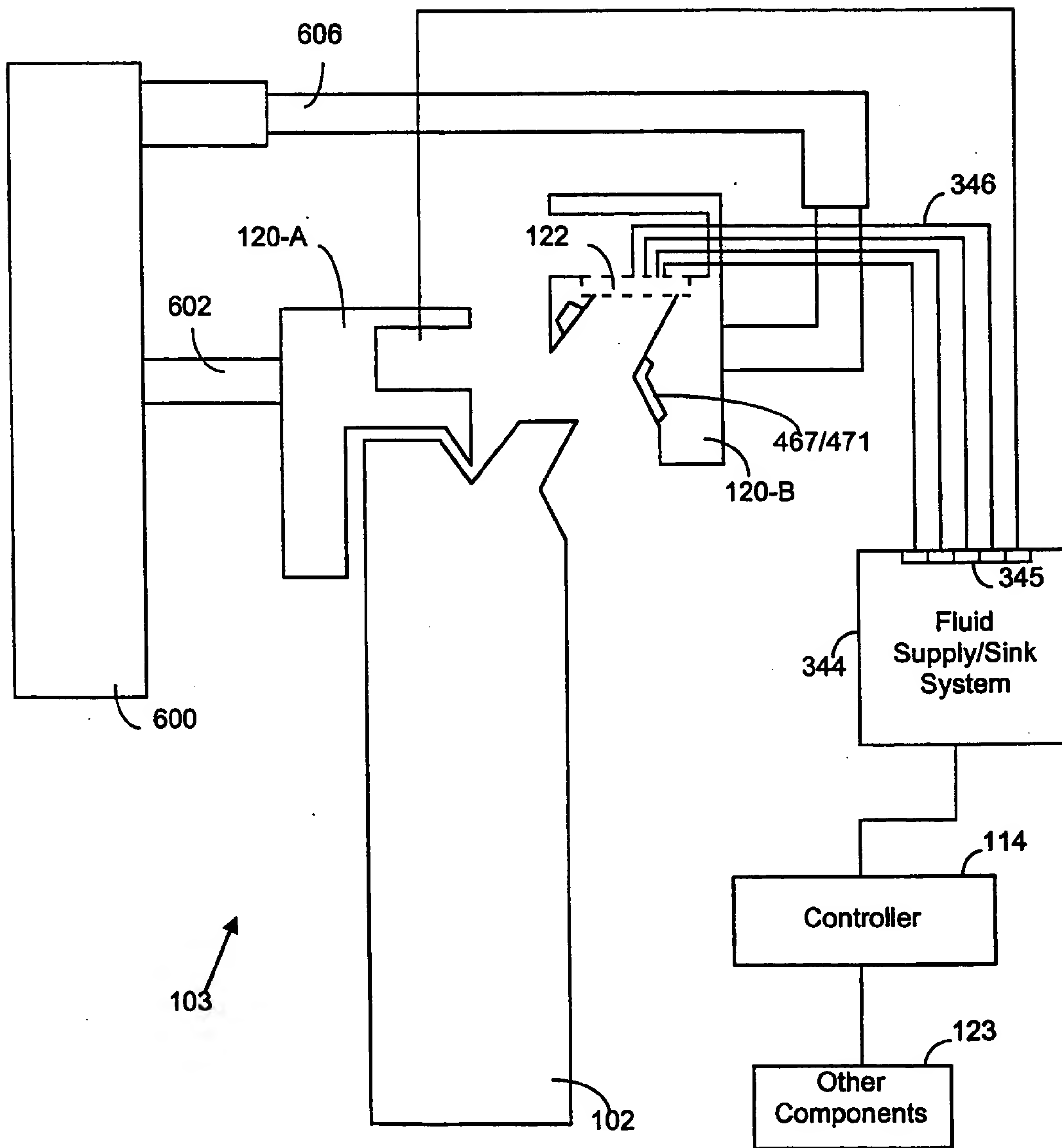


Figure 73

4858

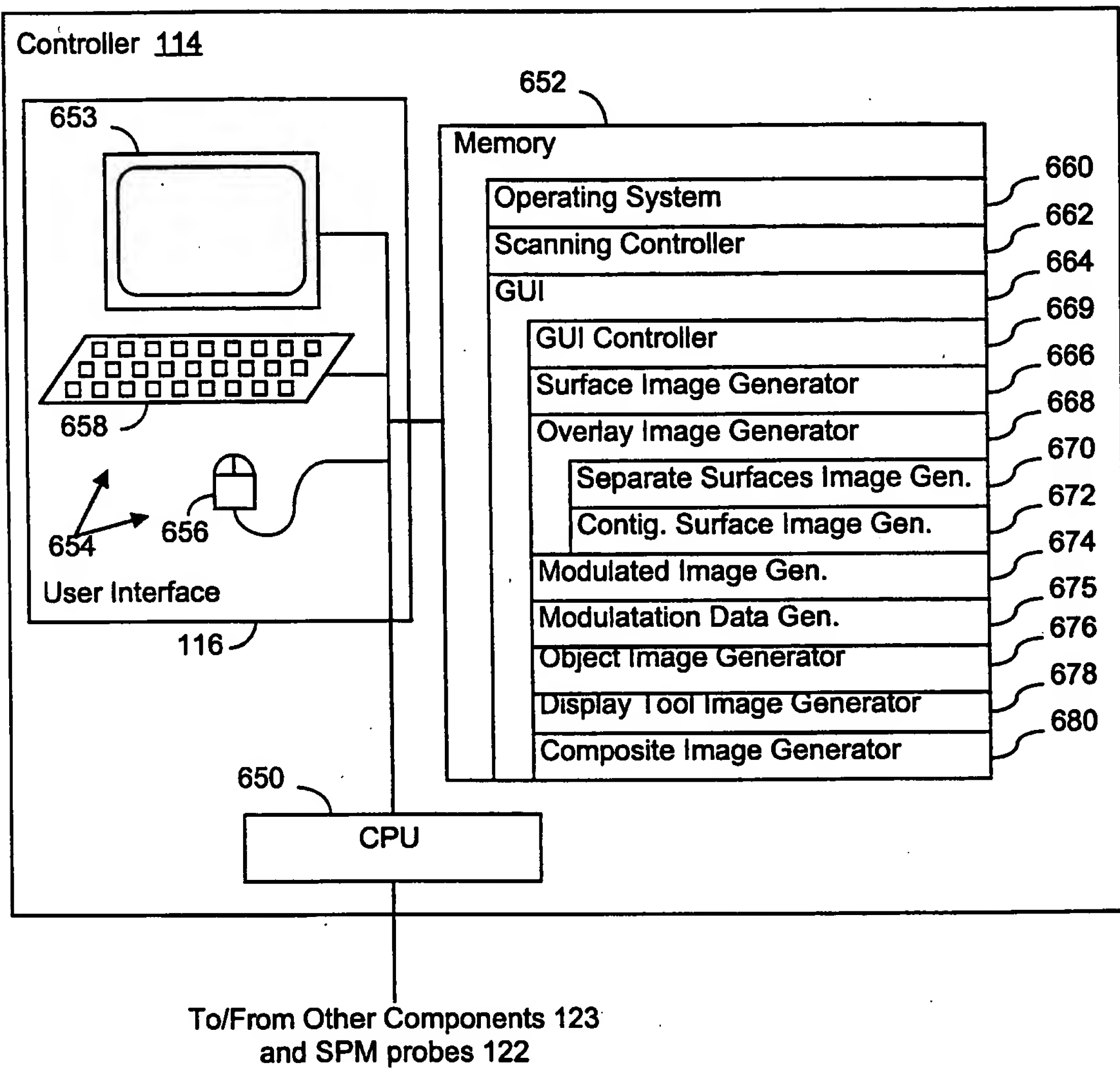
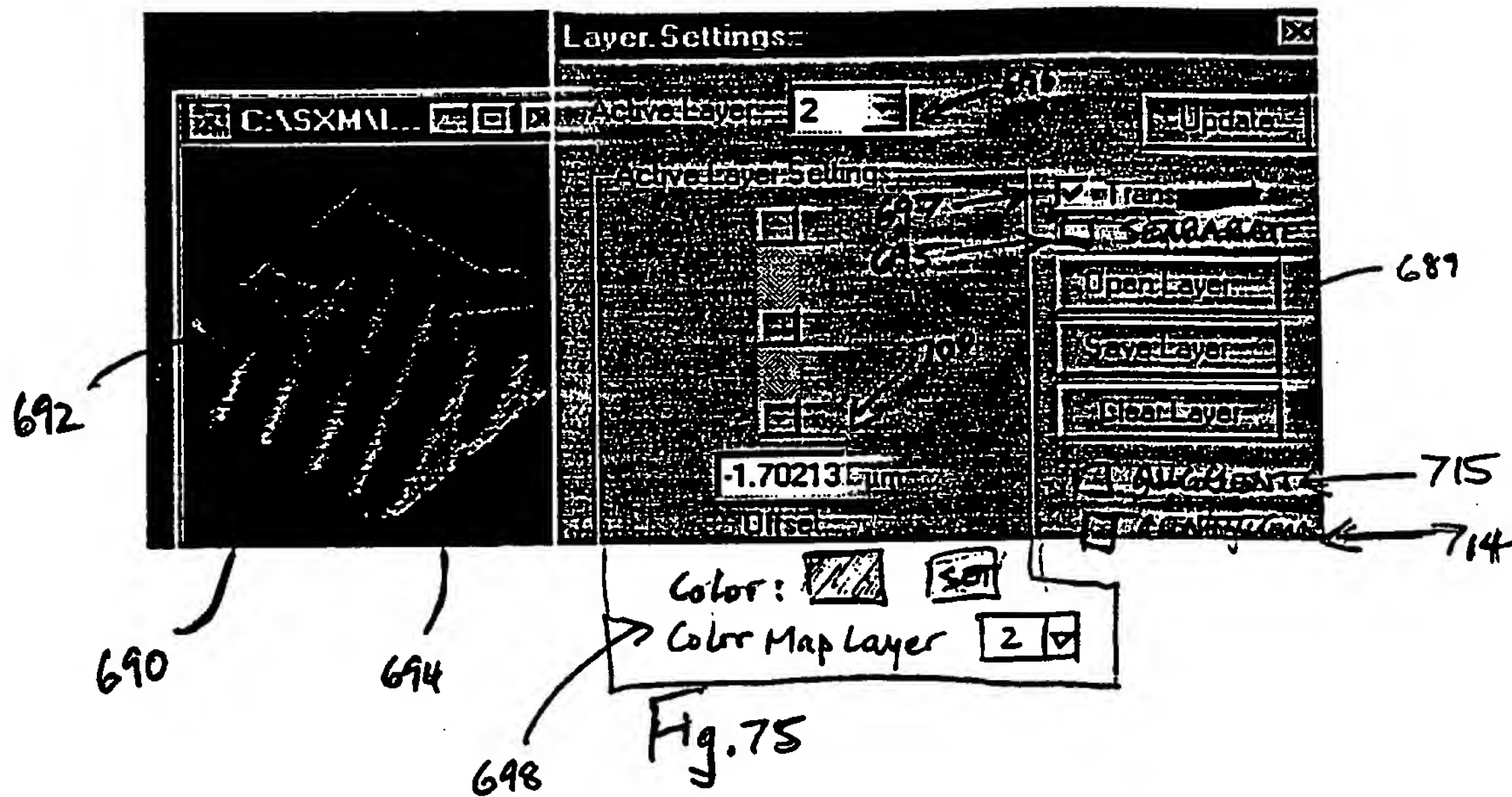


Figure 74

49/58



50/58

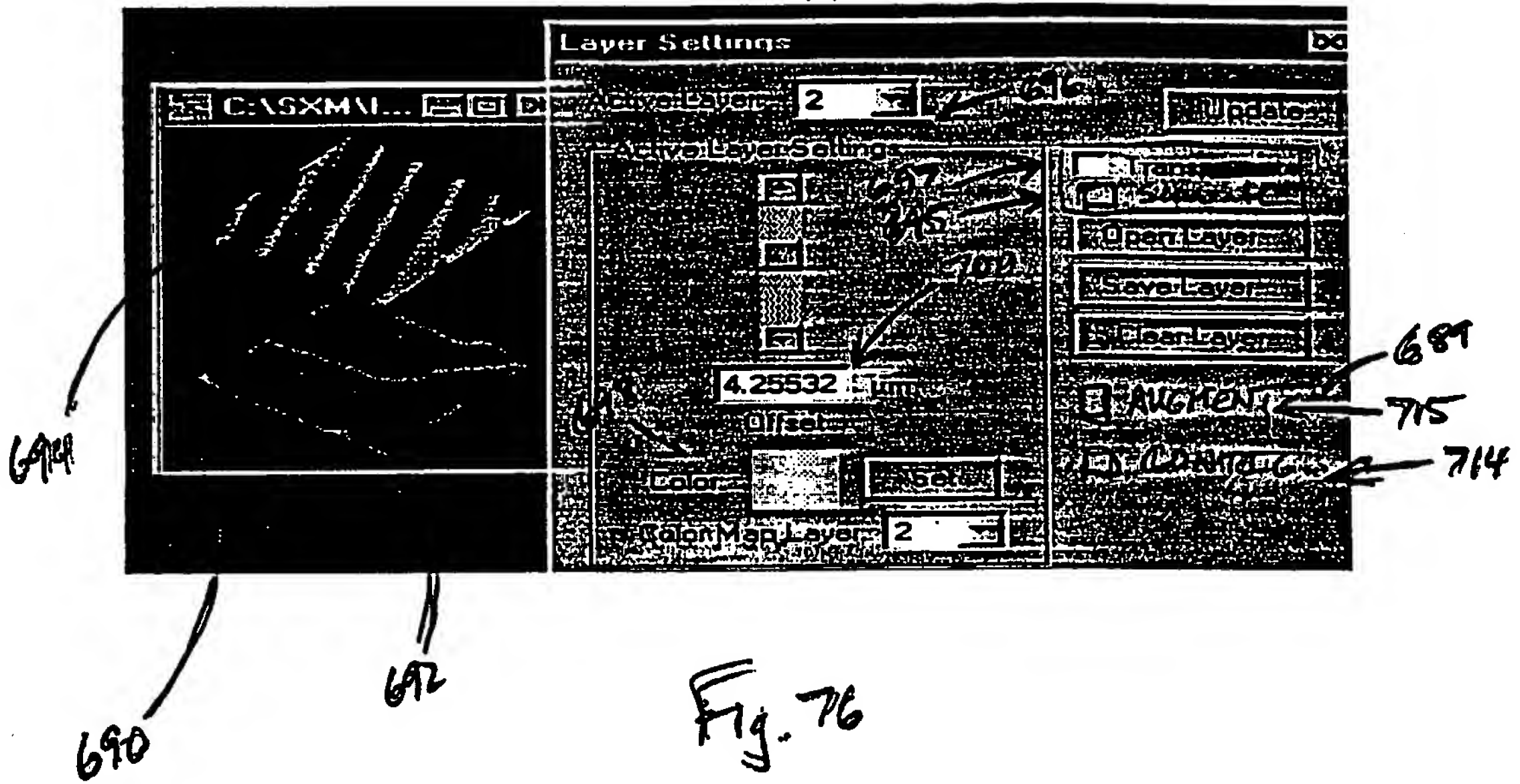


Fig. 76

Fig. 76

51/58

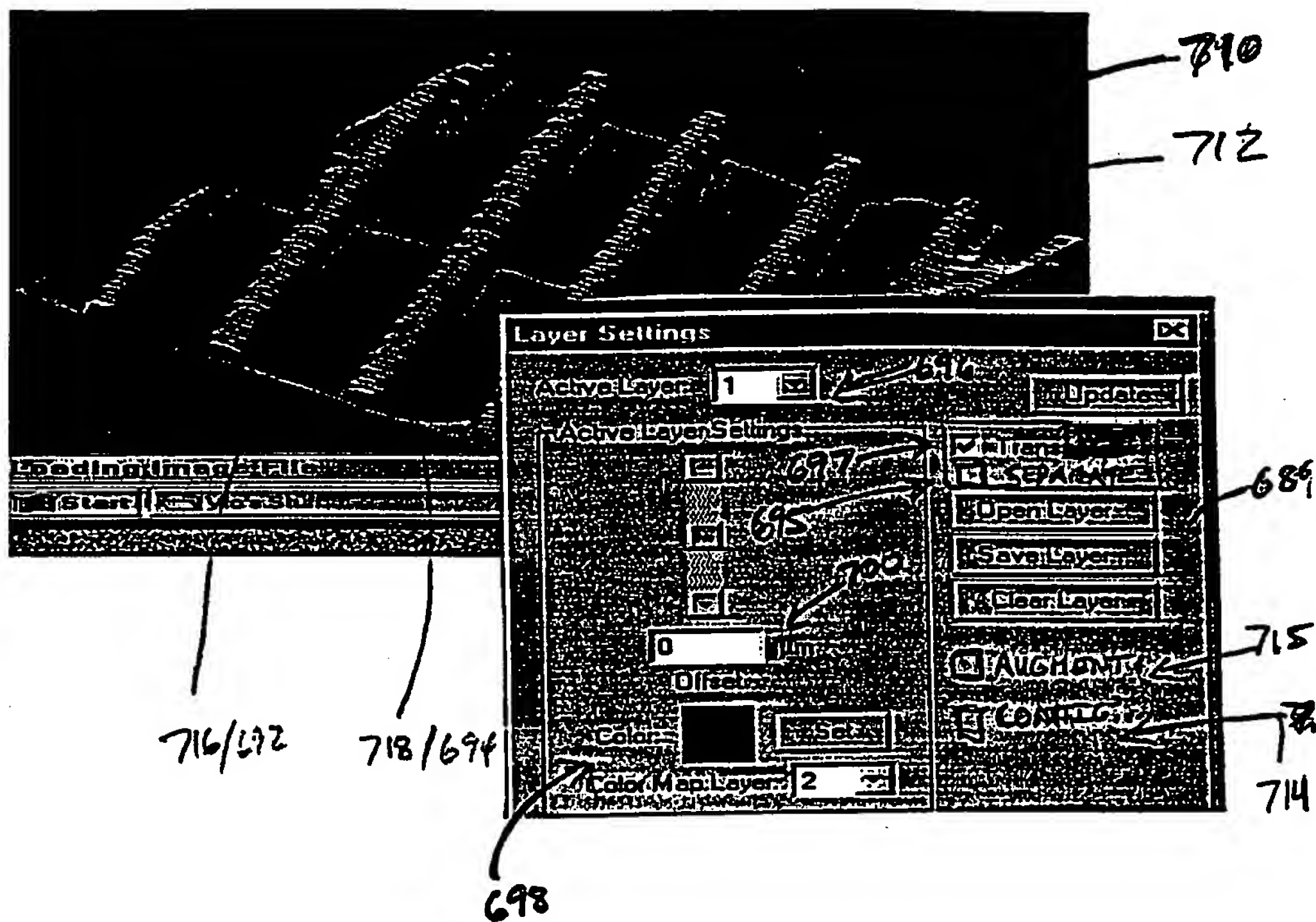


Fig. 77

52/58

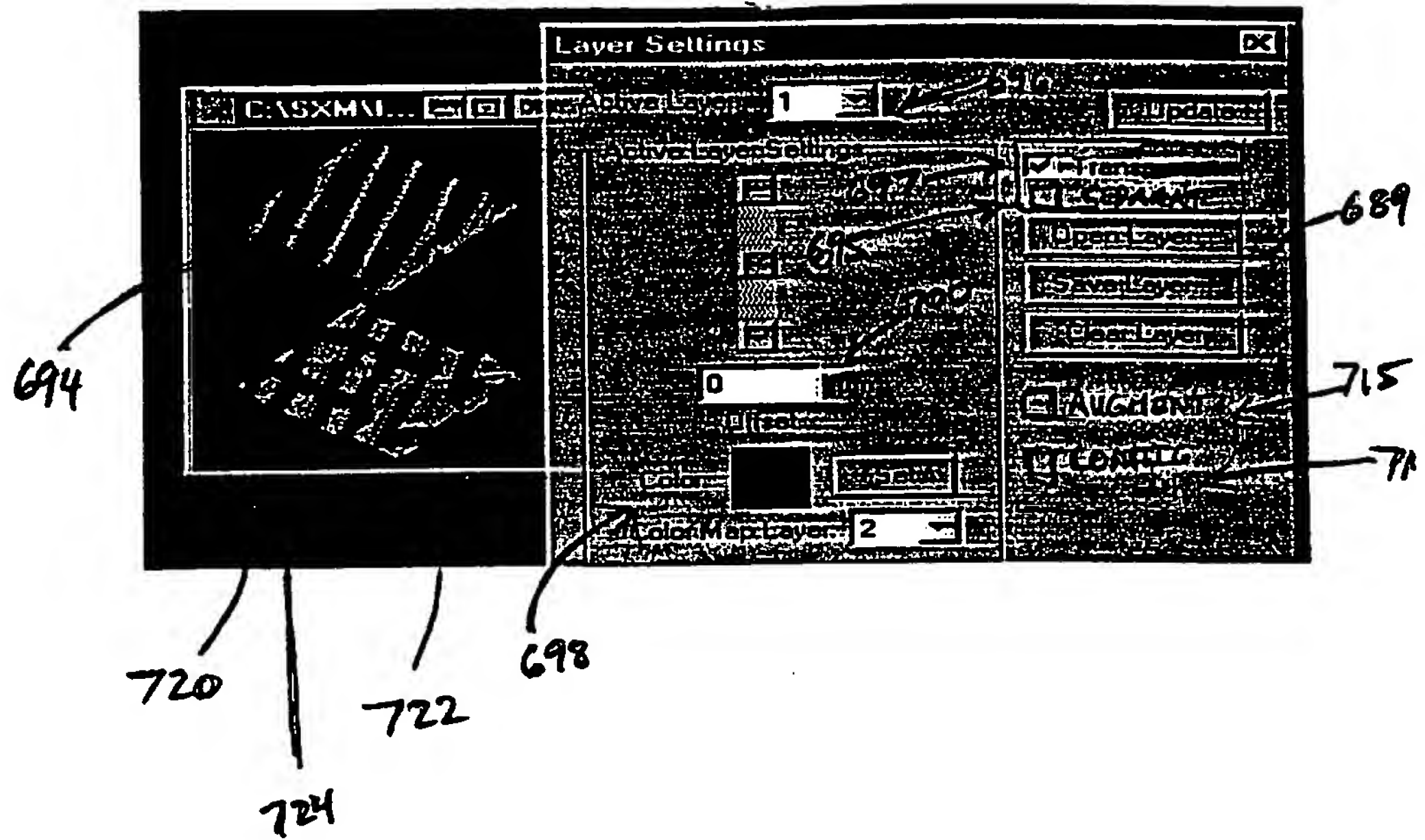


Fig. 78

FIG. 78

53/58

FIG. 79

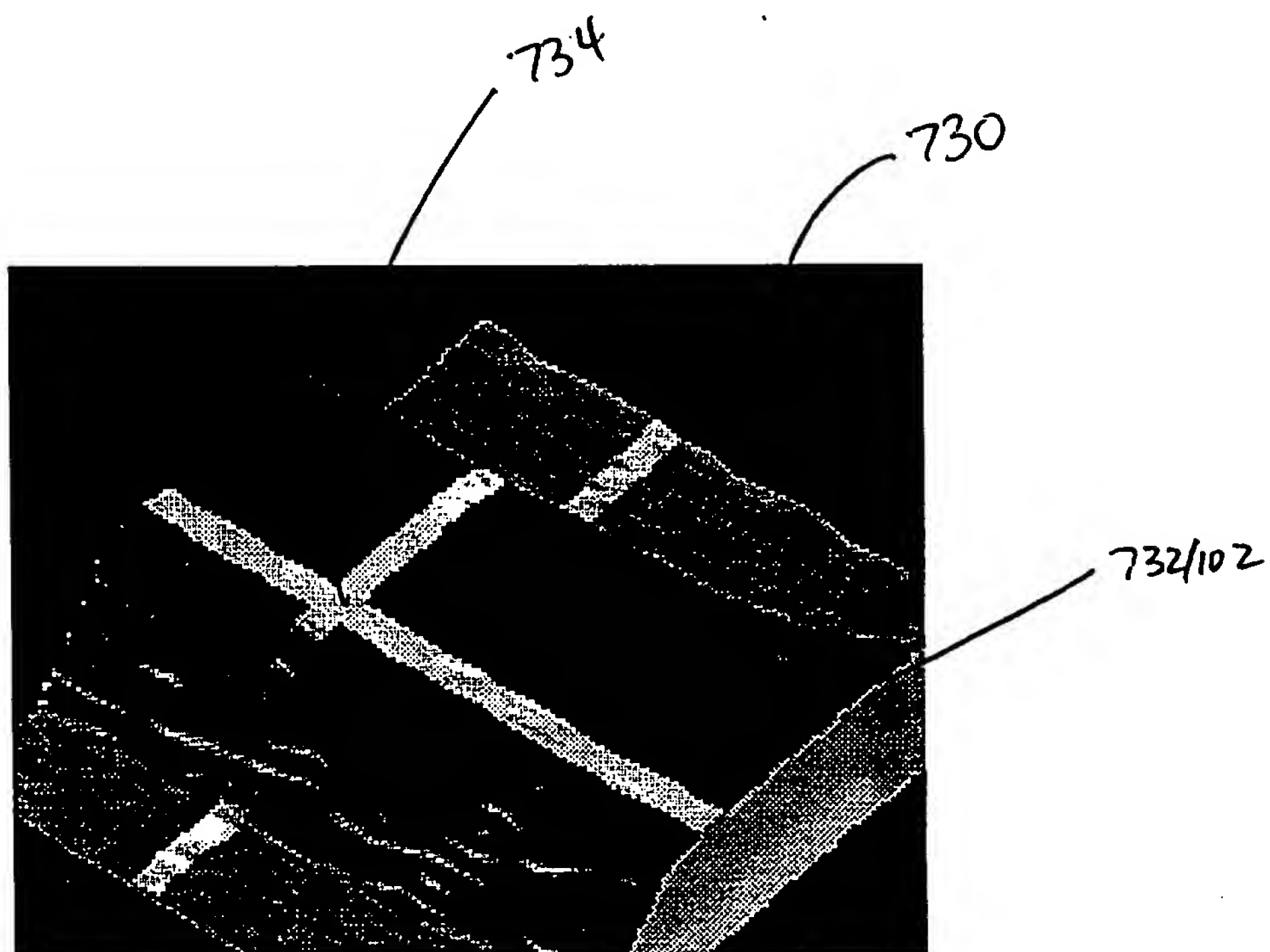


Fig. 79

5-4/58

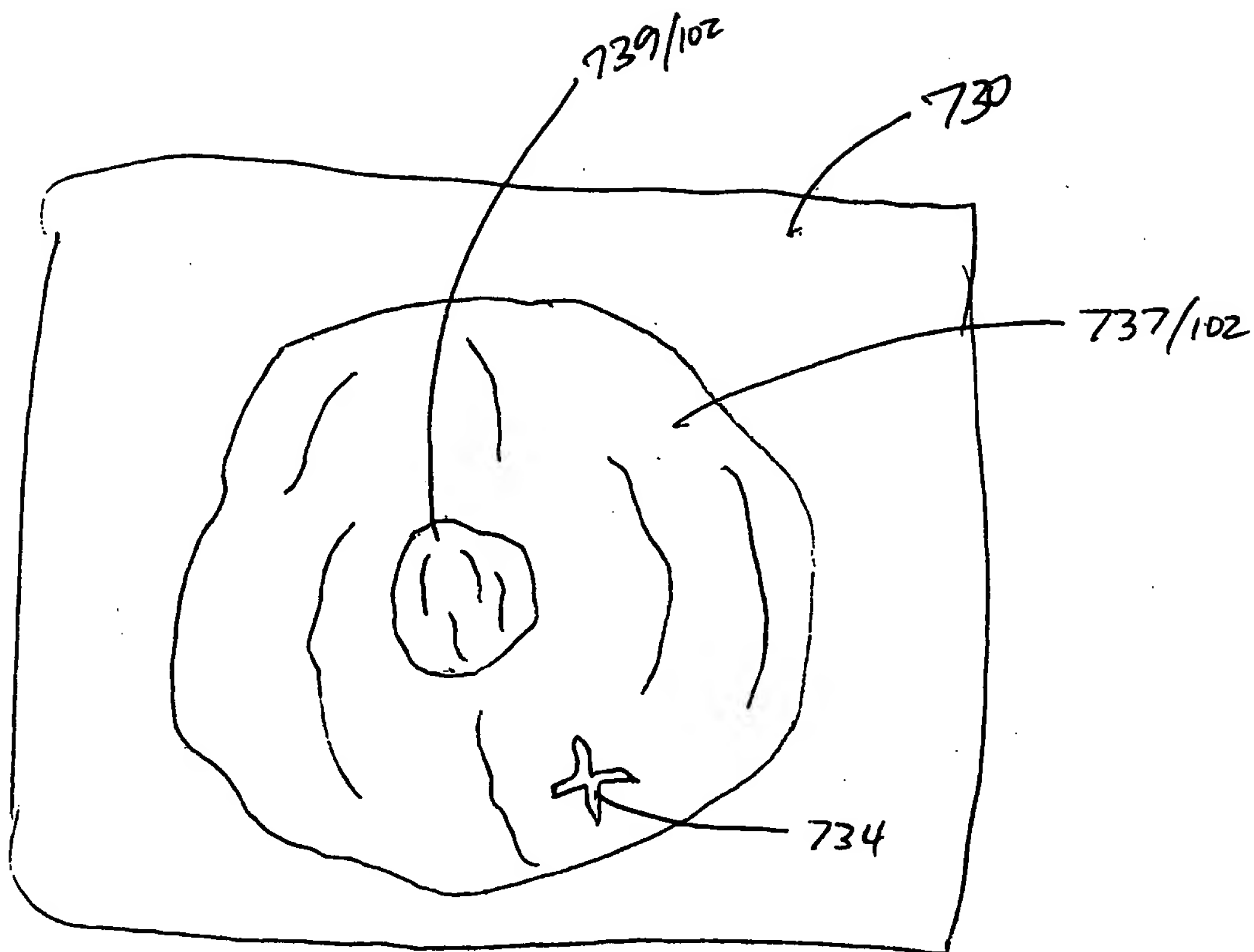


Fig. 80

55158

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732

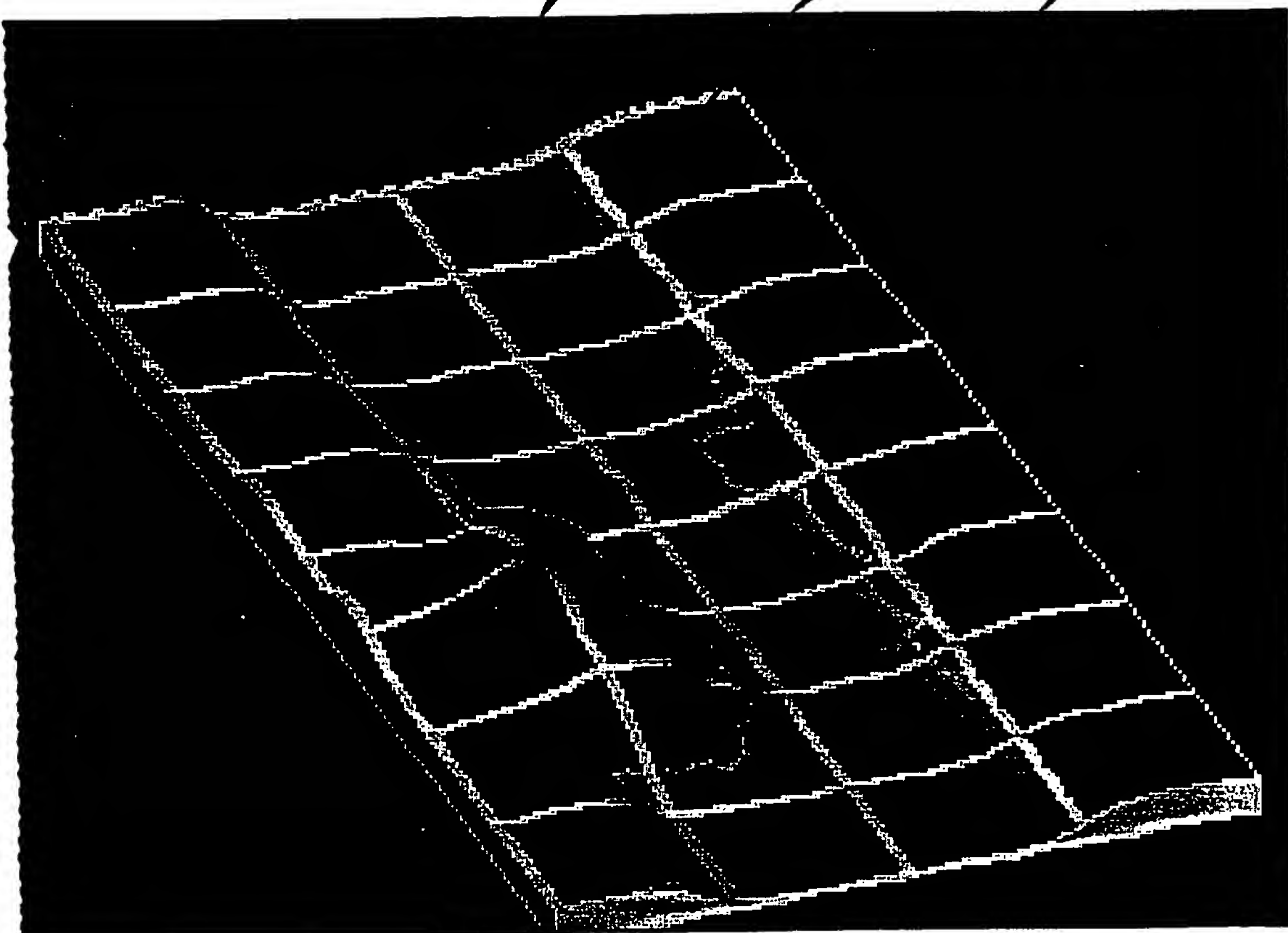


Fig. 81

57/58

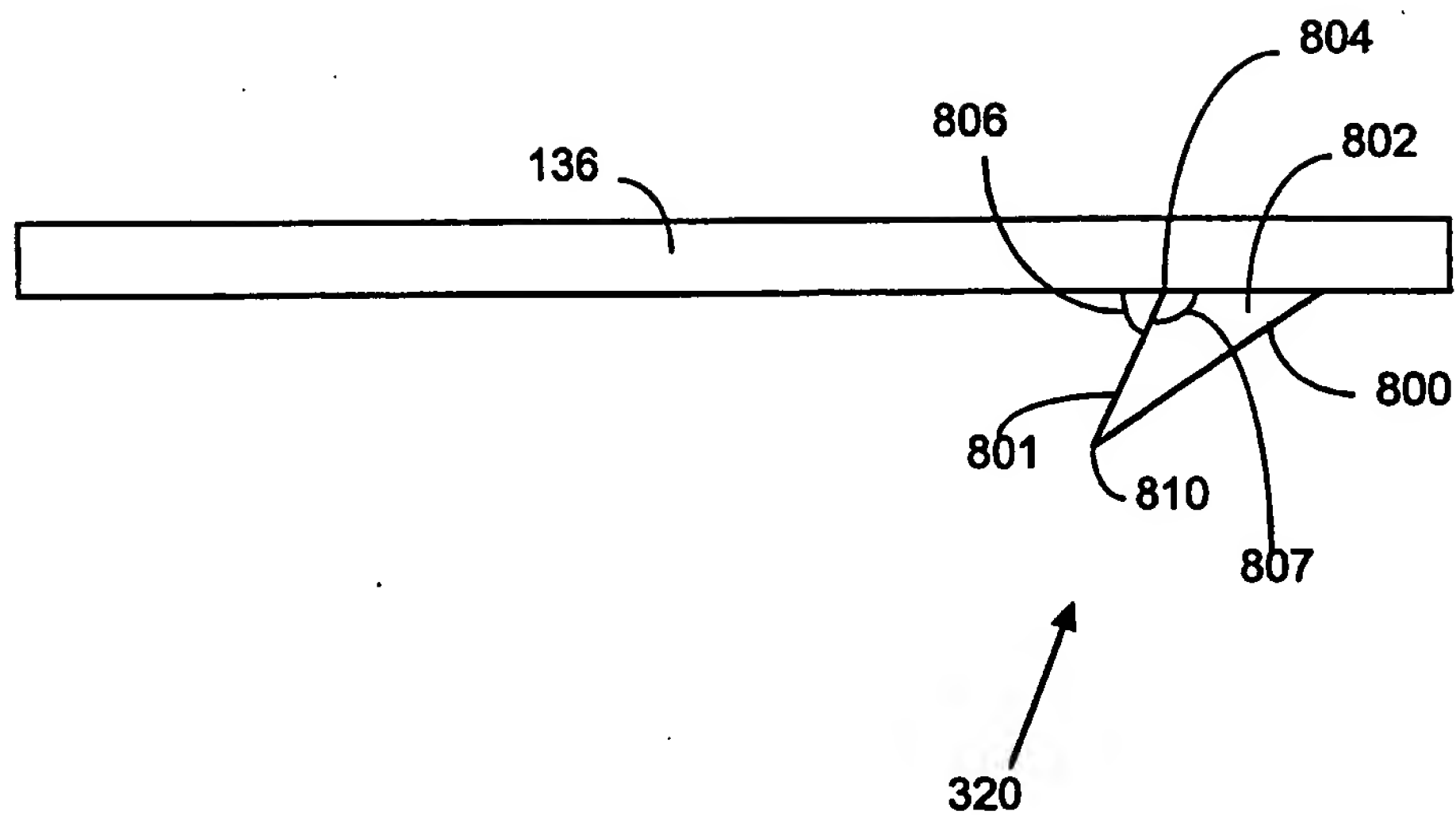


Figure 83

58/58

121

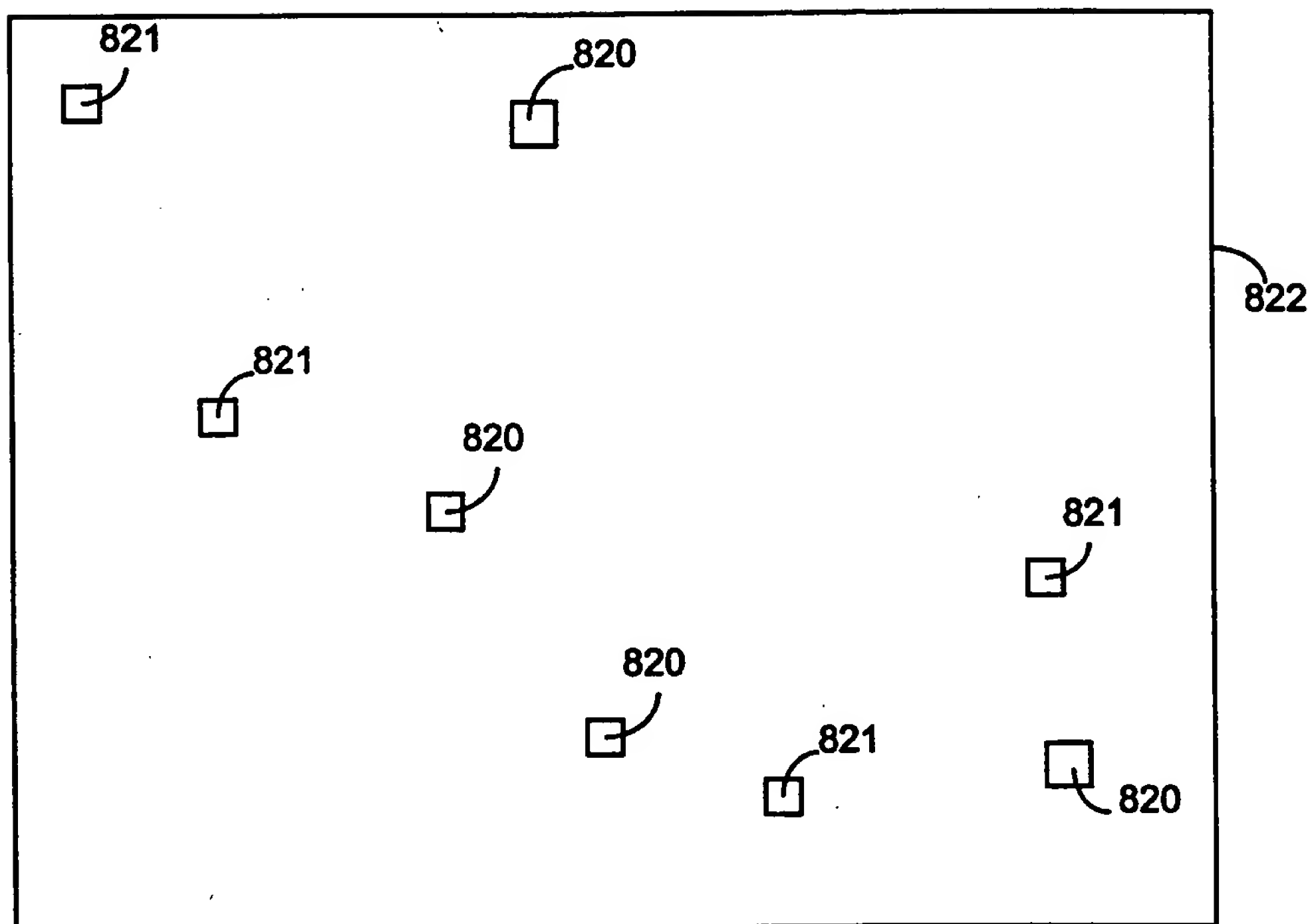


Figure 84

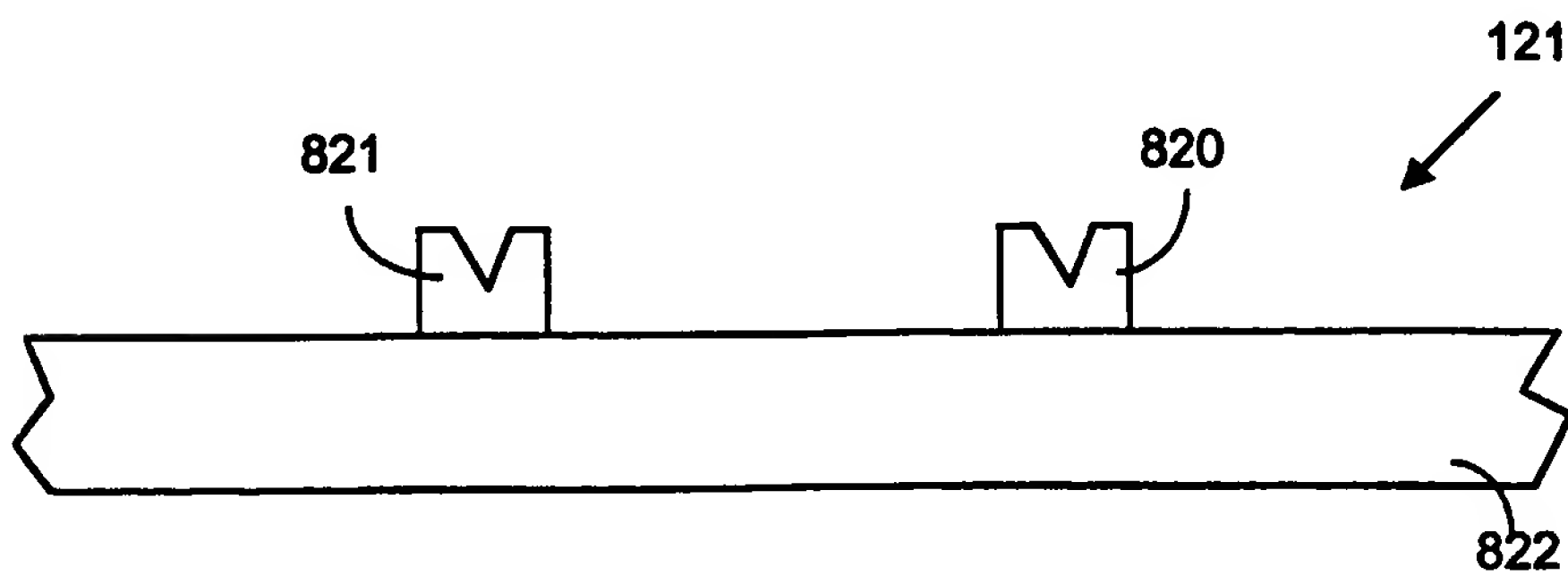


Figure 85

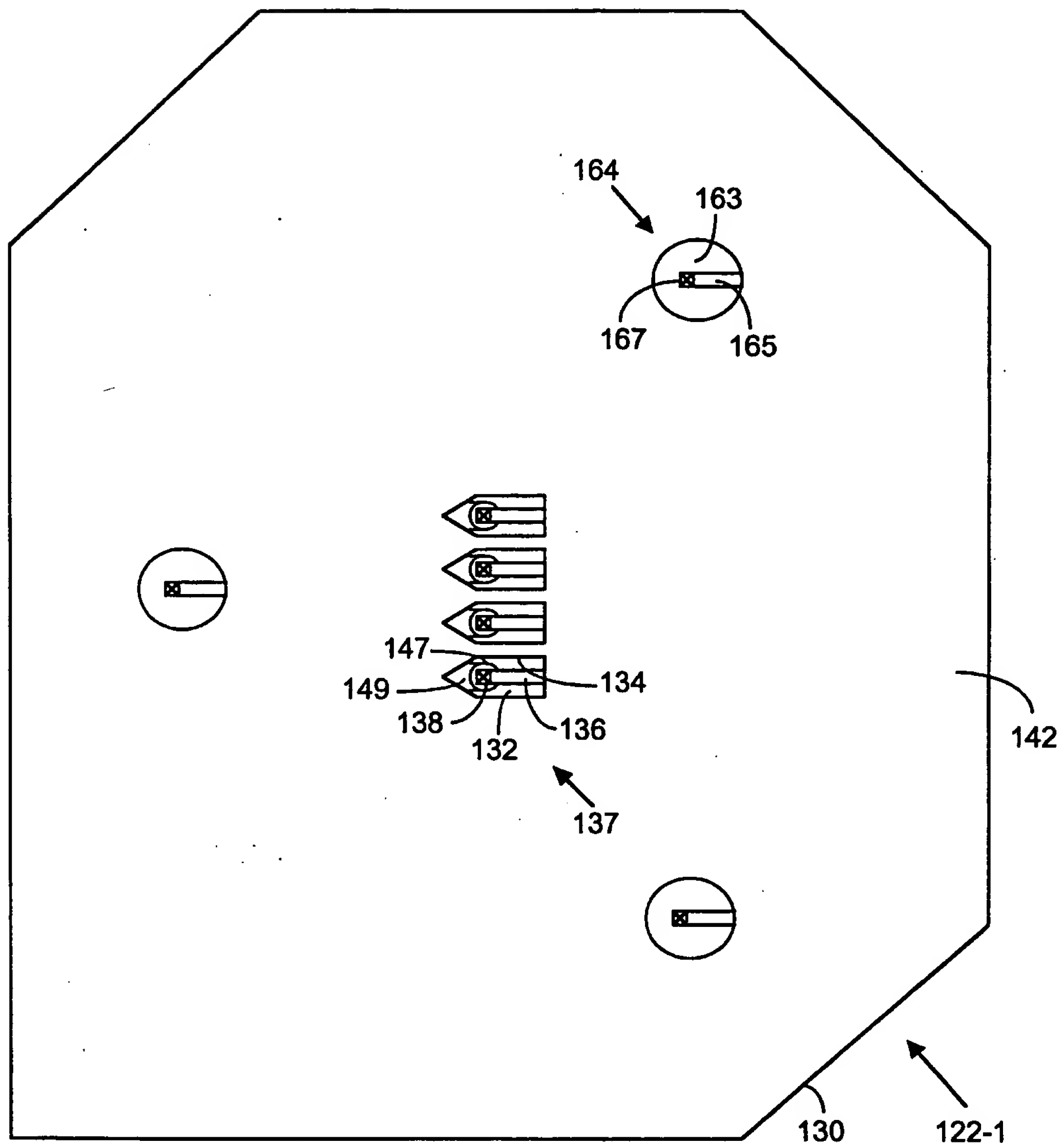


Figure 2

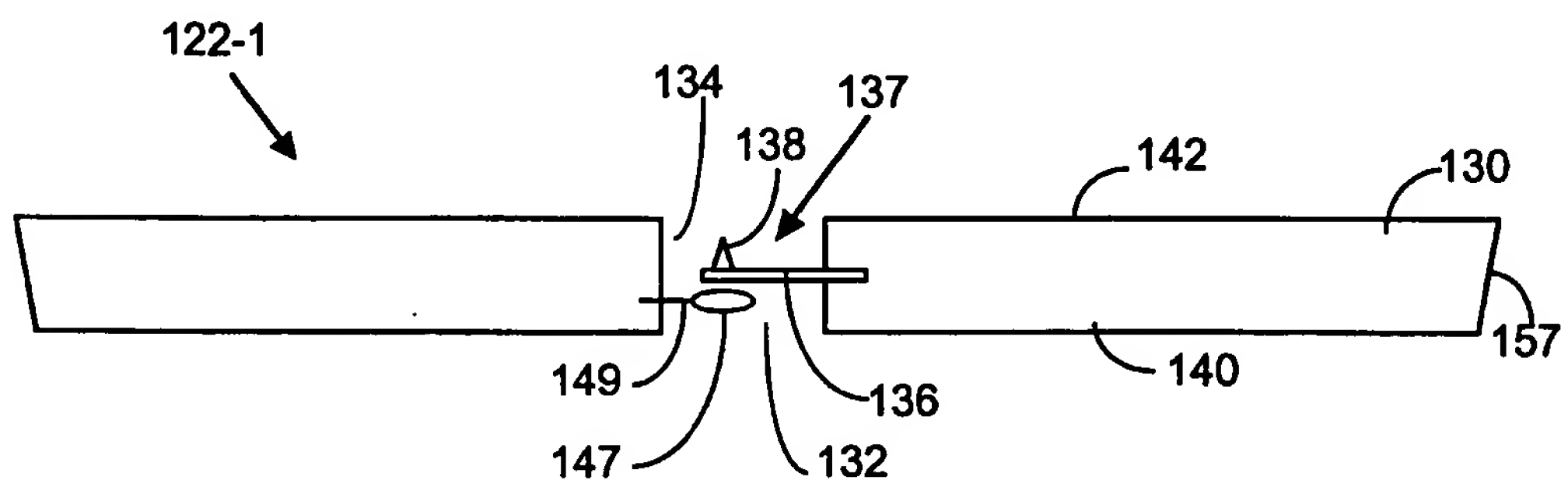


Figure 3

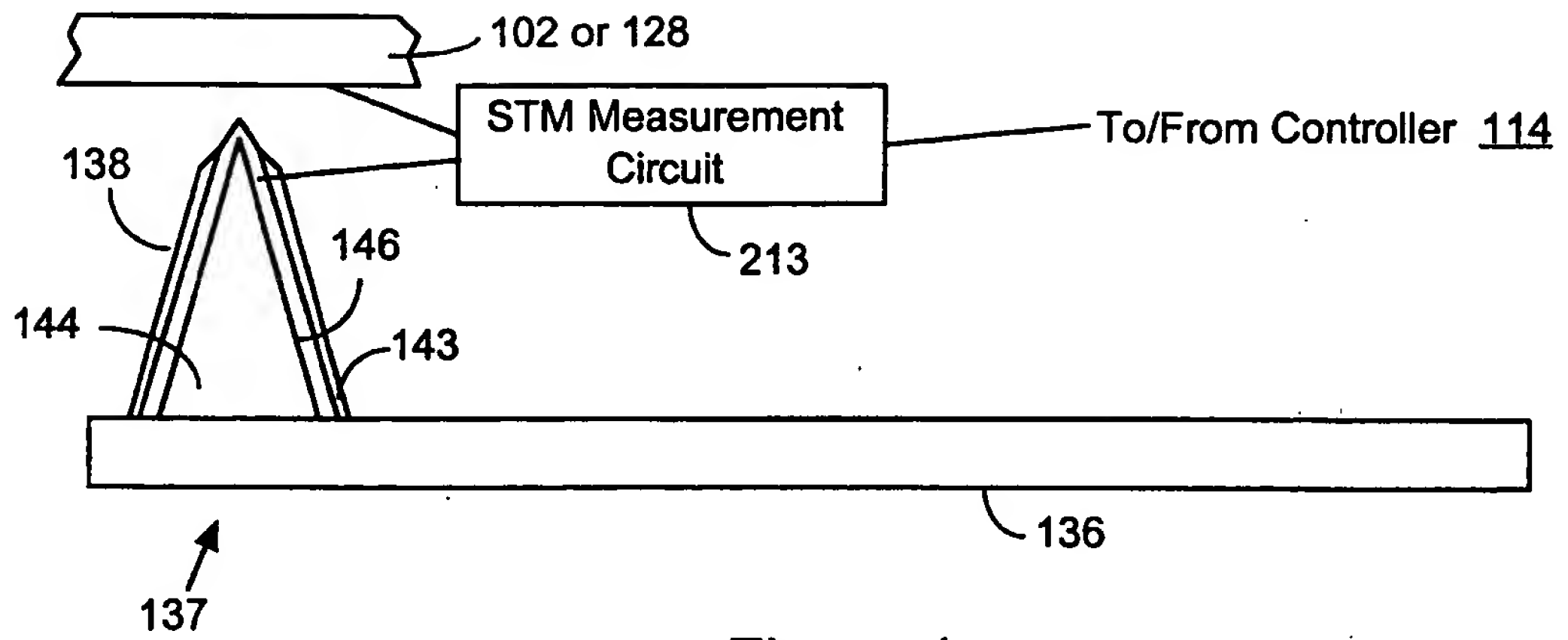


Figure 4

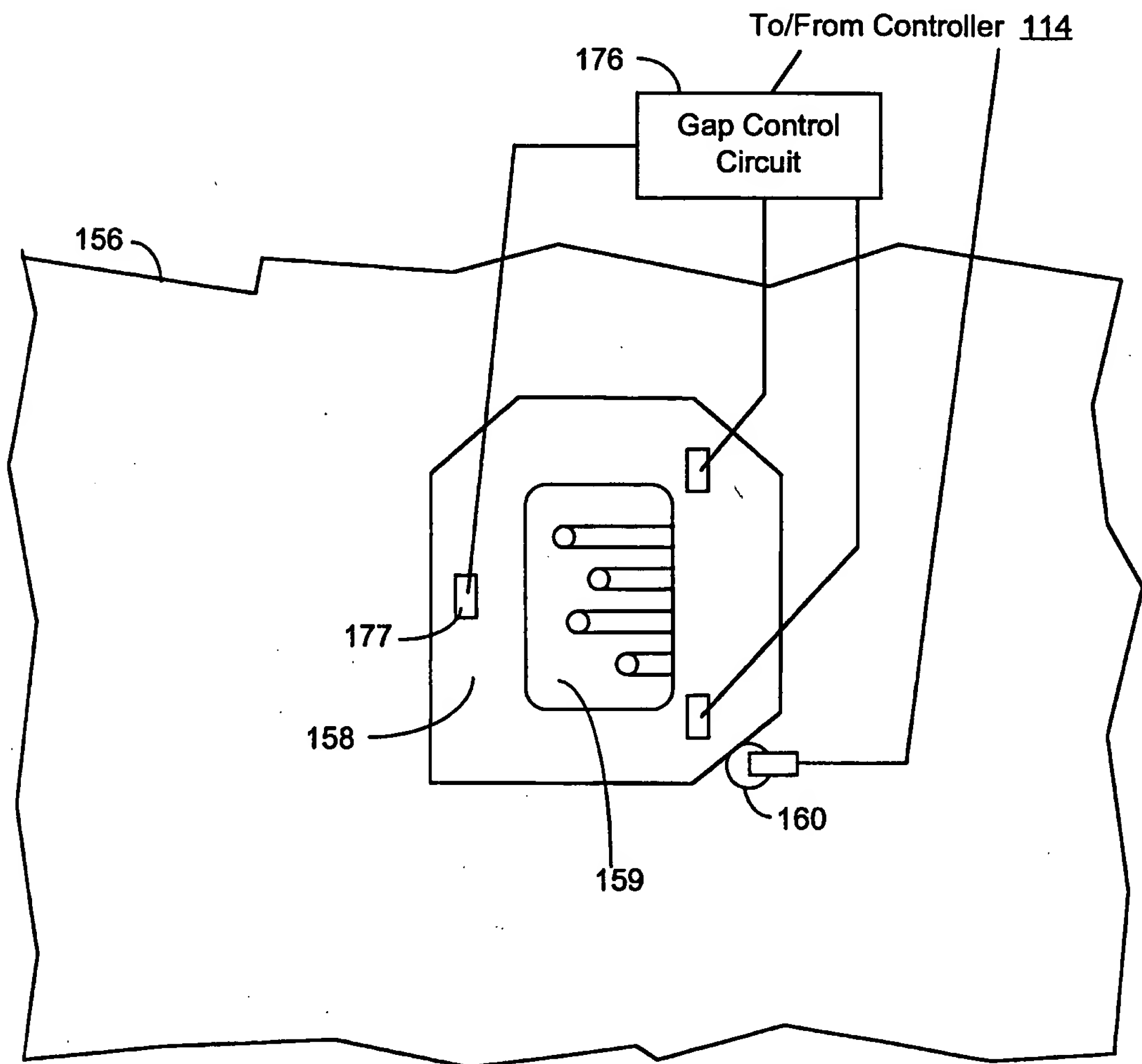


Figure 6

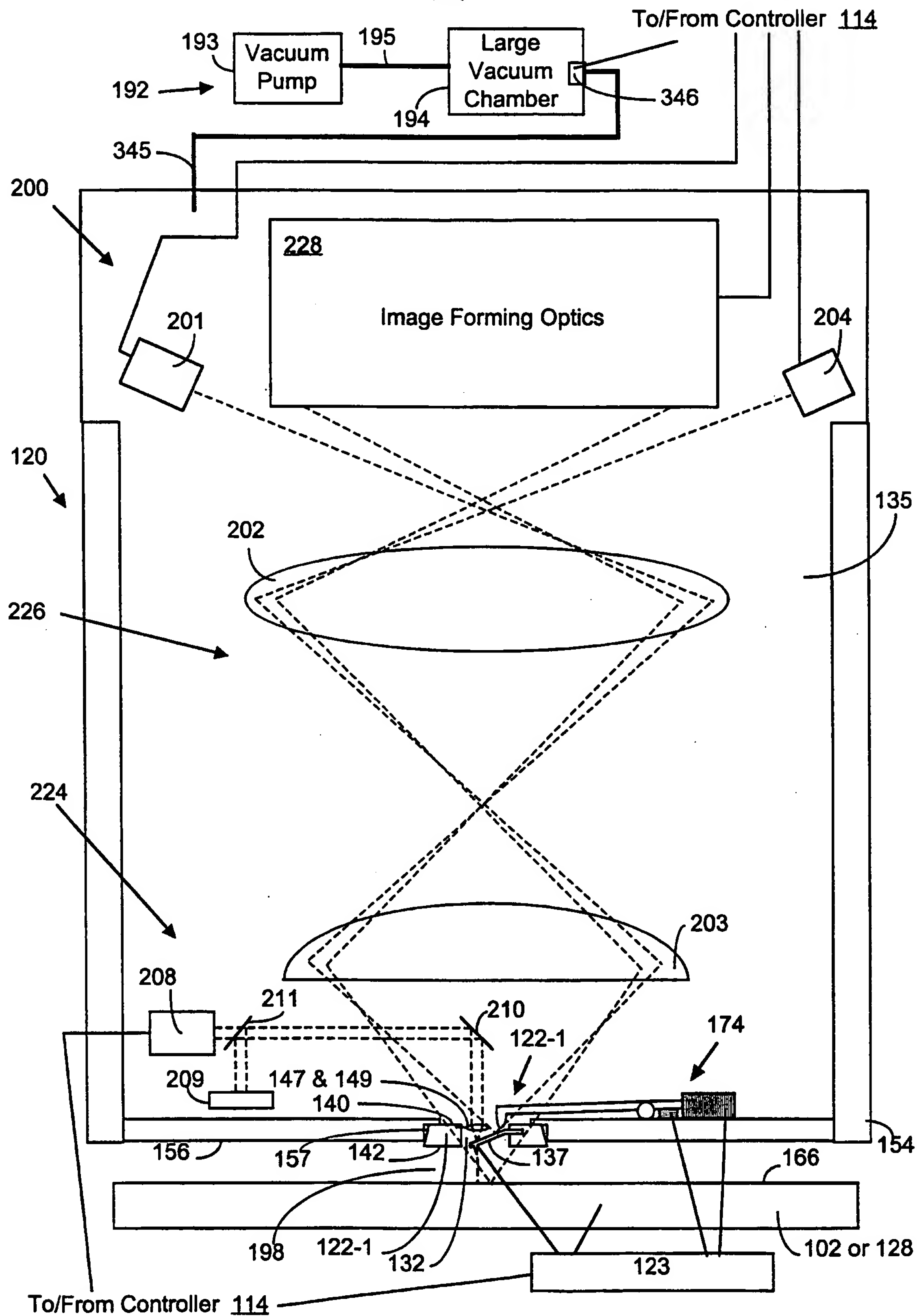


Figure 5

5/58

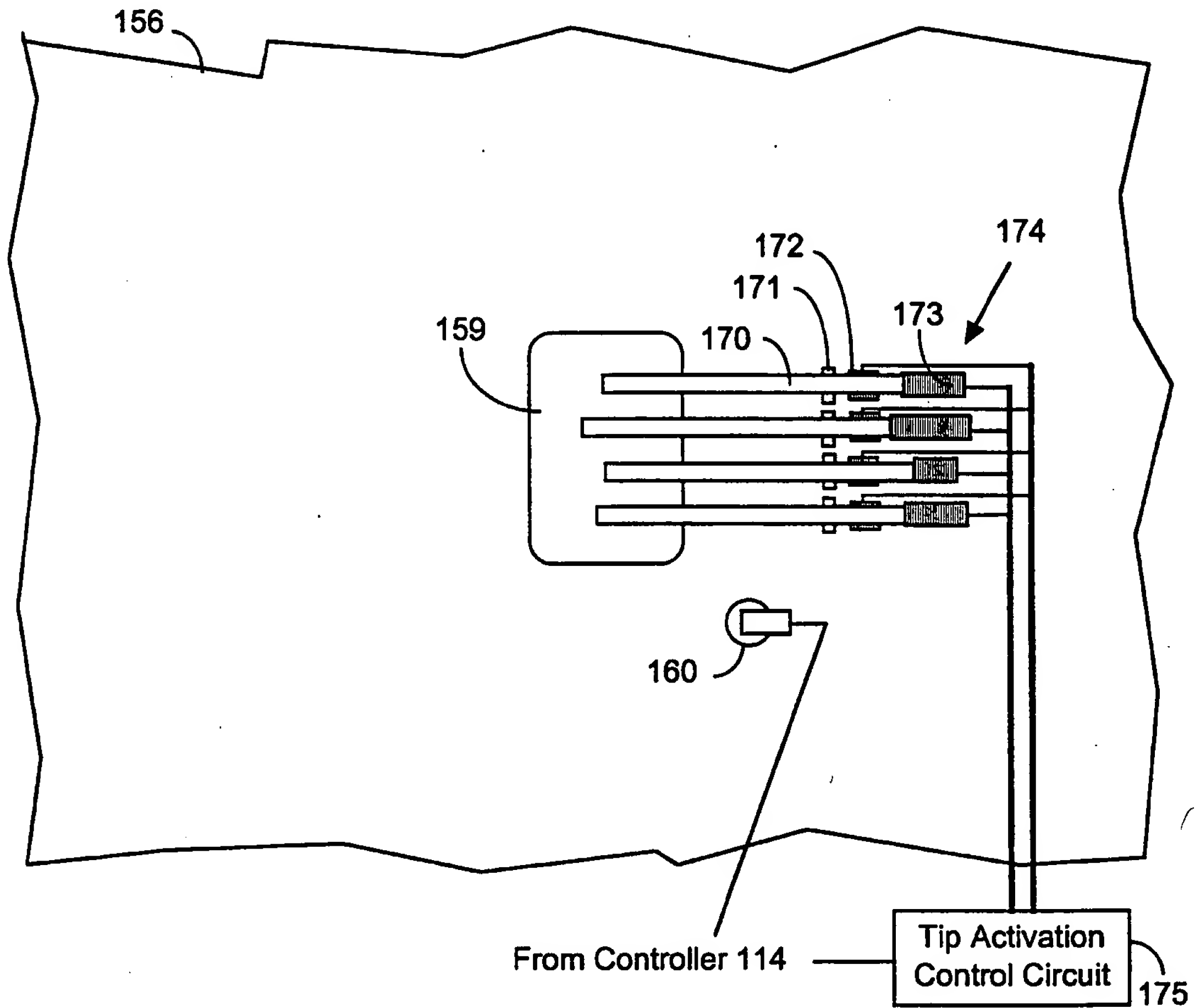


Figure 7

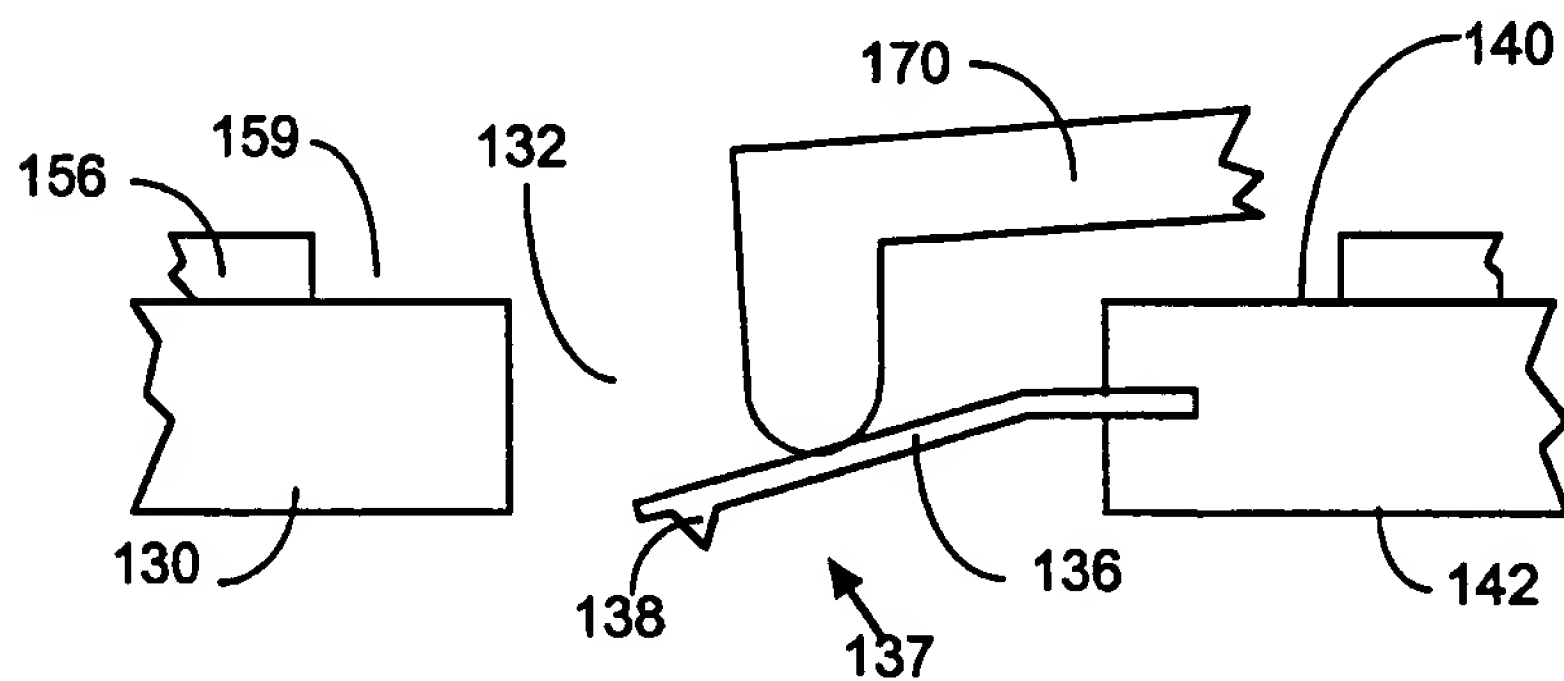


Figure 8

6/58

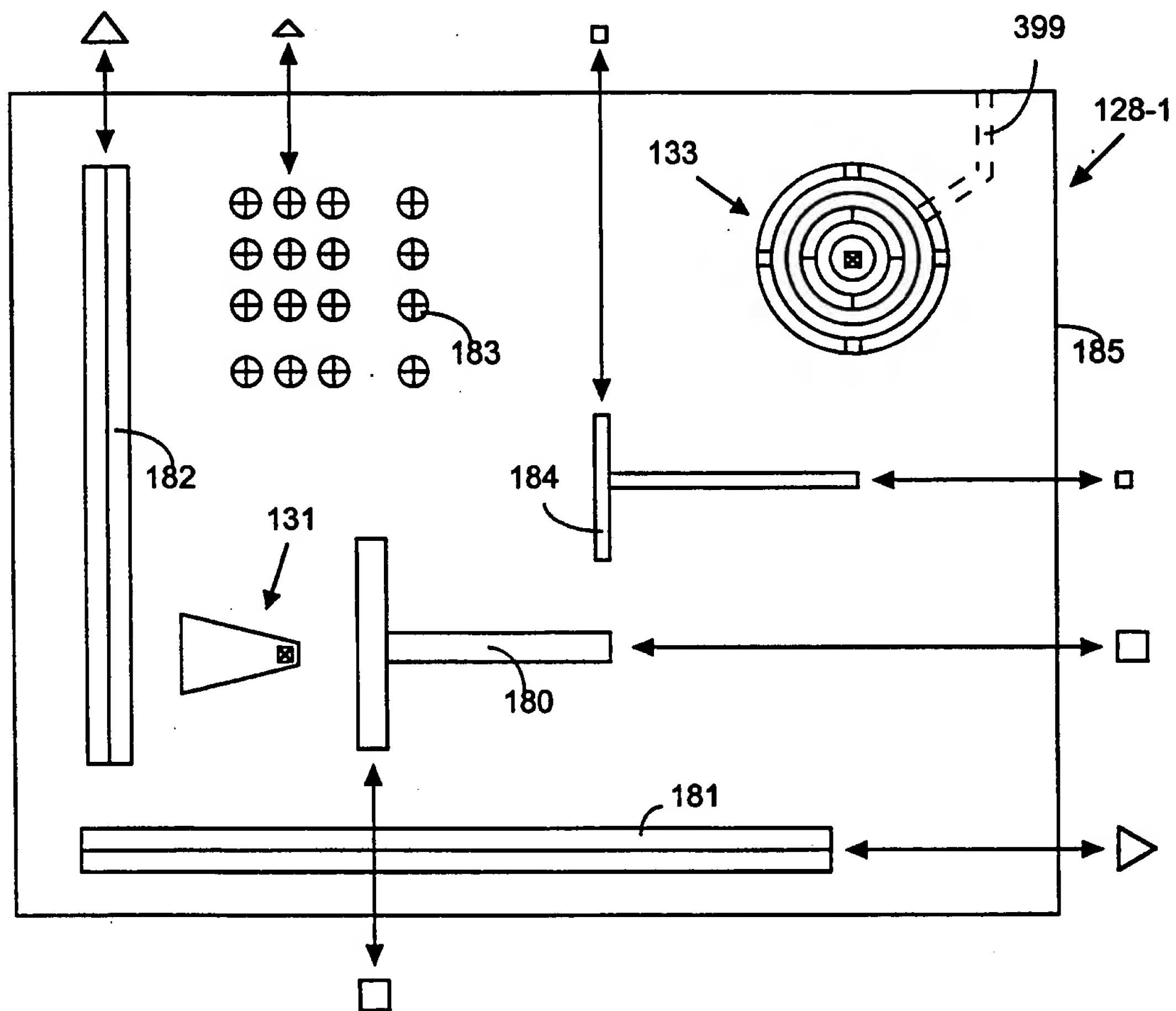


Figure 9

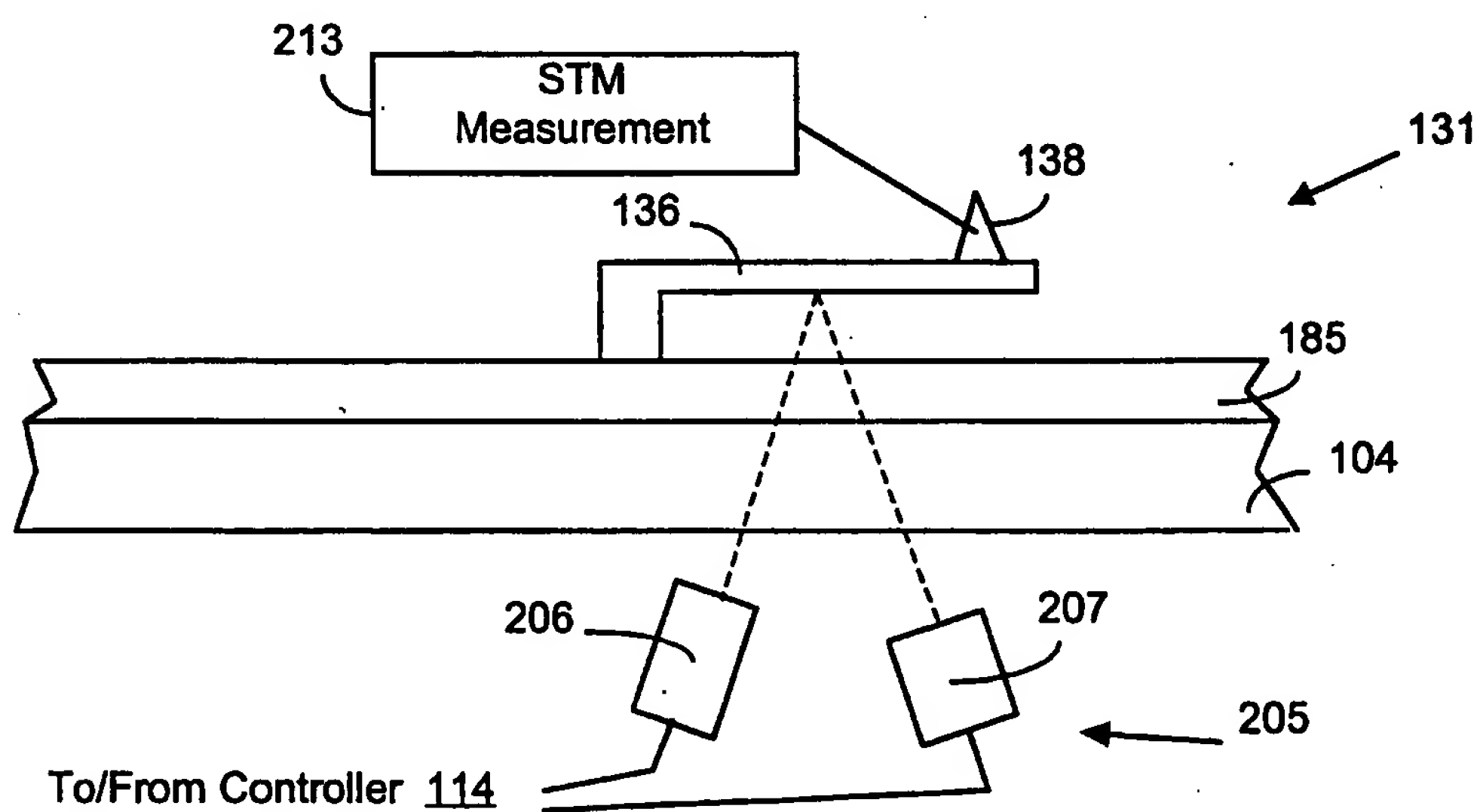


Figure 10

7/58

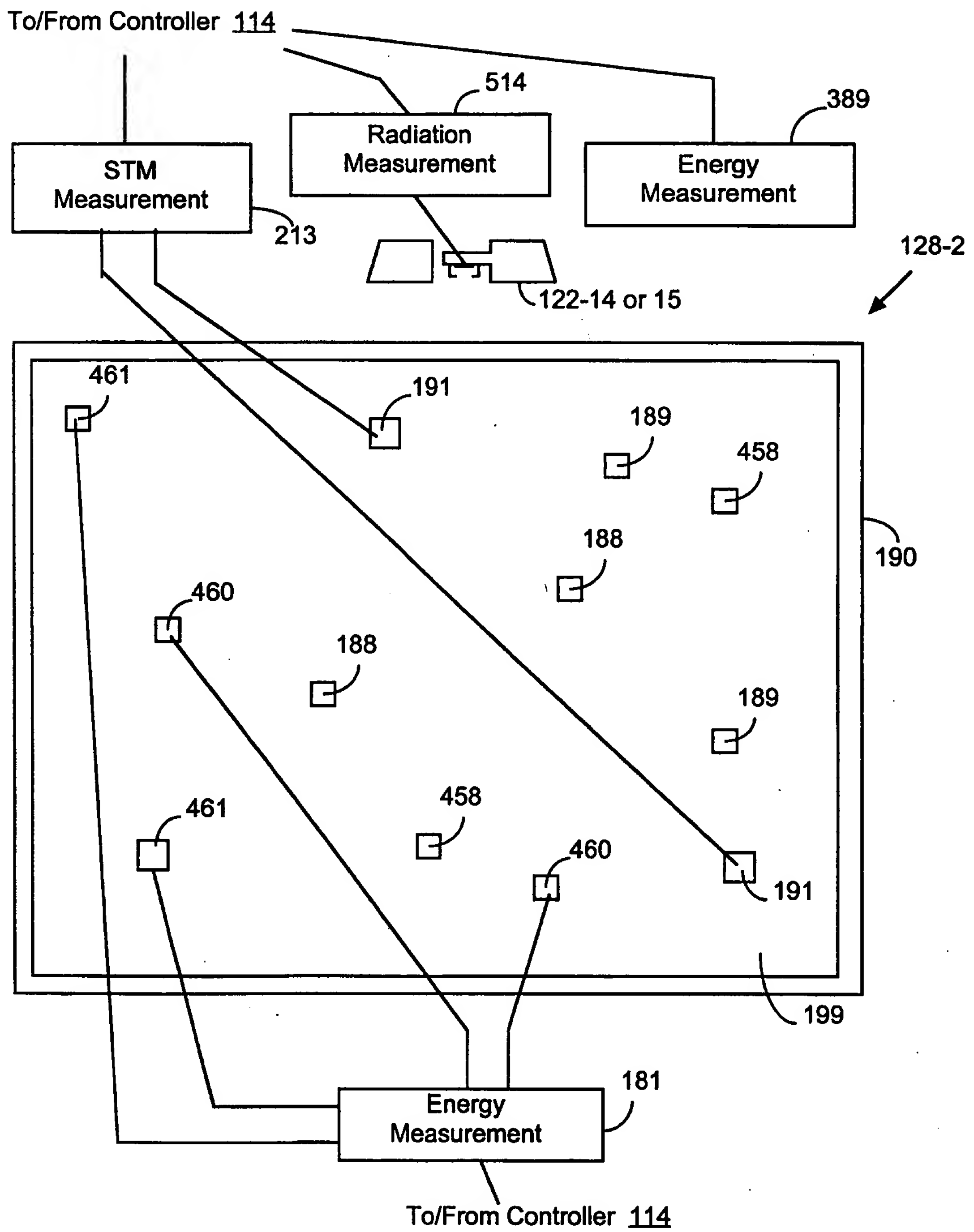


Figure 11

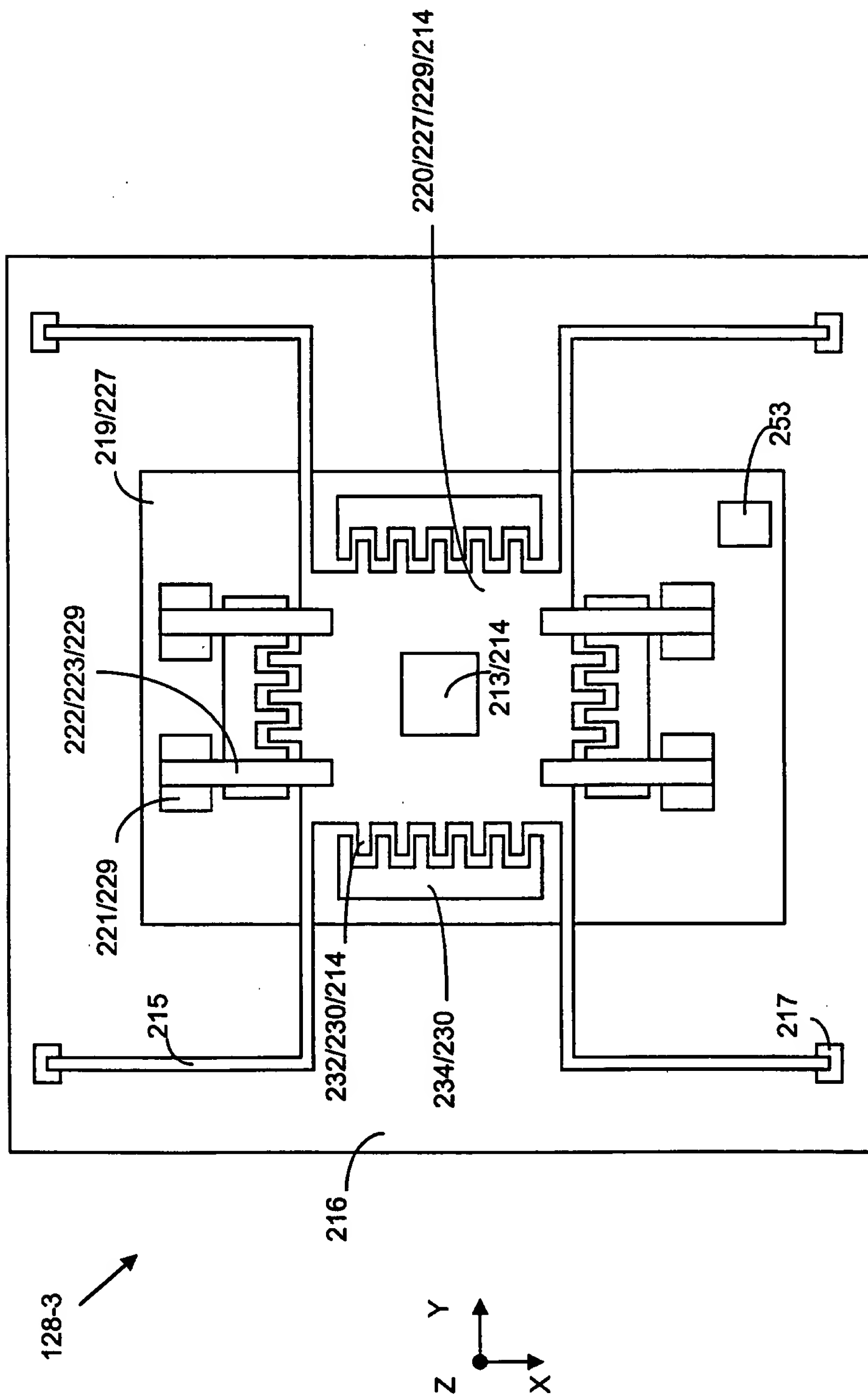


Figure 12

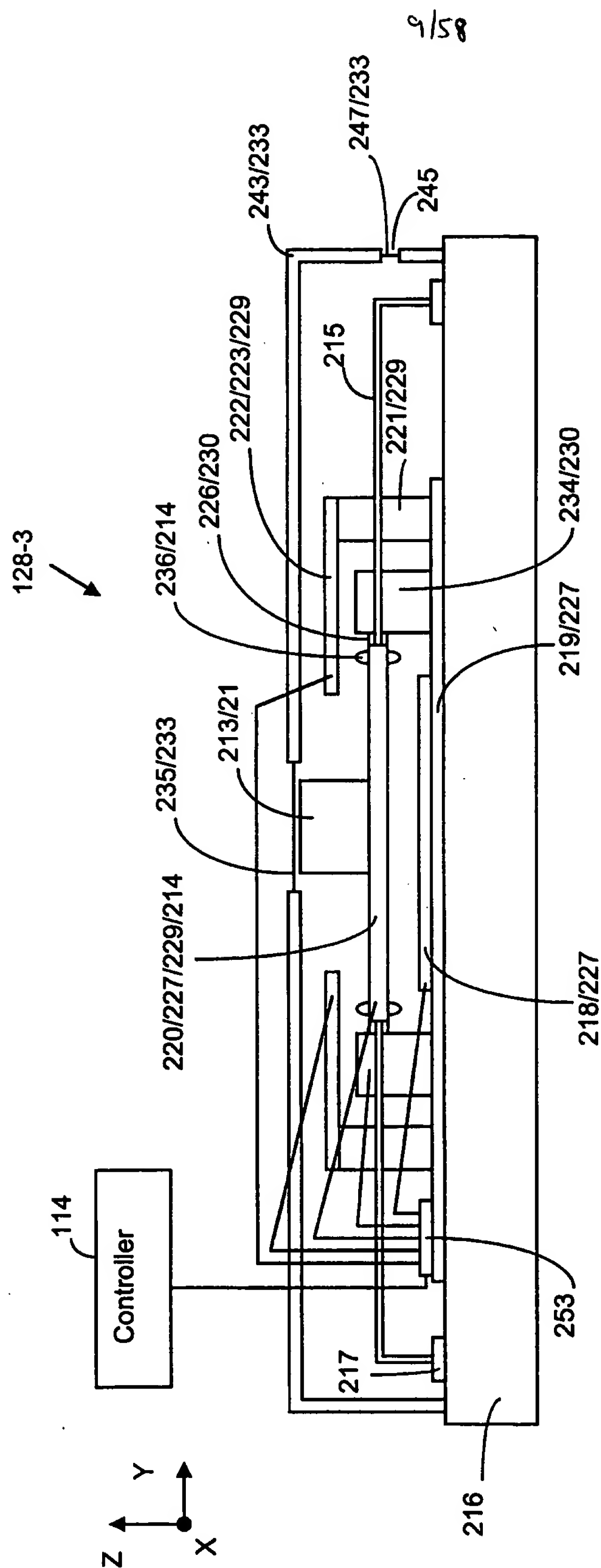


Figure 13

10/58

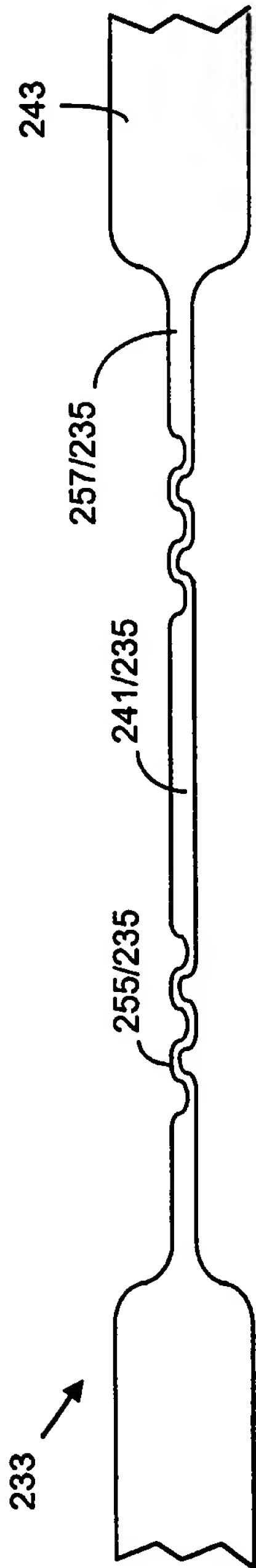


Figure 14

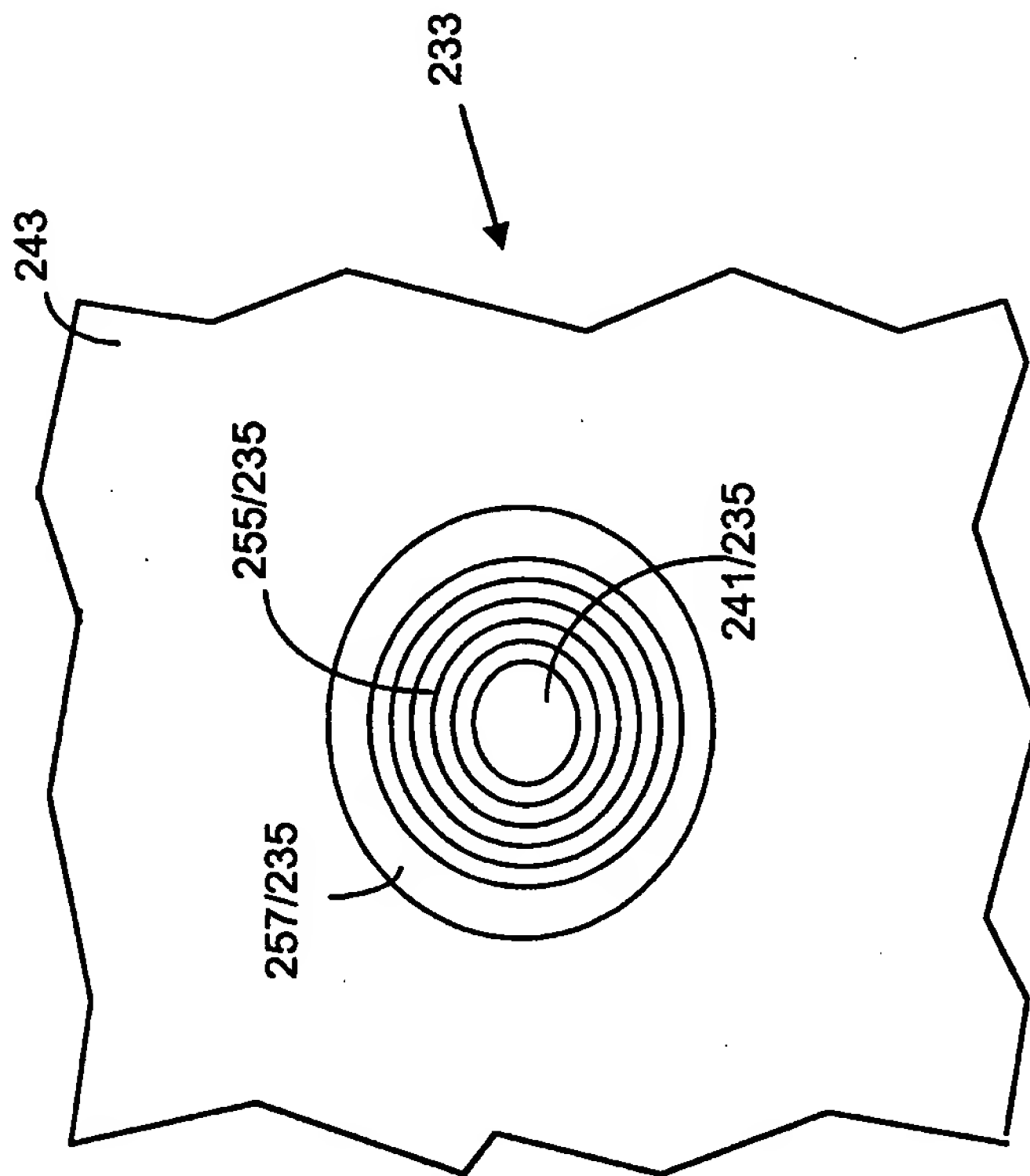


Figure 15

11/58

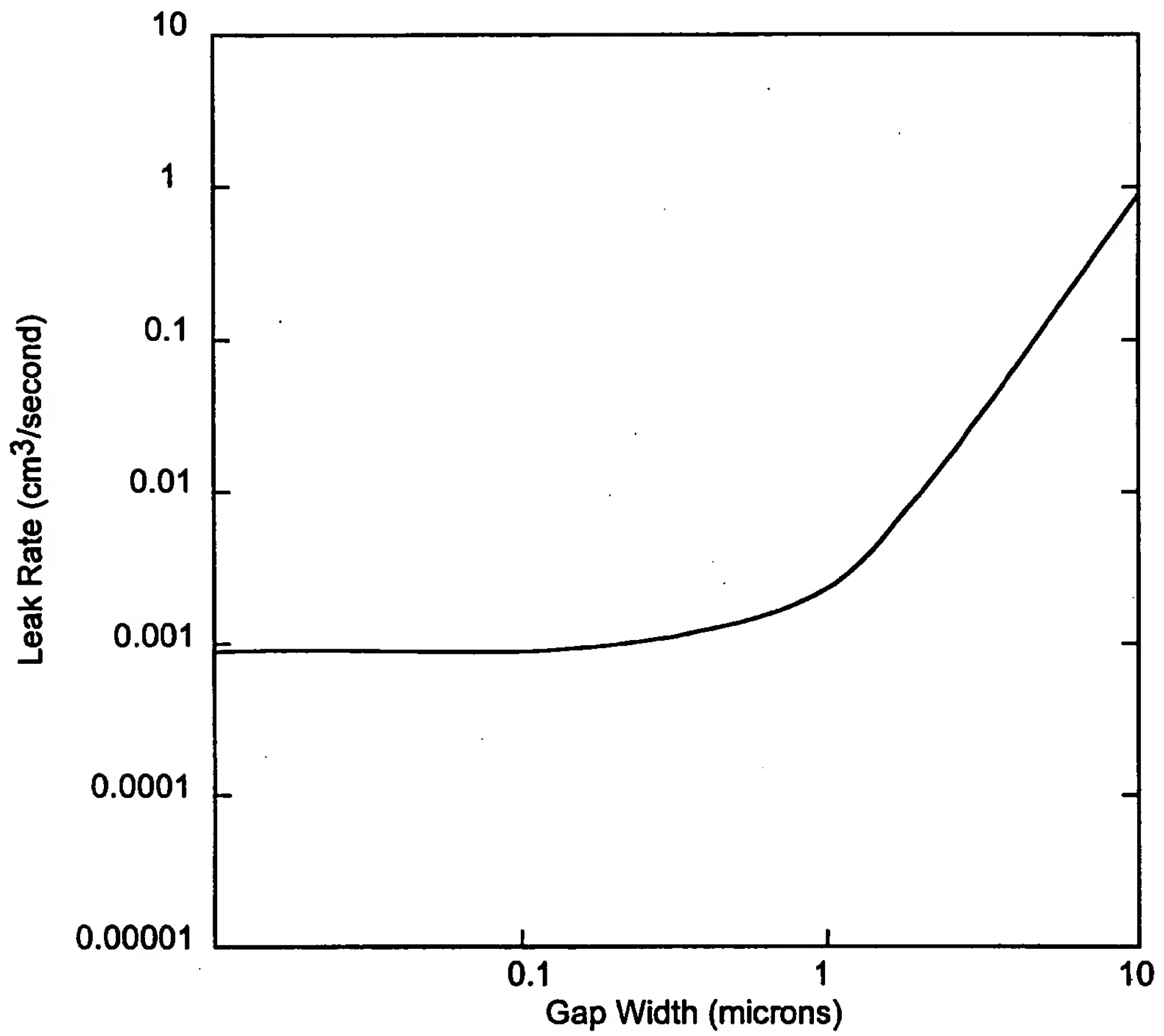


Figure 16

12/58

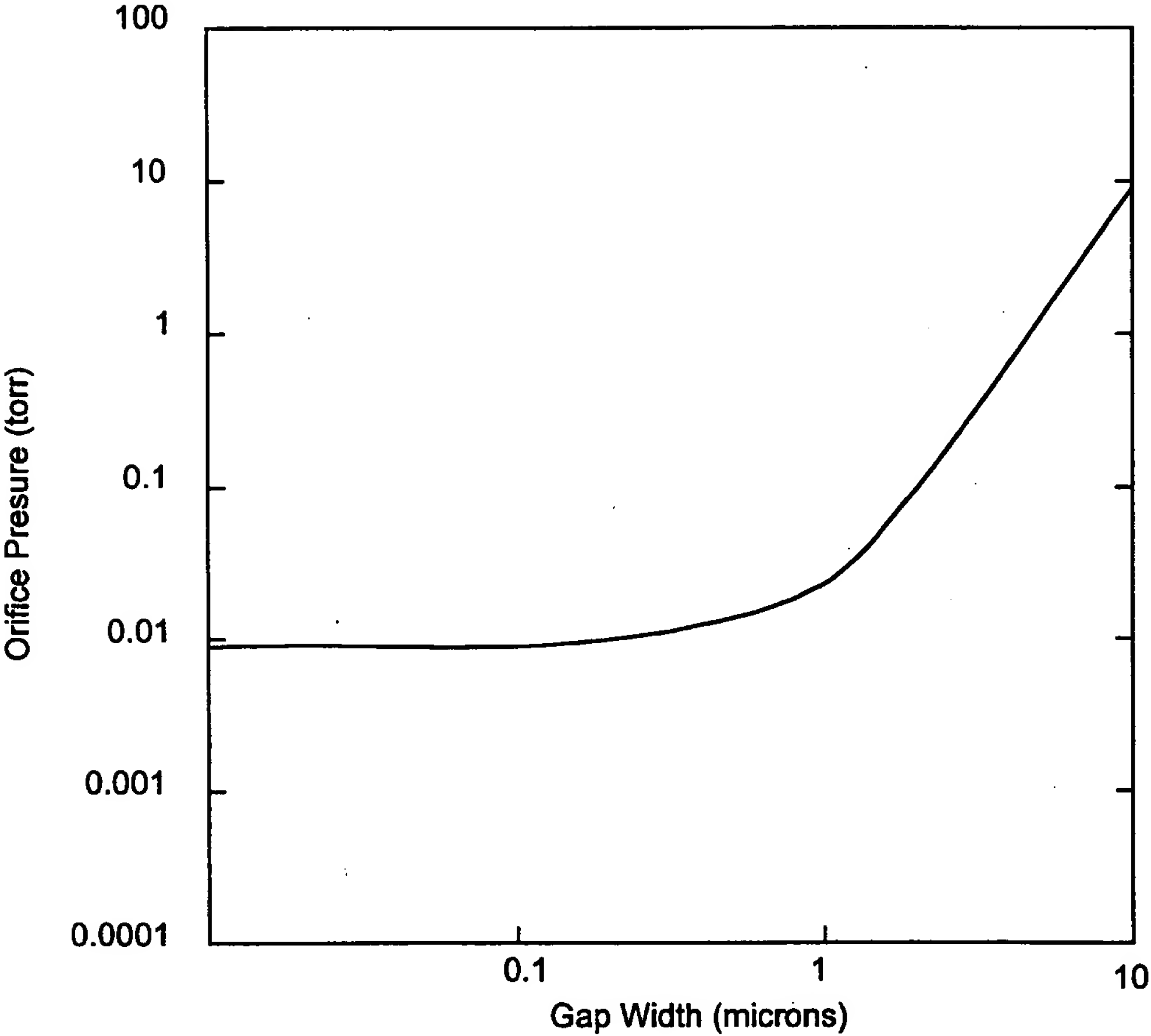


Figure 17

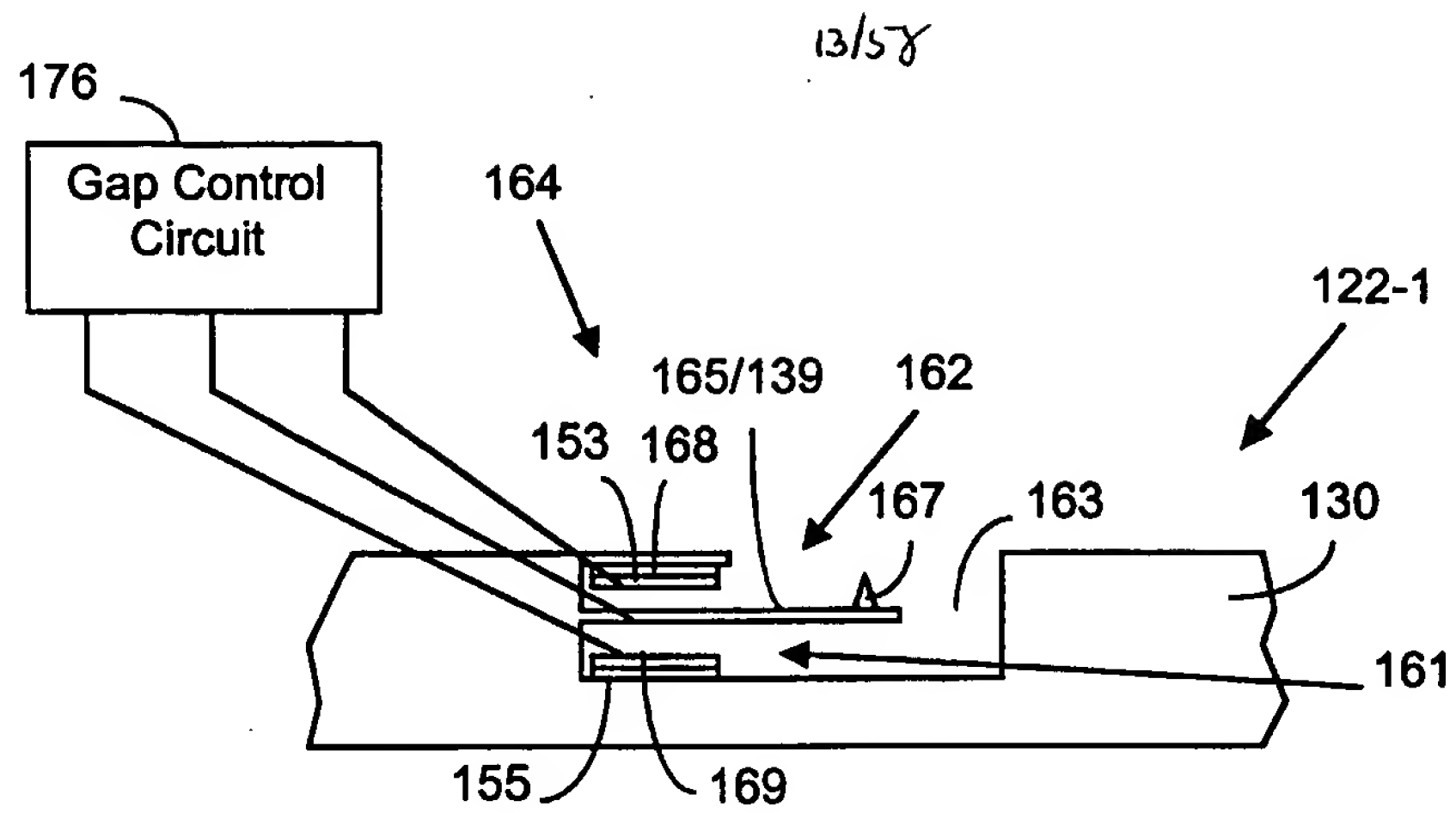


Figure 18

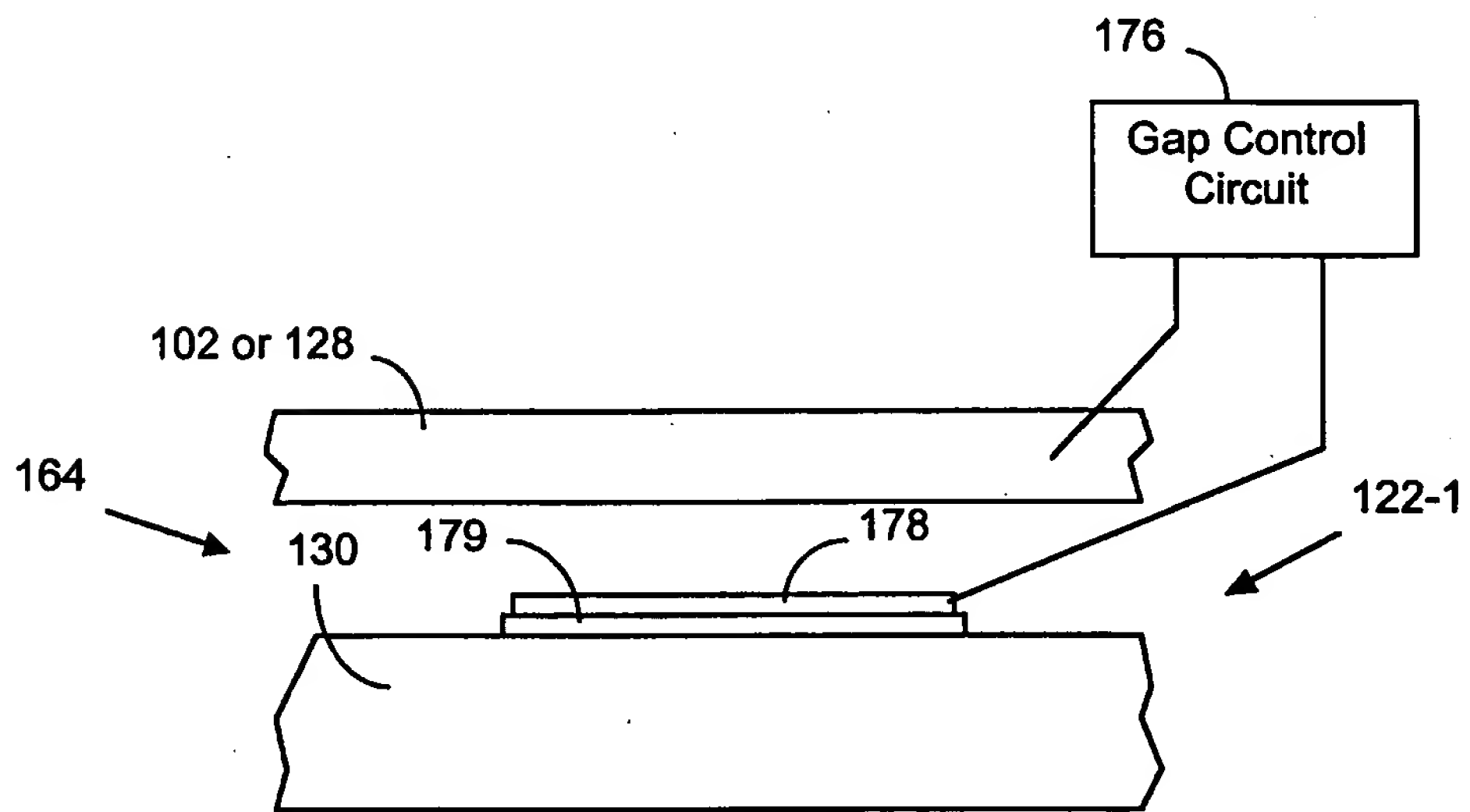


Figure 19

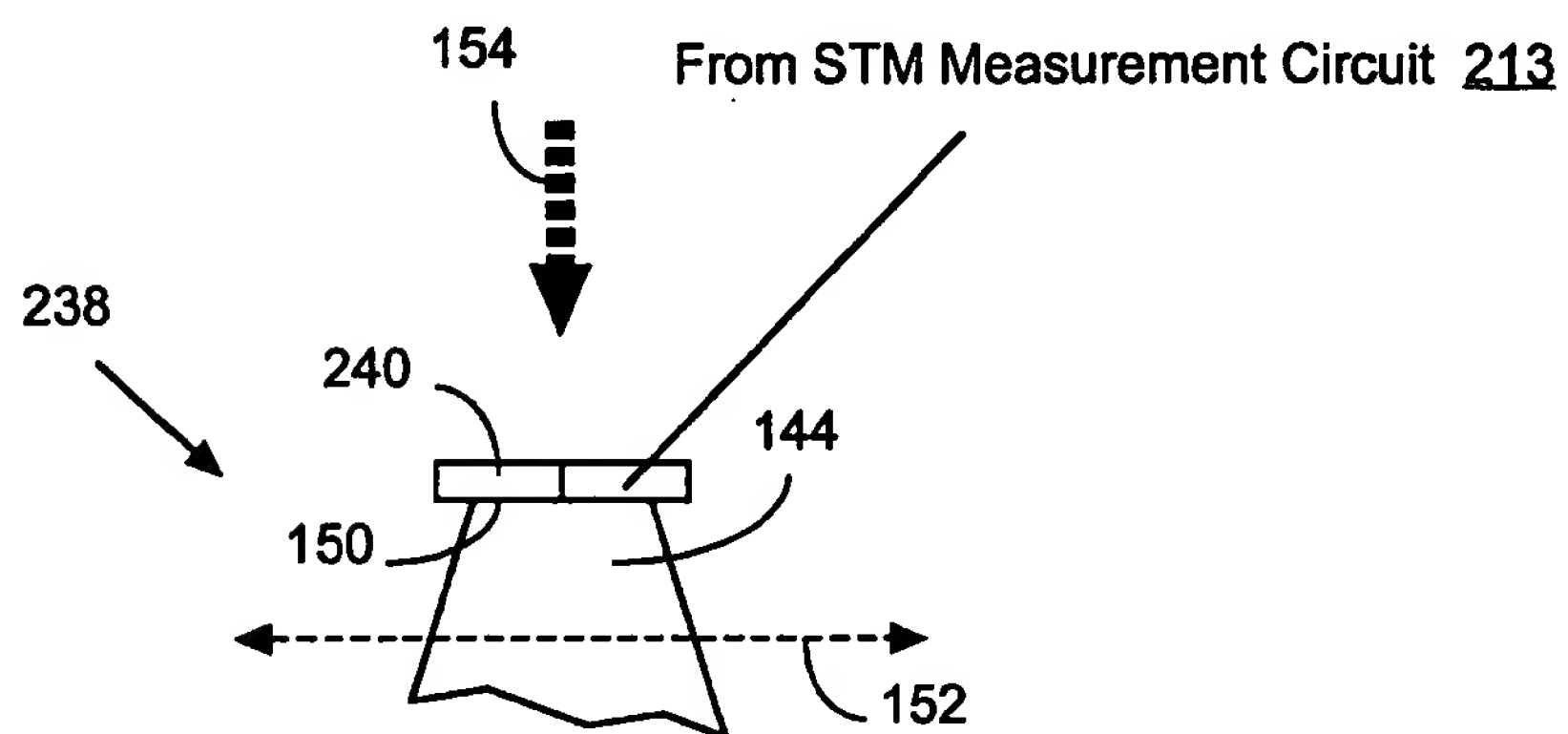


Figure 21

122-2

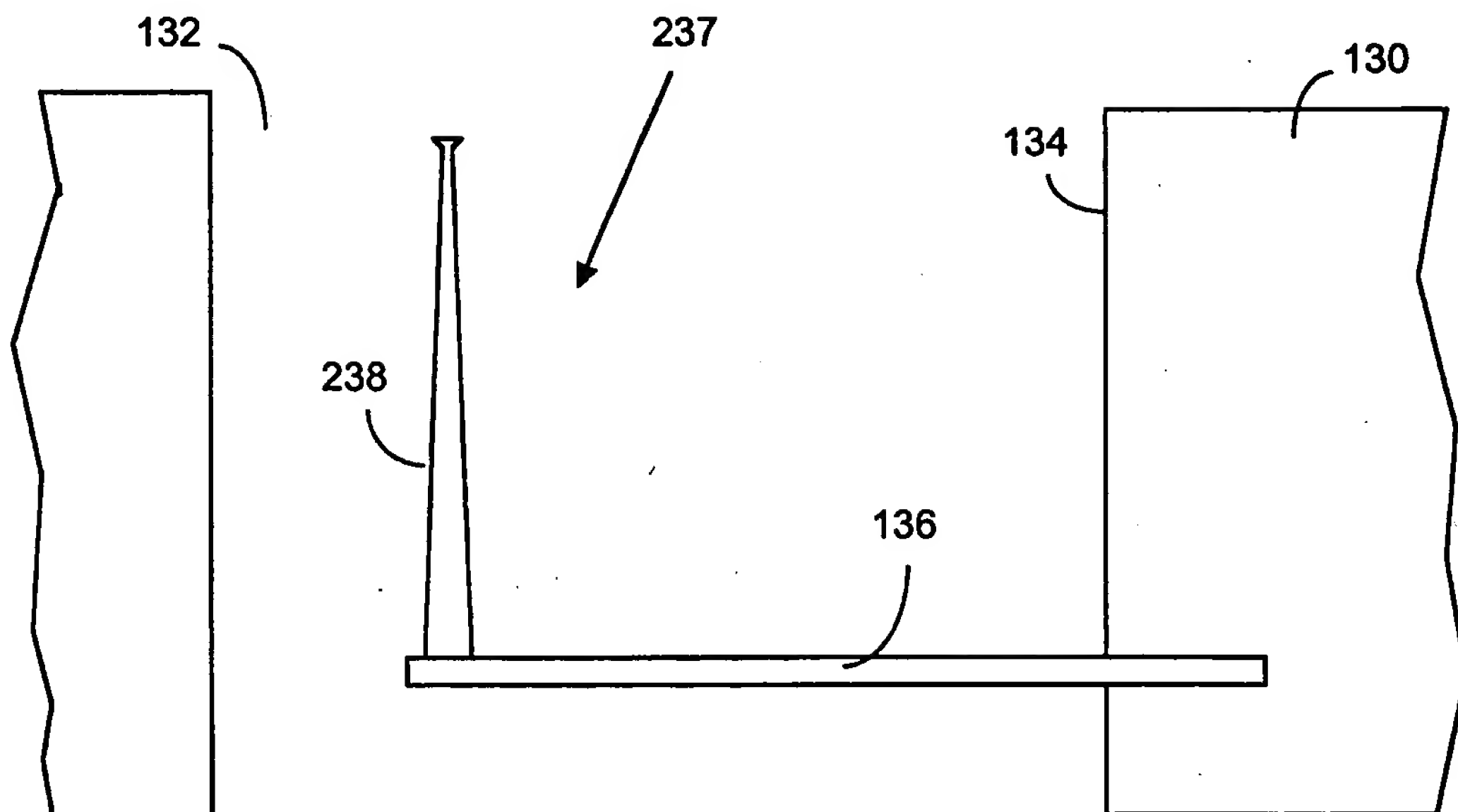


Figure 20

From STM
Measurement
Circuit 213

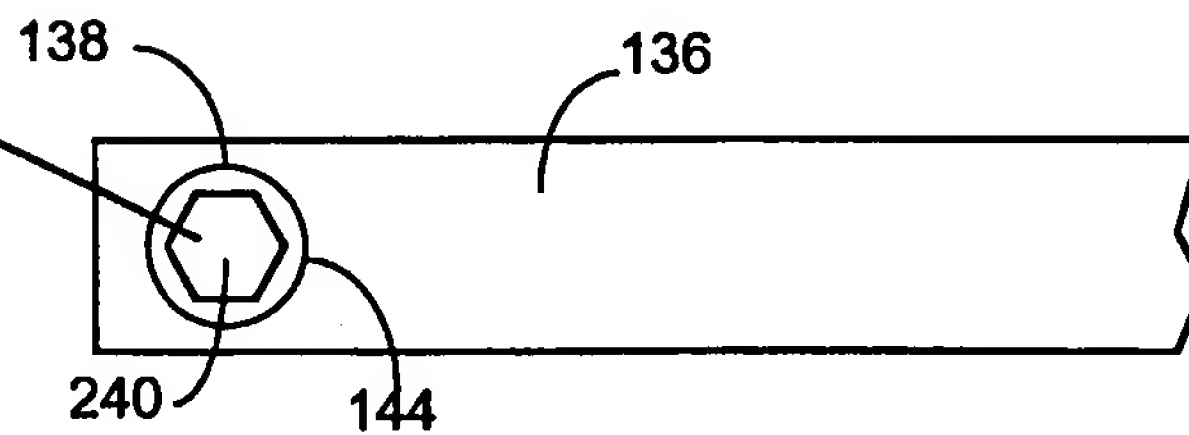


Figure 22

From STM
Measurement
Circuit 213

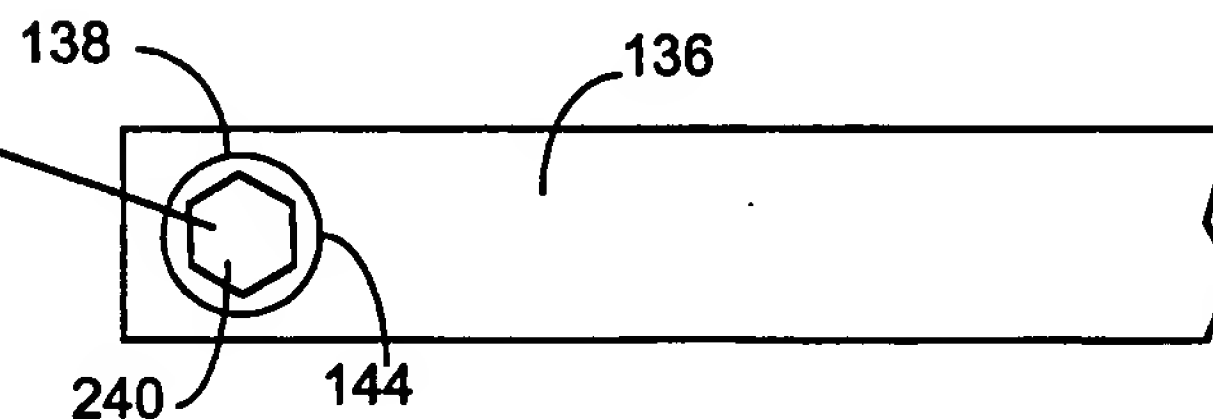


Figure 23

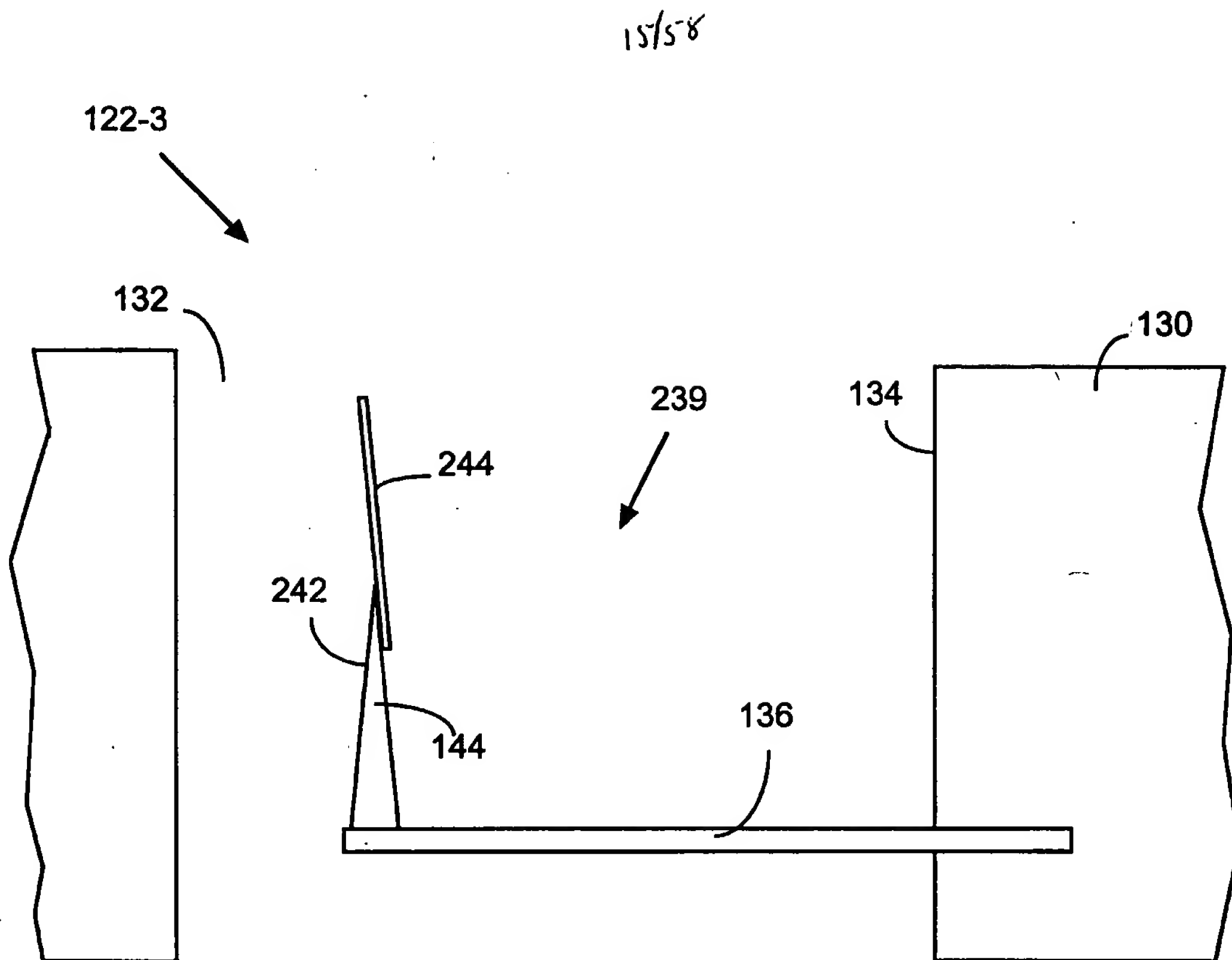


Figure 24

From STM Measurement Circuit 213

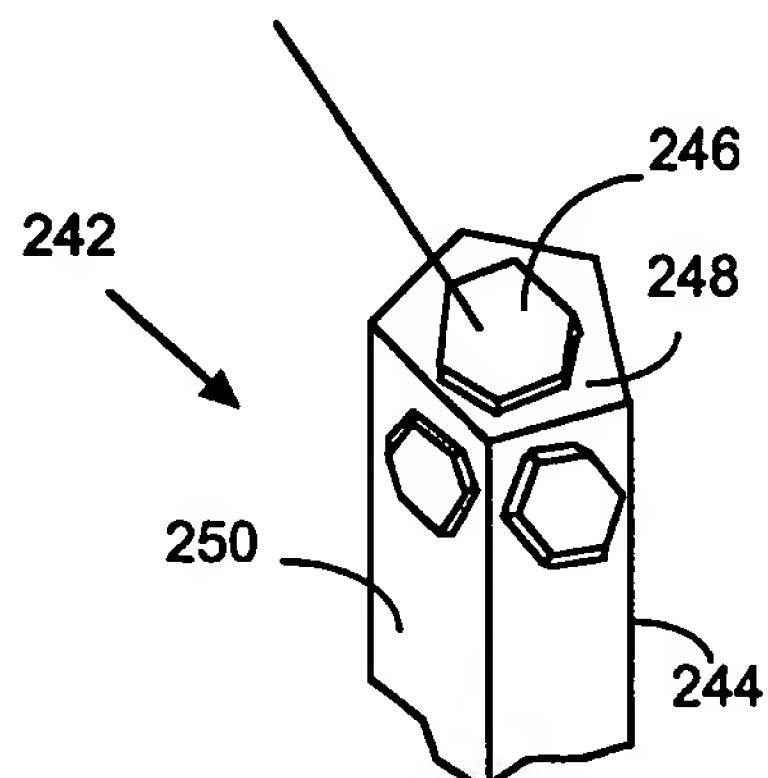


Figure 25

16/58

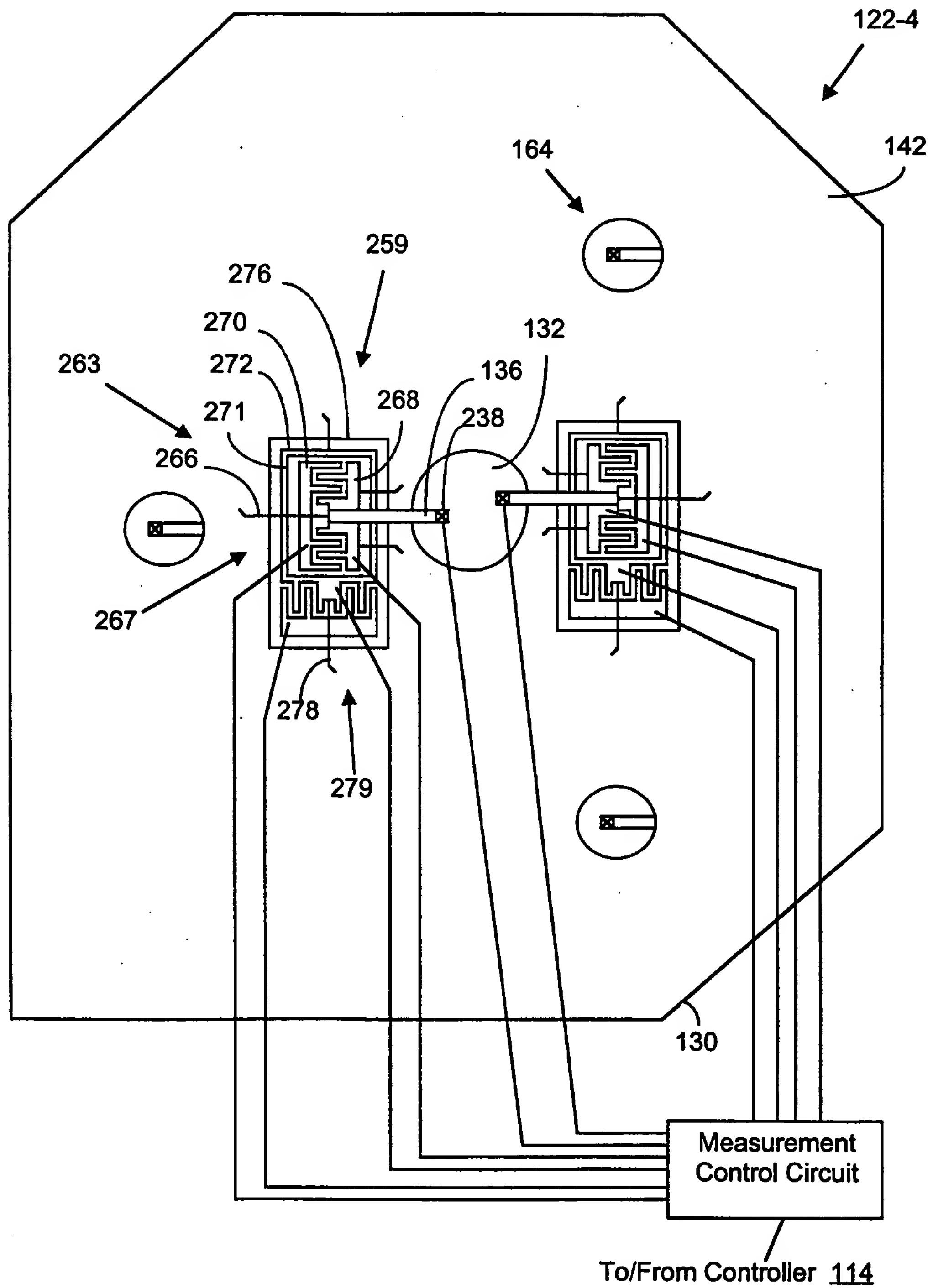


Figure 26

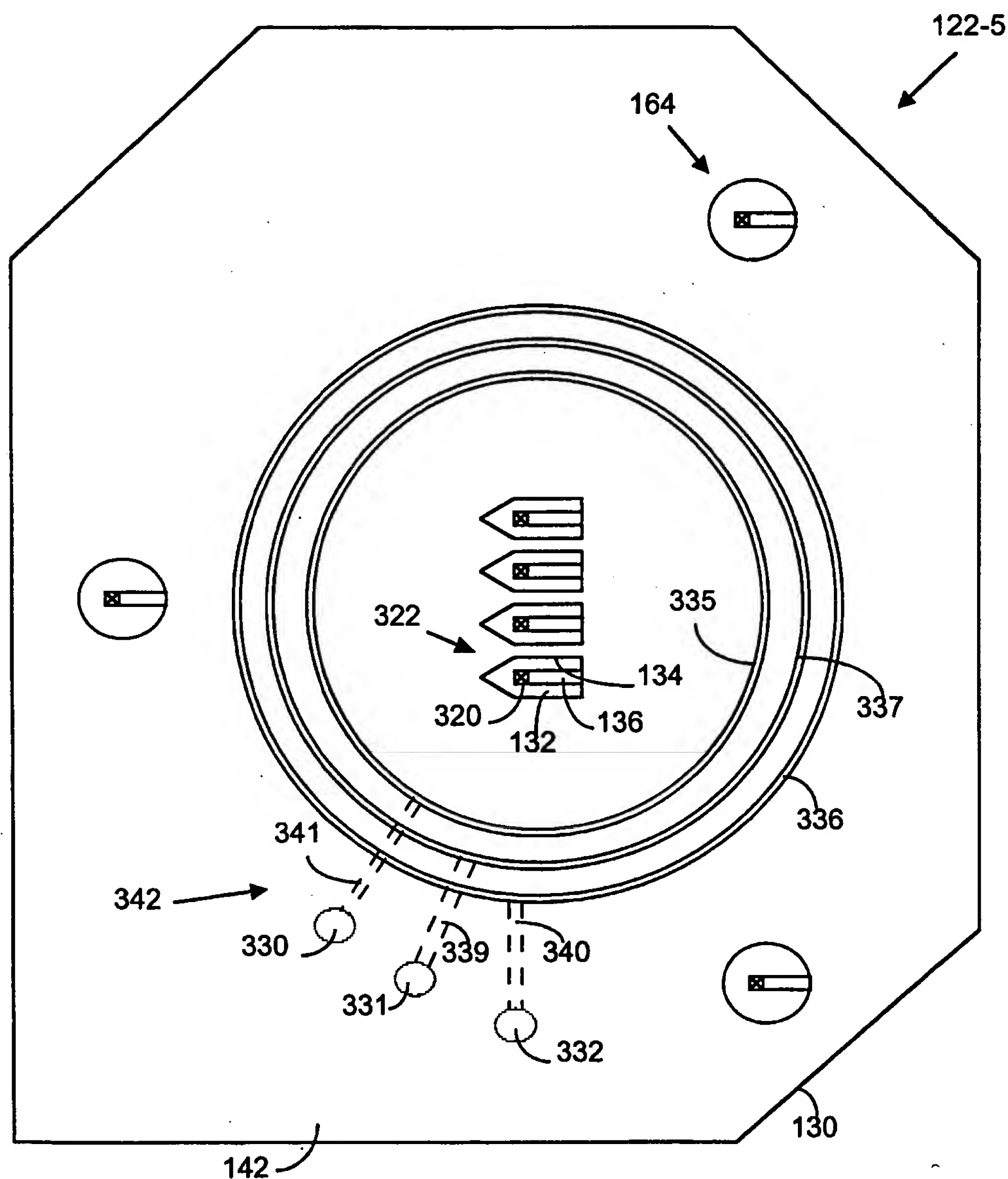


Figure 27

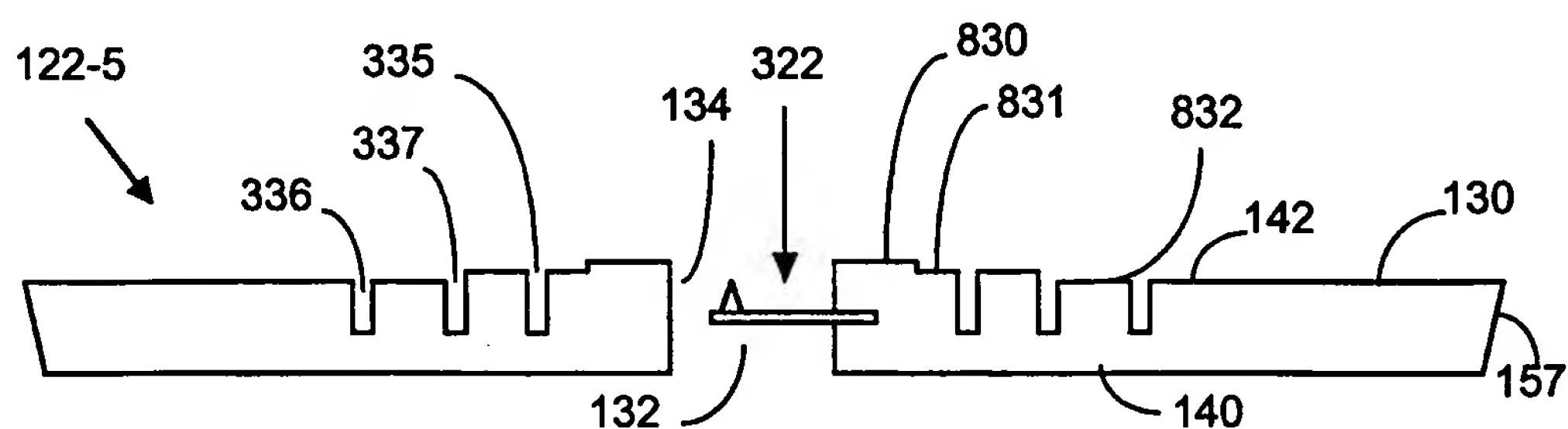


Figure 86

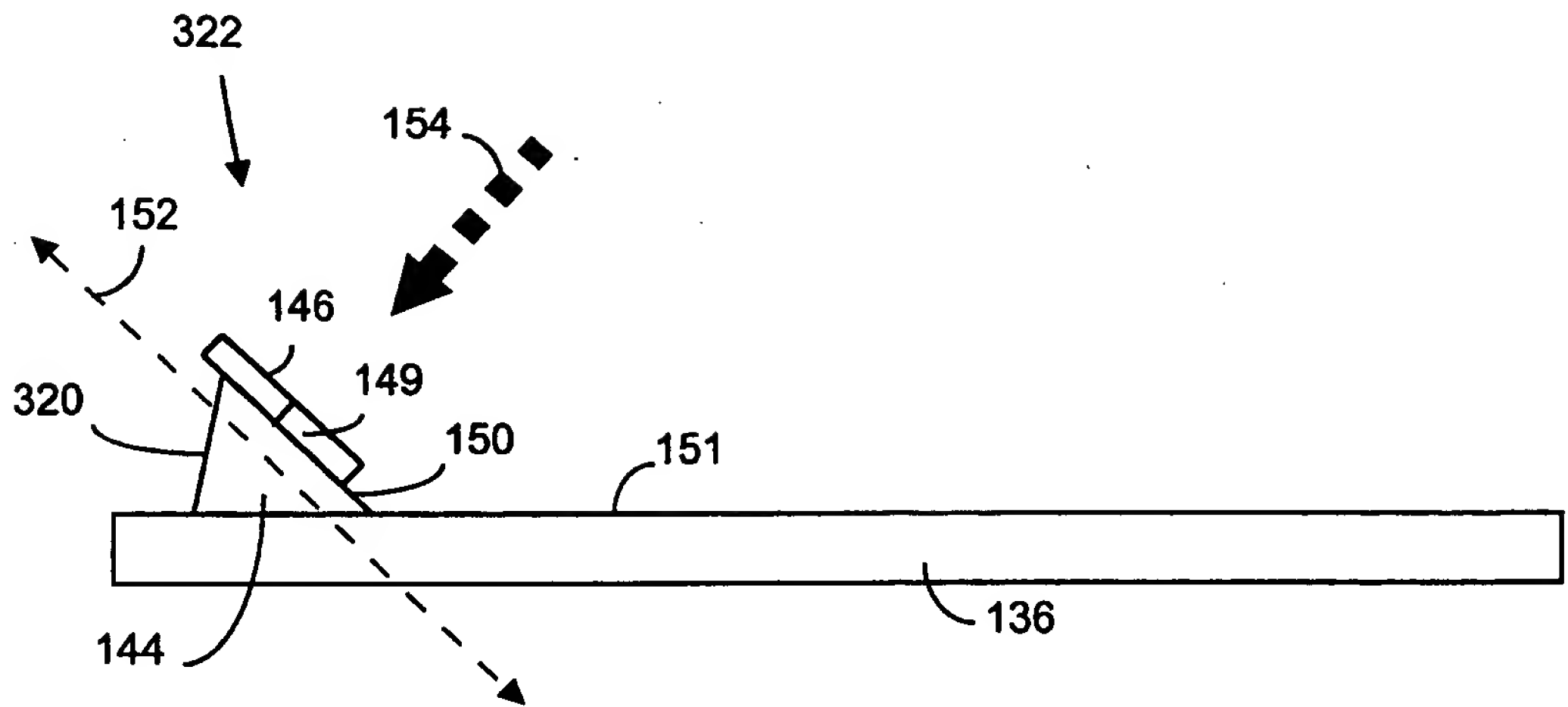


Figure 28

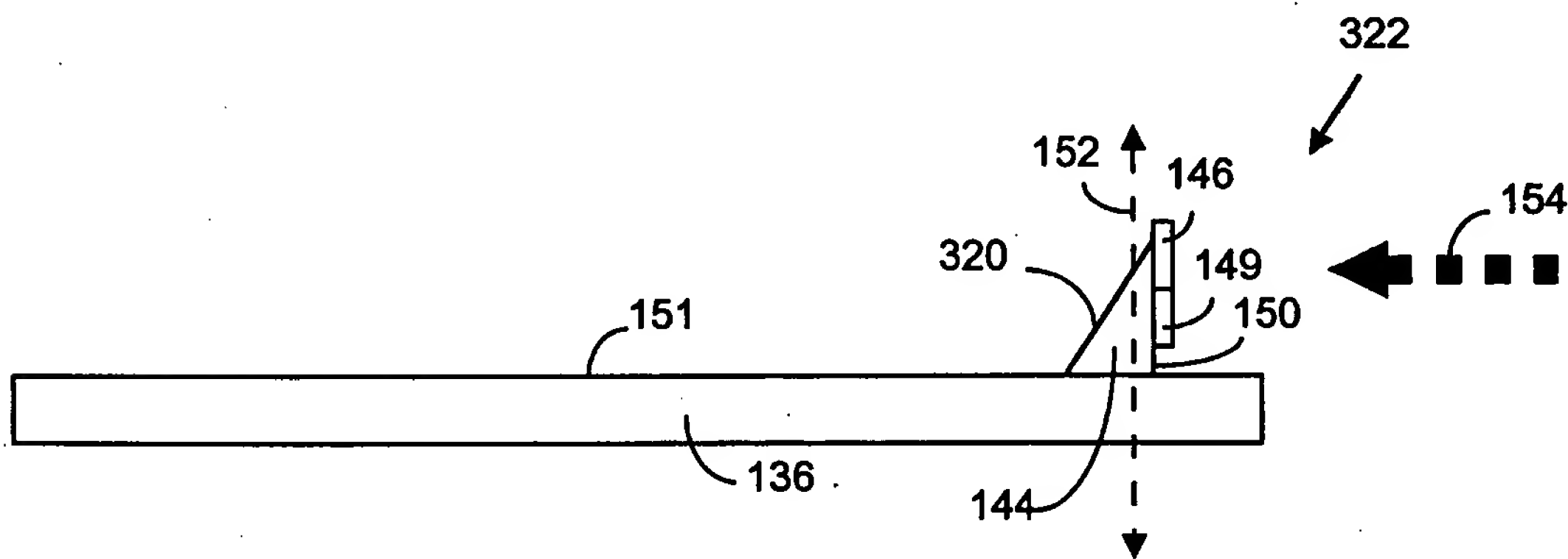


Figure 29

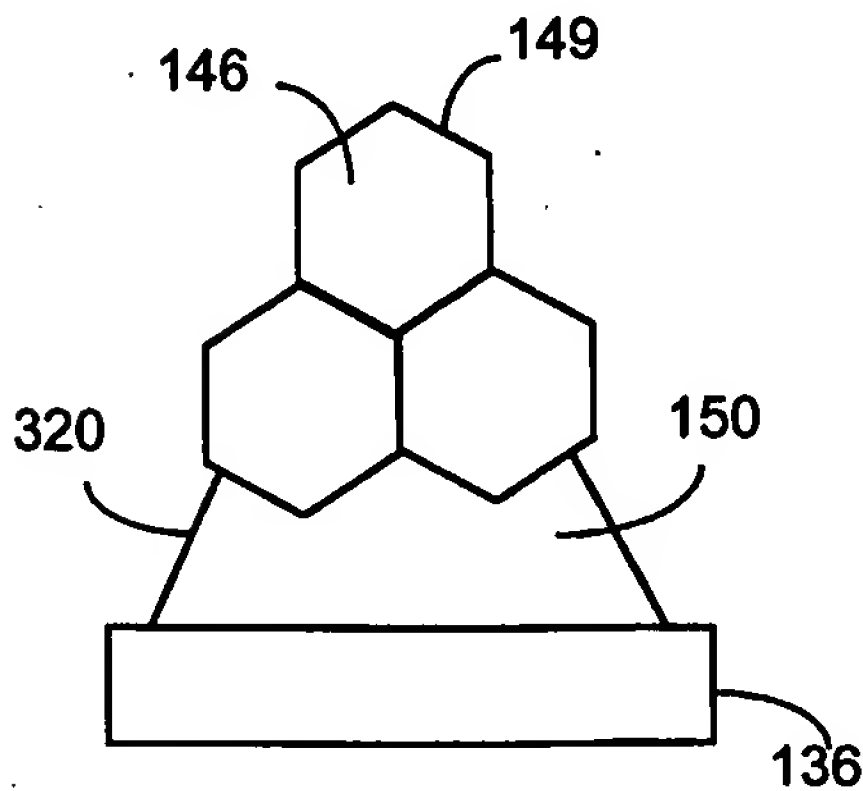


Figure 30

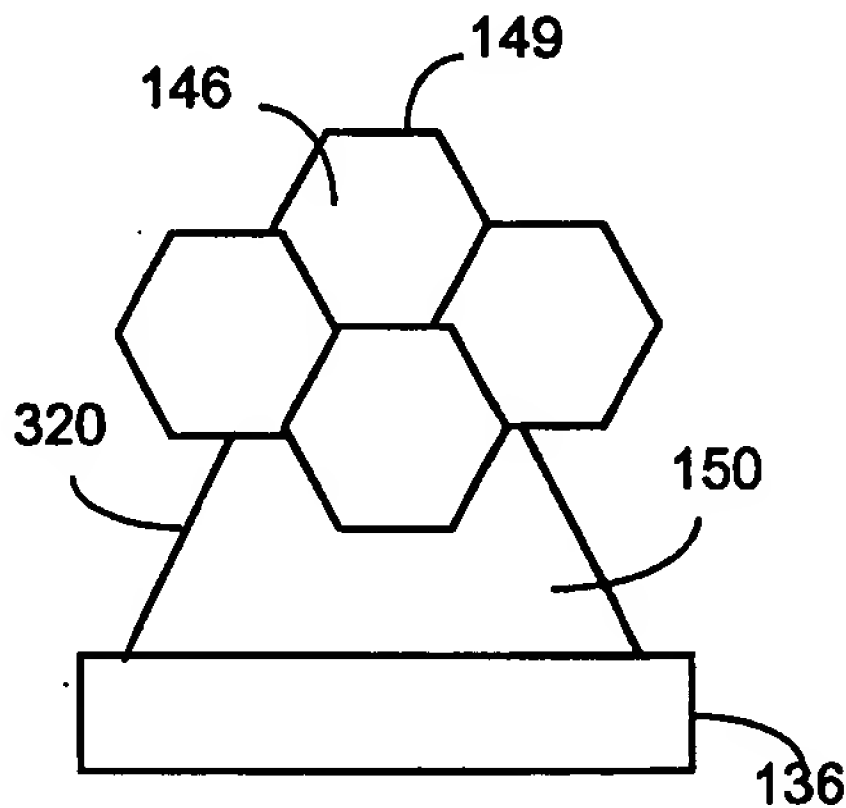


Figure 31

19/58

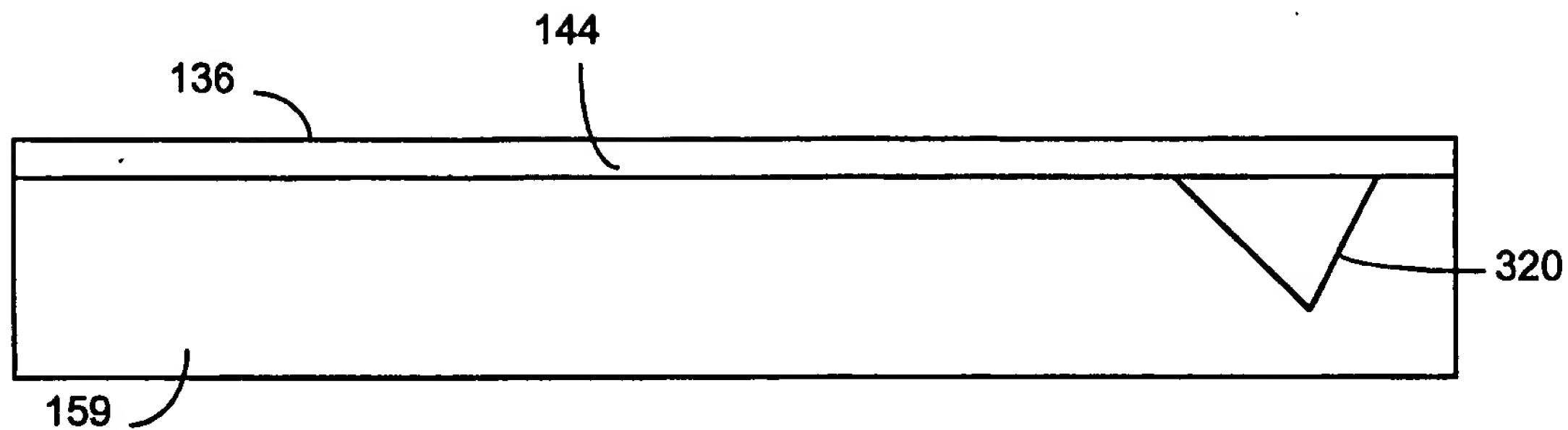


Figure 32

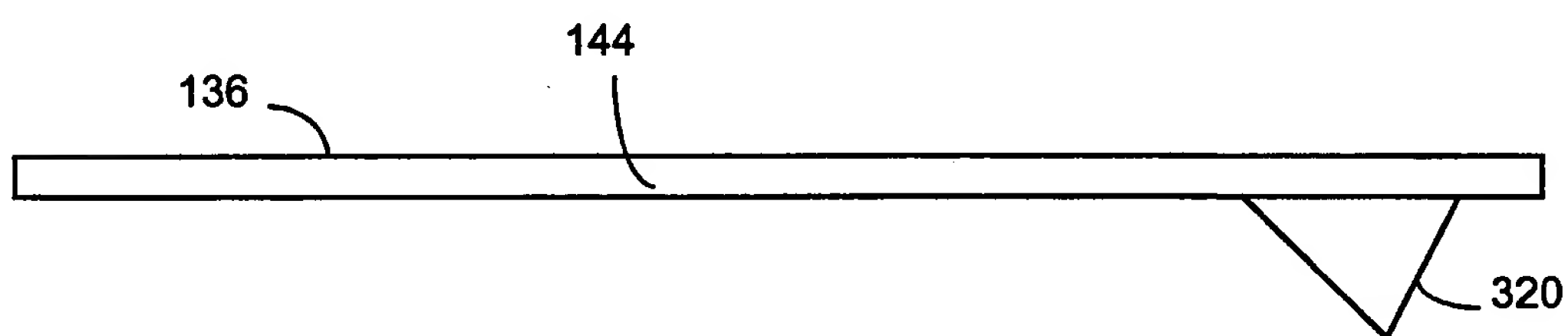


Figure 33

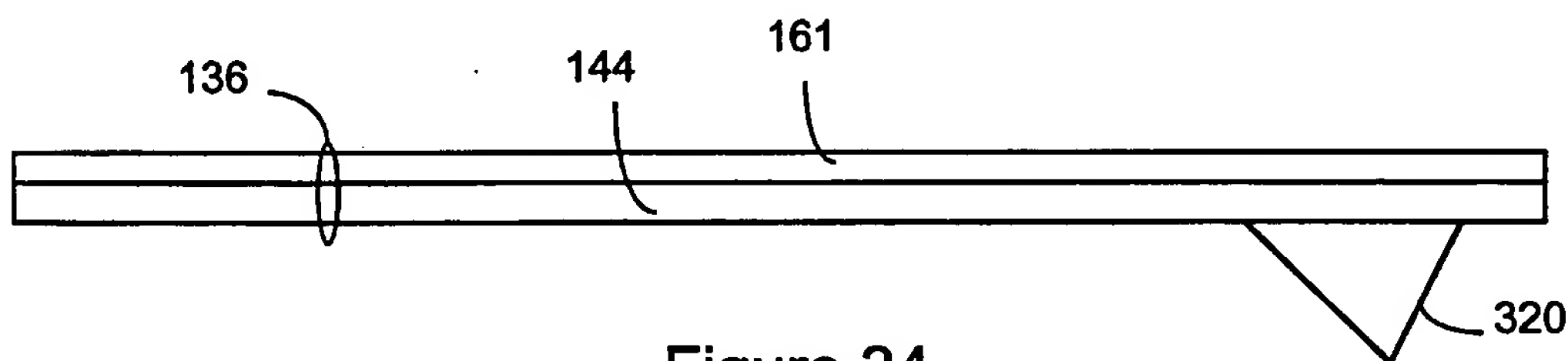


Figure 34

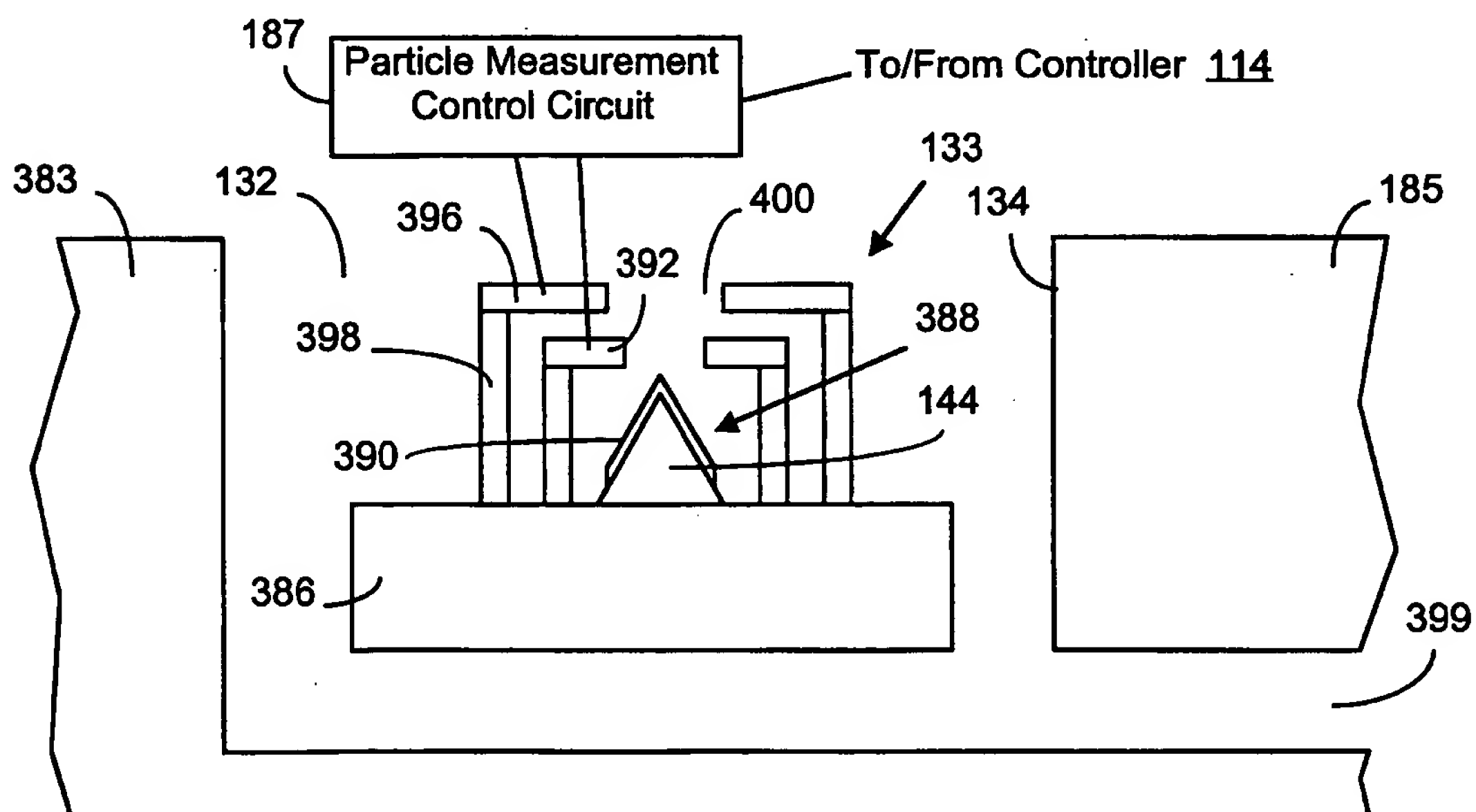


Figure 87

For filing only

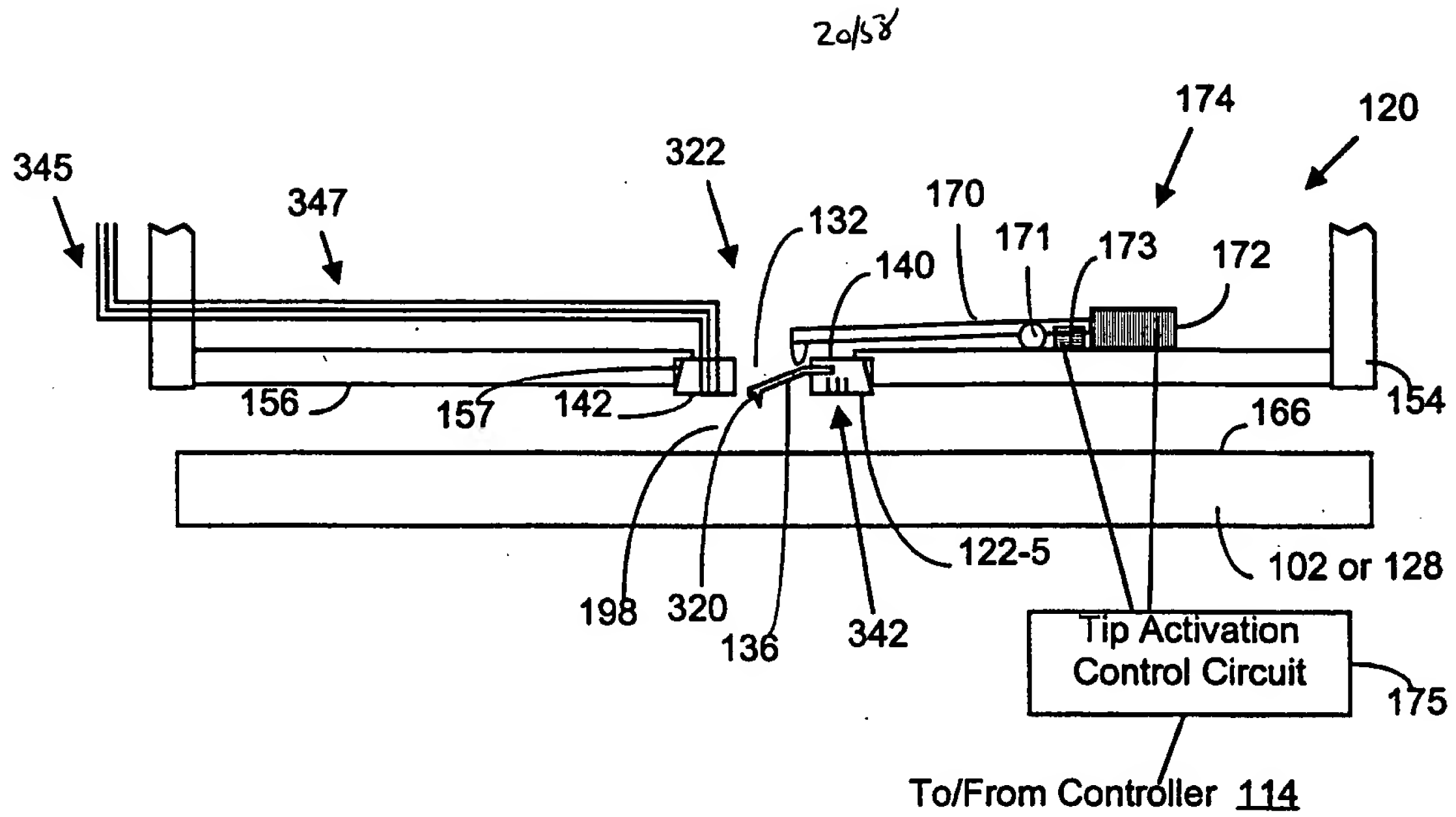


Figure 35

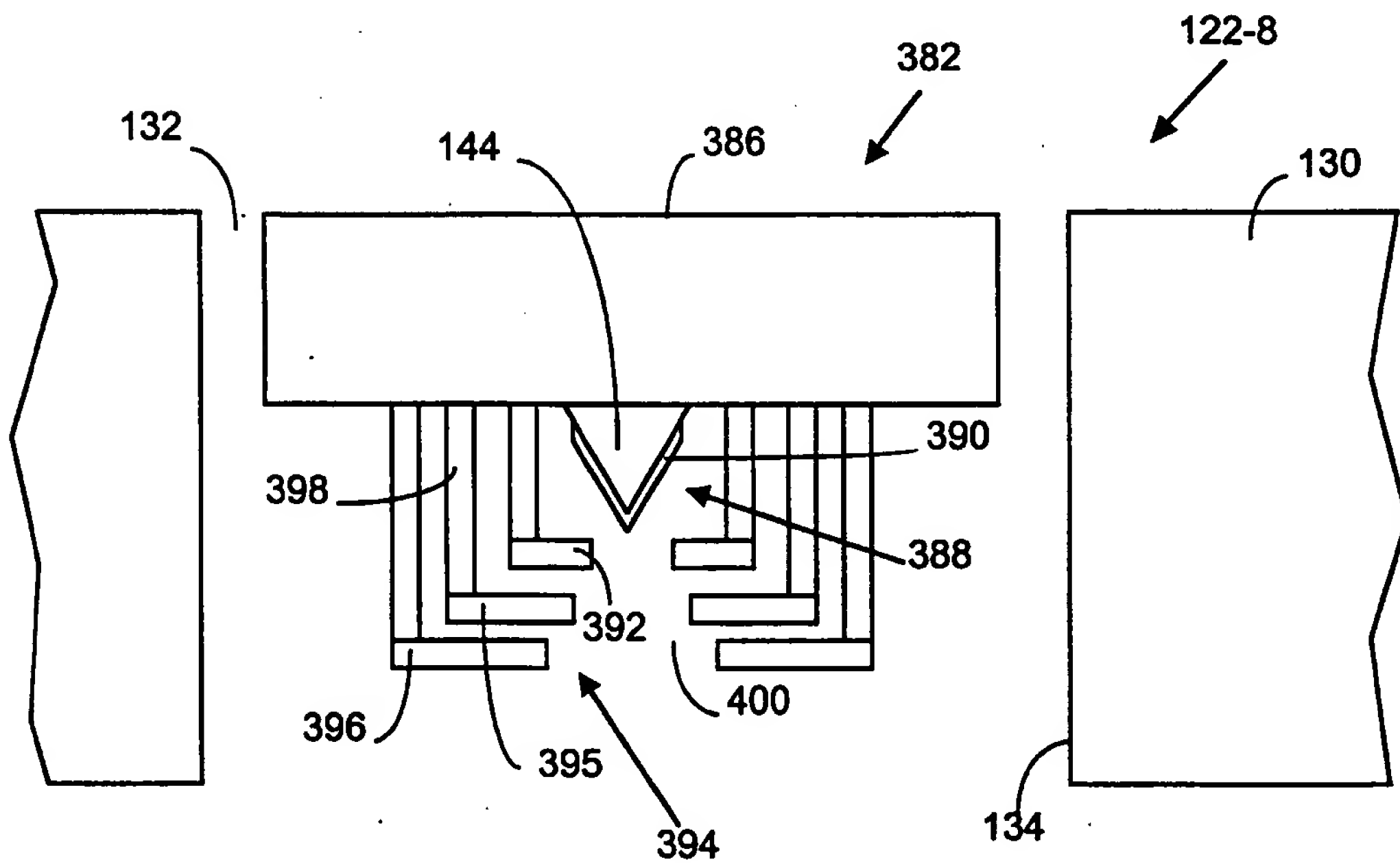


Figure 41

21/58

122-6

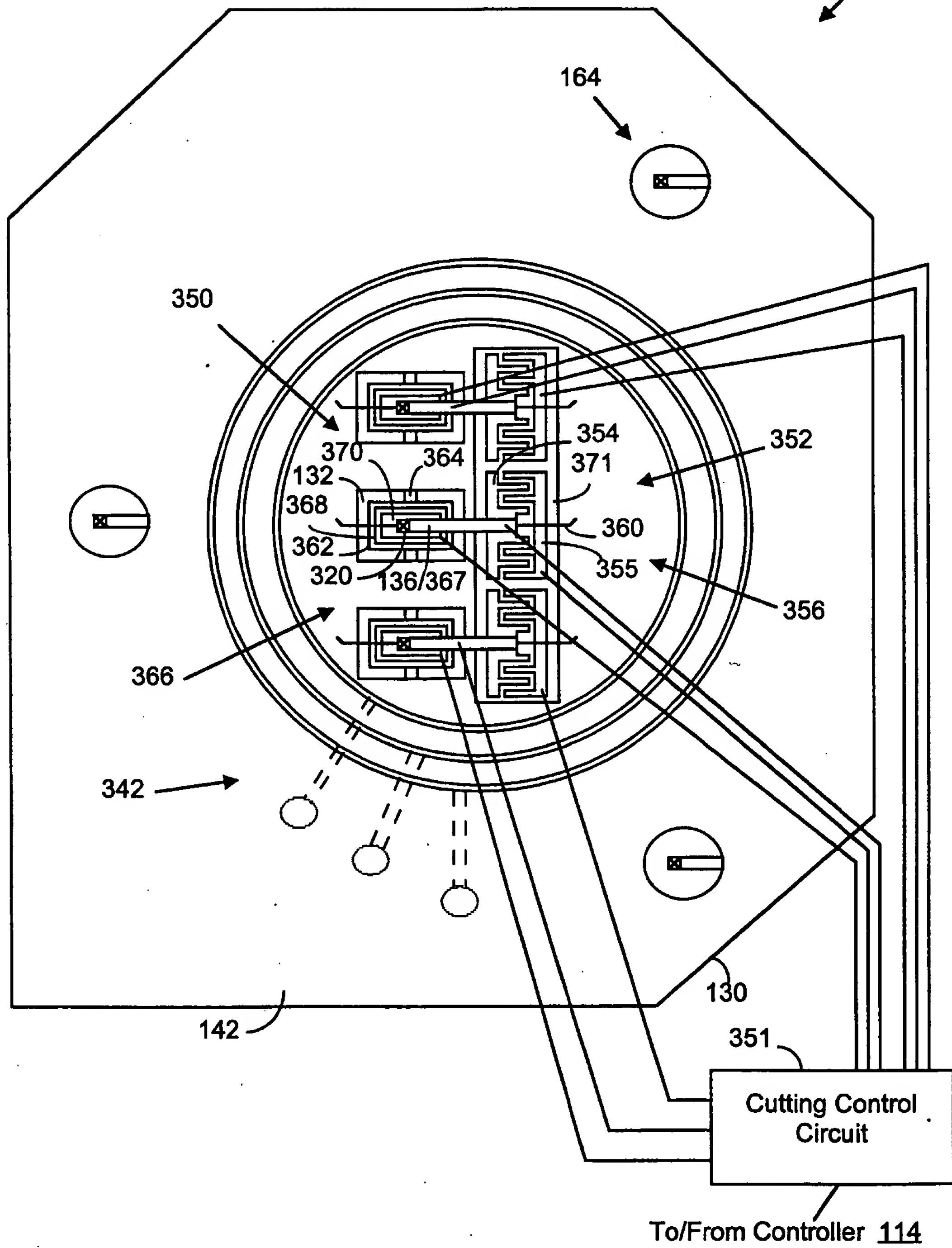


Figure 36

22/58

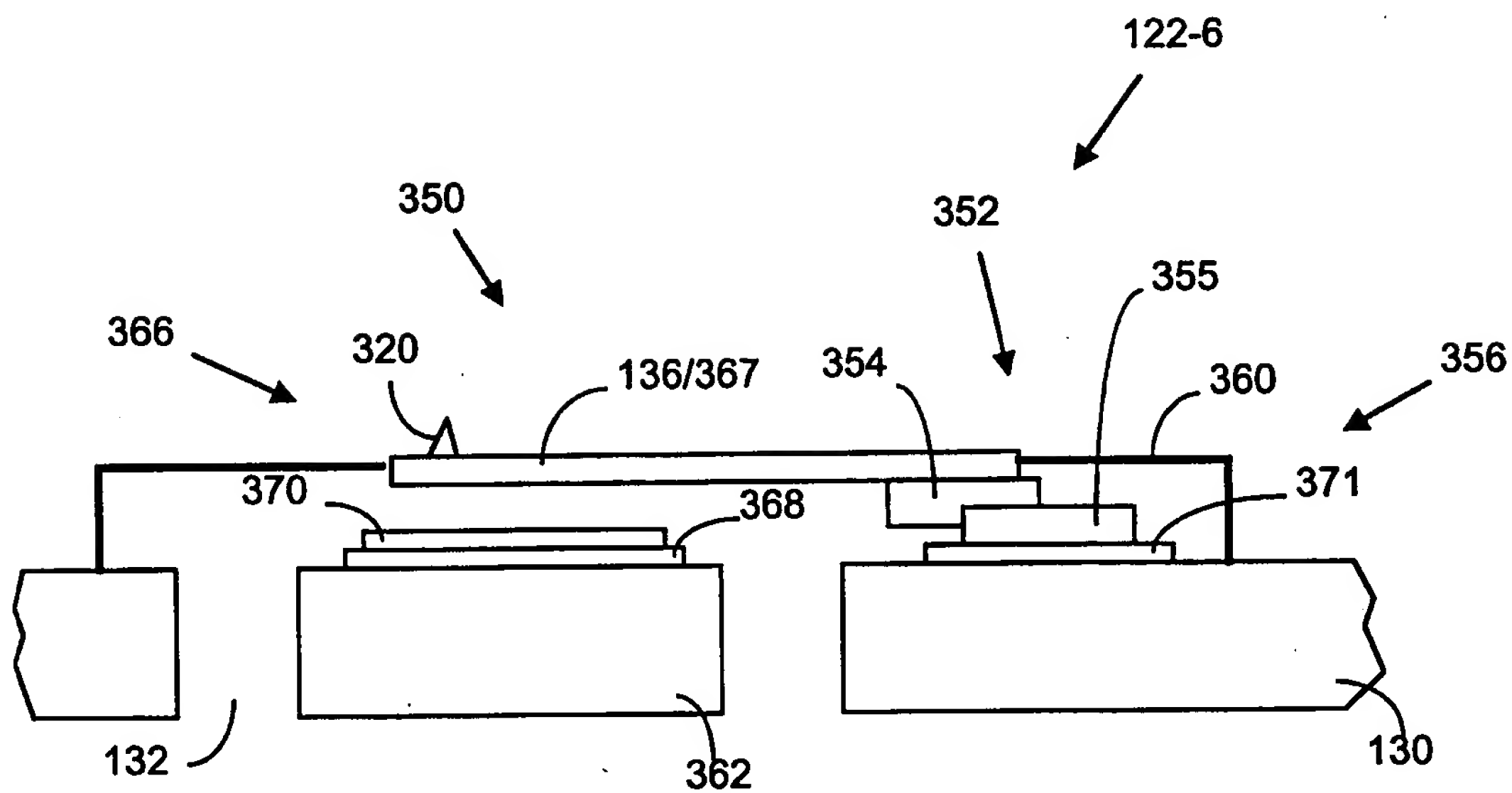


Figure 37

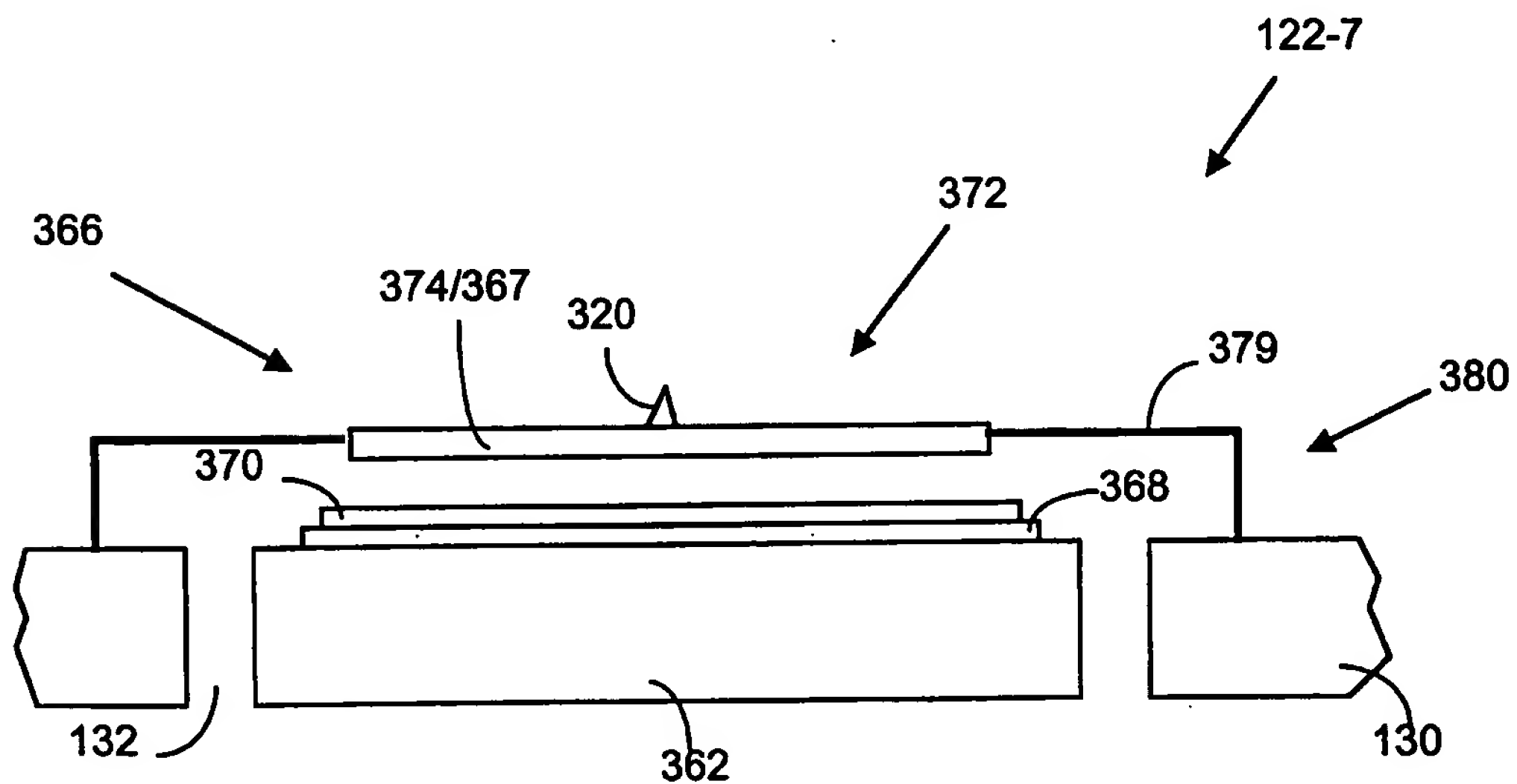


Figure 39

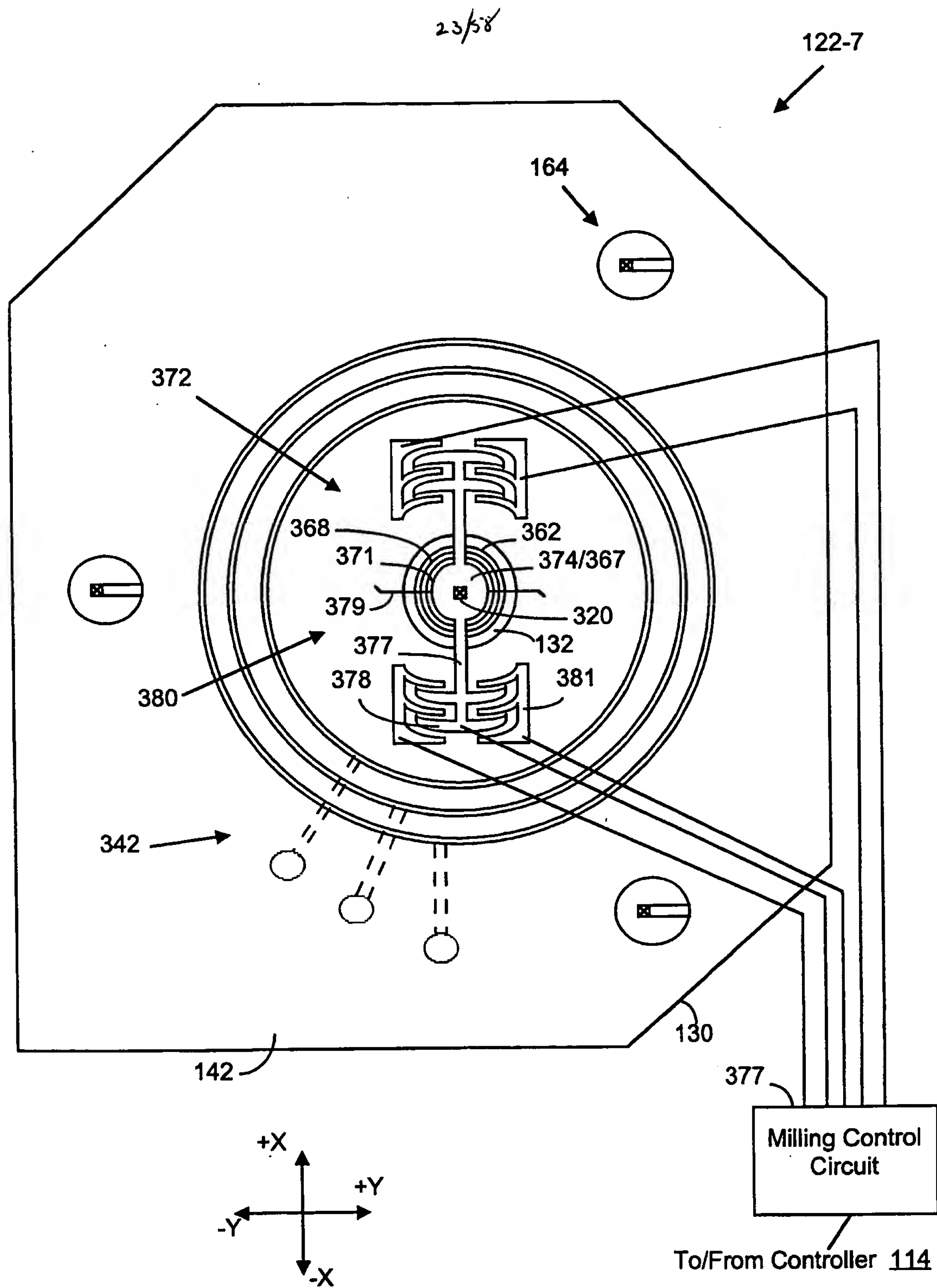


Figure 38

SUBSTITUTE SHEET (RULE 26)

24/5-8

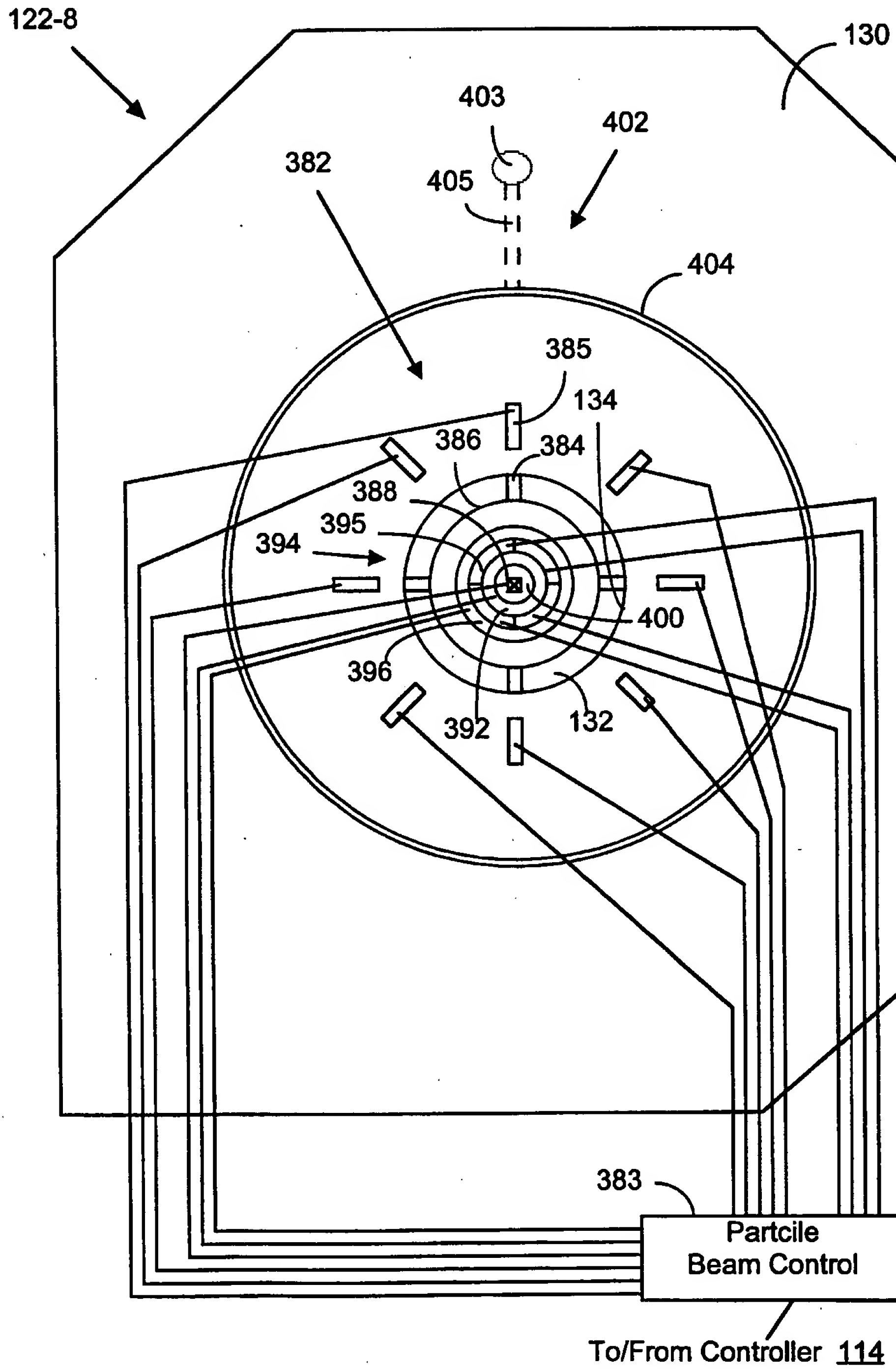


Figure 40

25/58

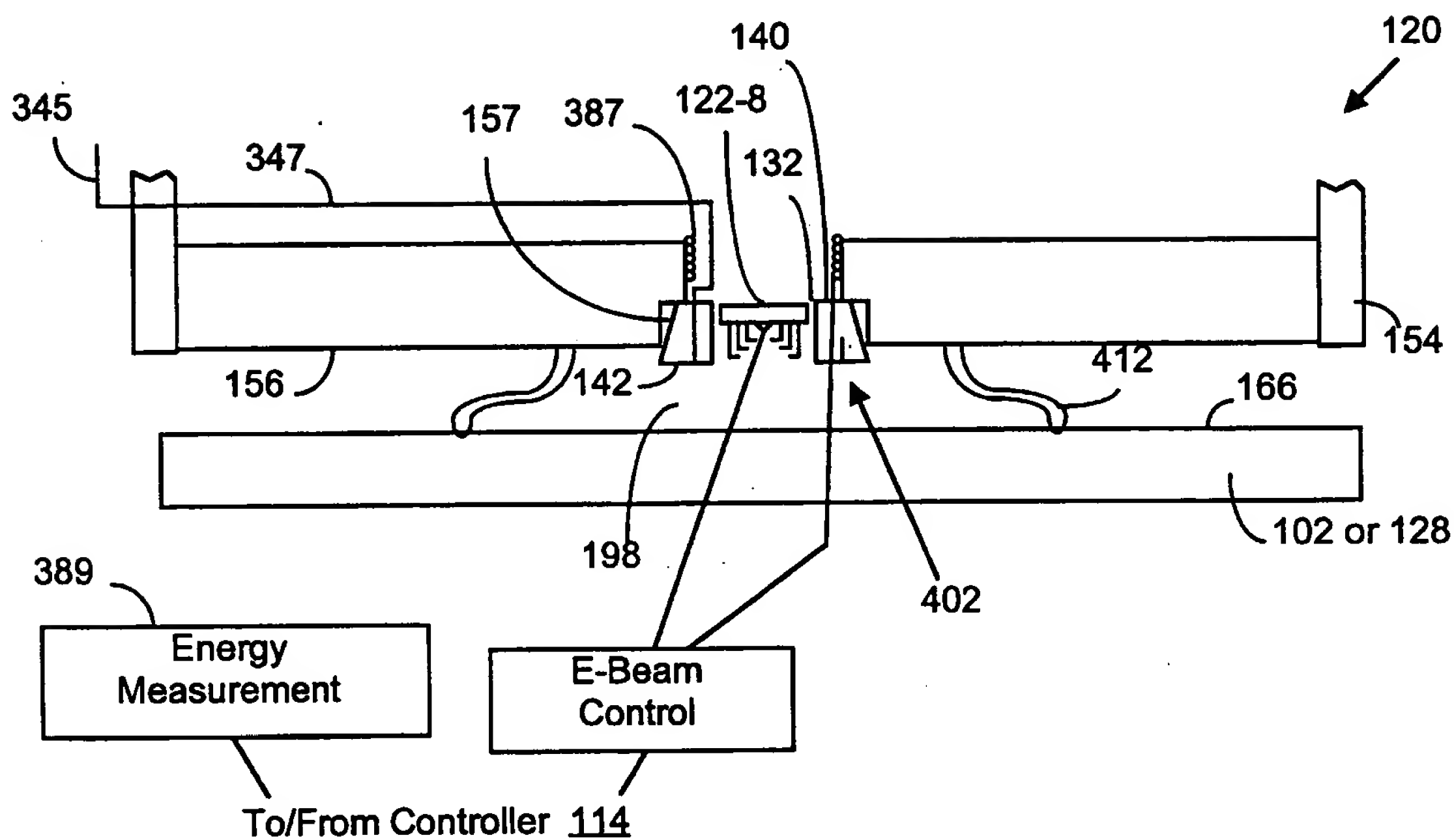


Figure 42

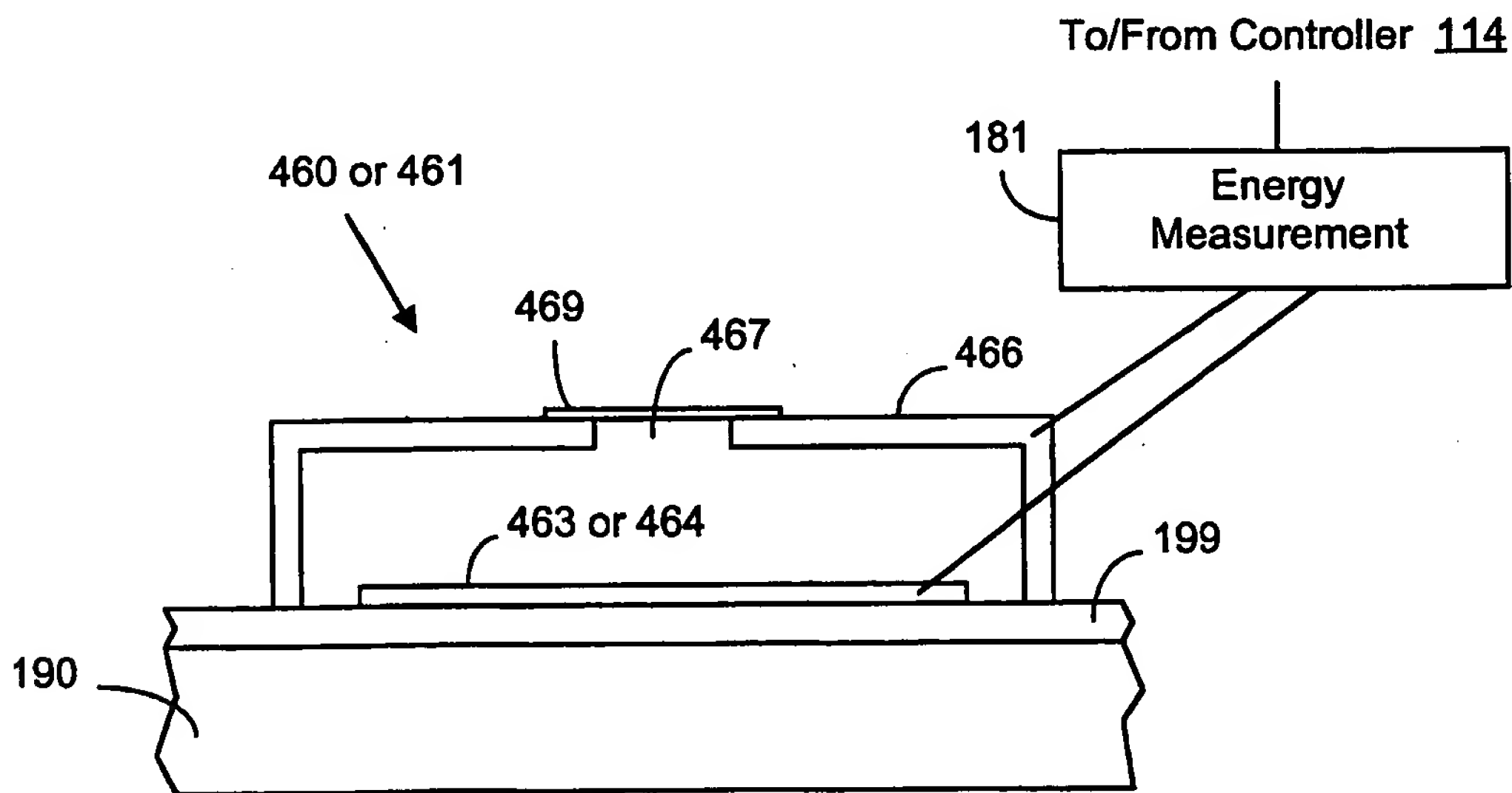


Figure 52

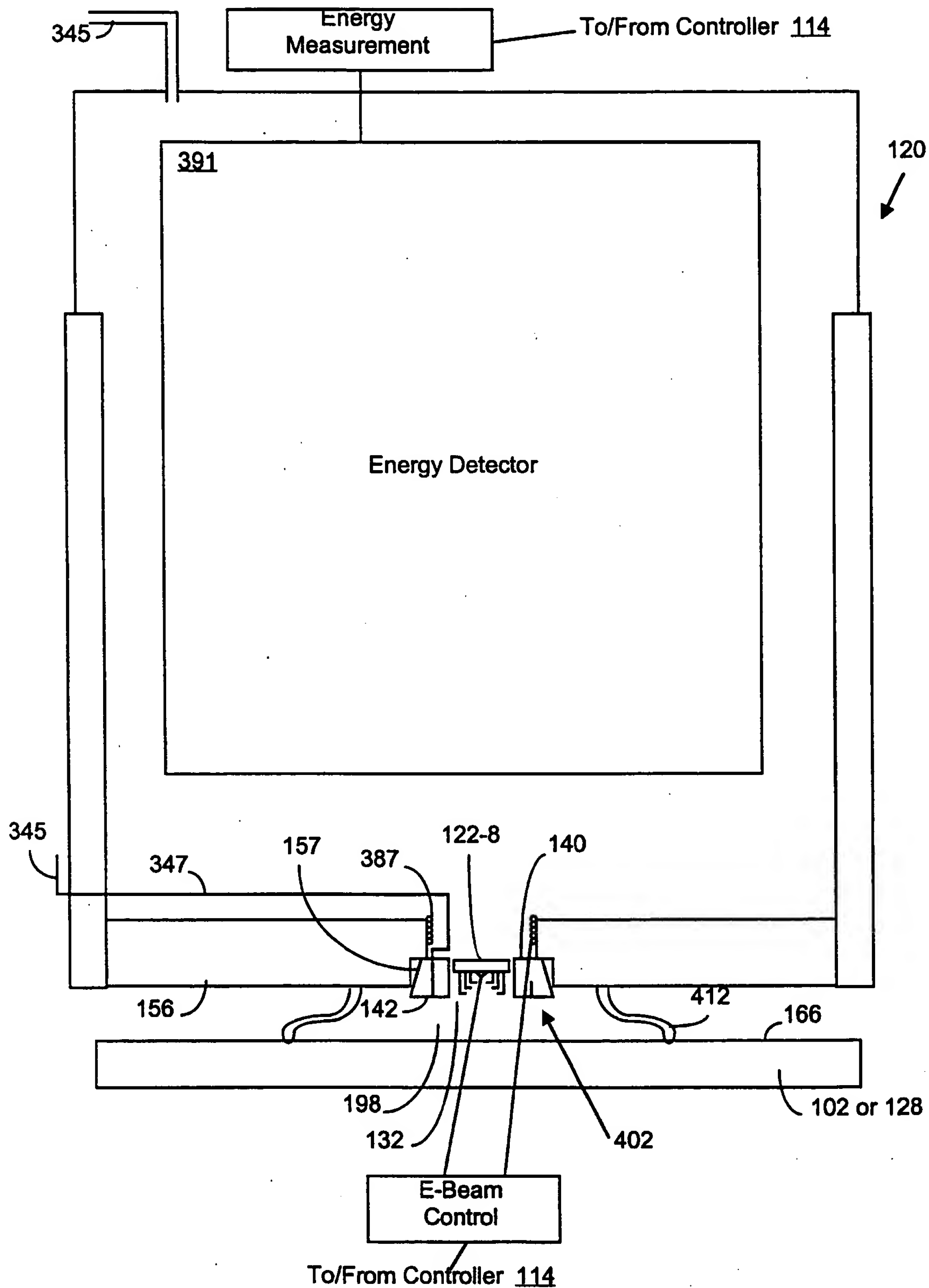


Figure 43

27/58

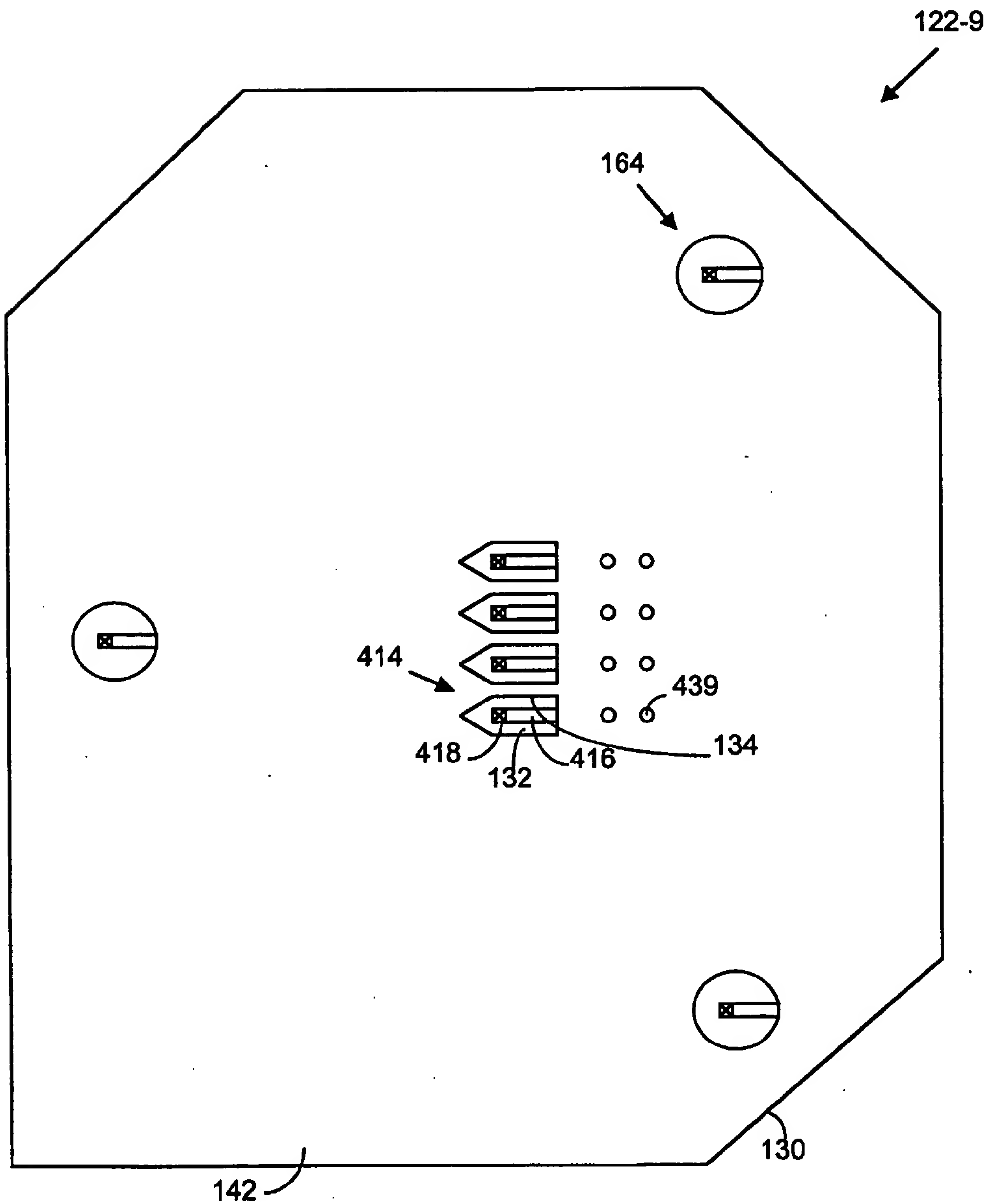


Figure 44

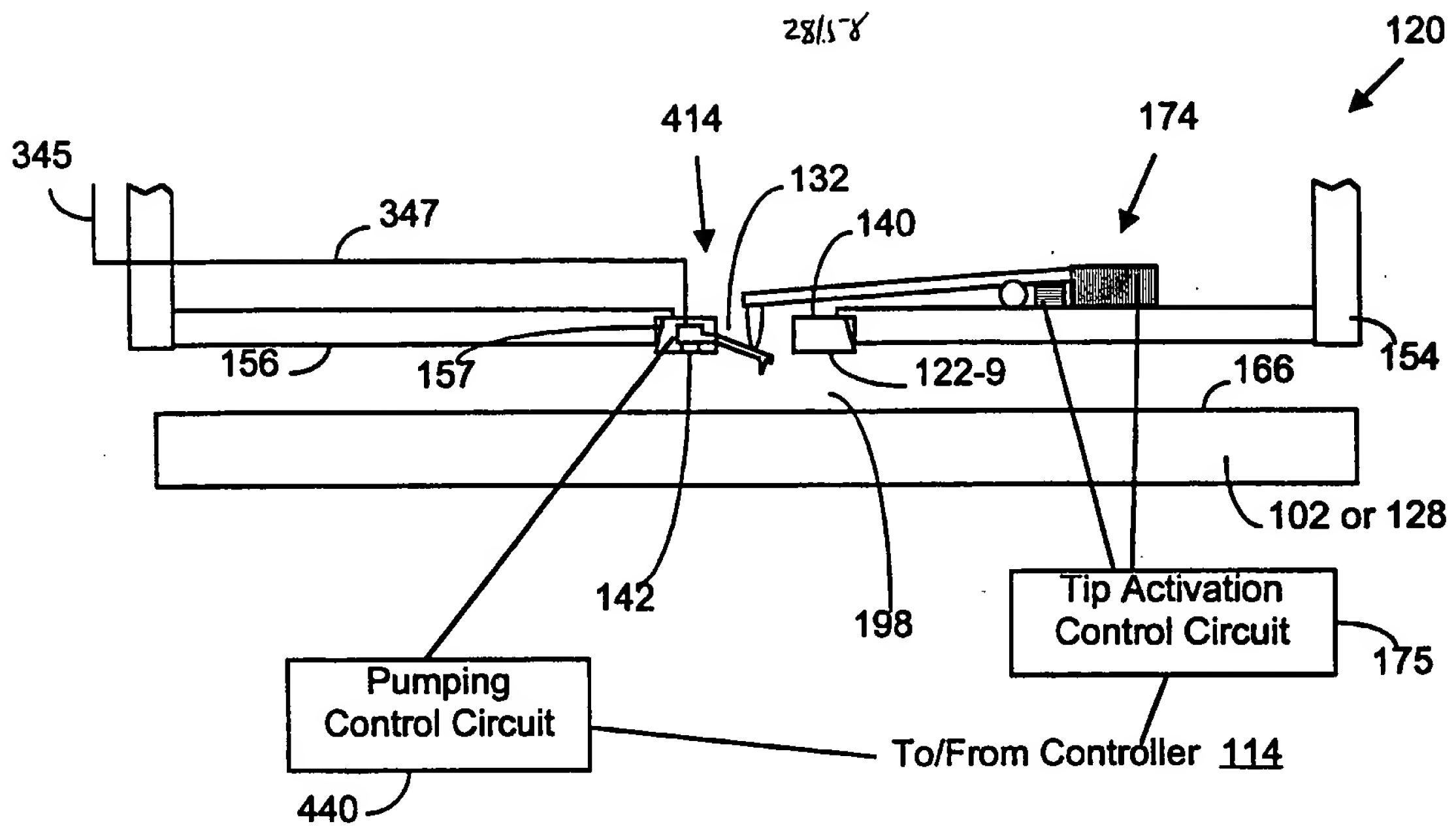


Figure 46

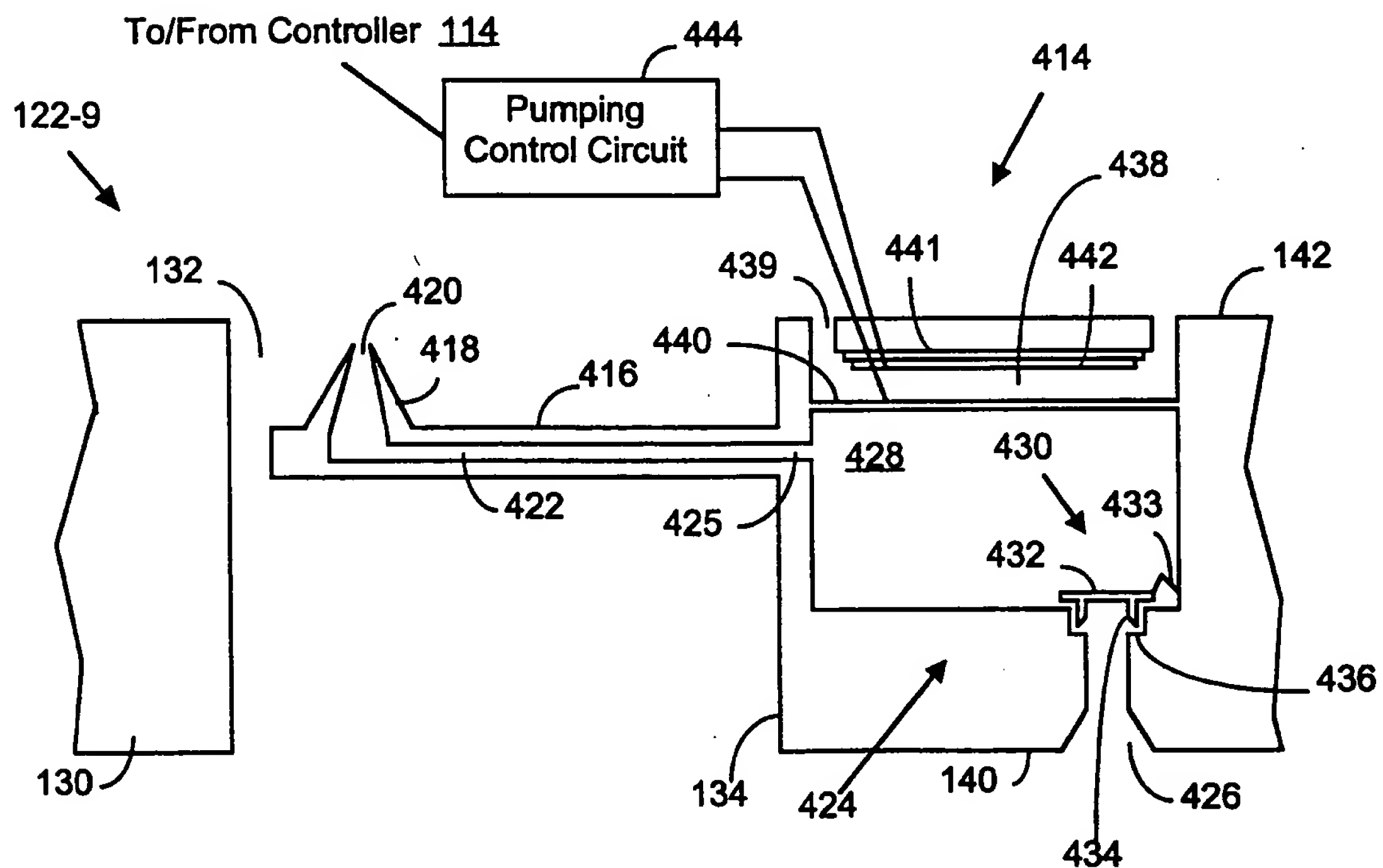


Figure 45

29/58

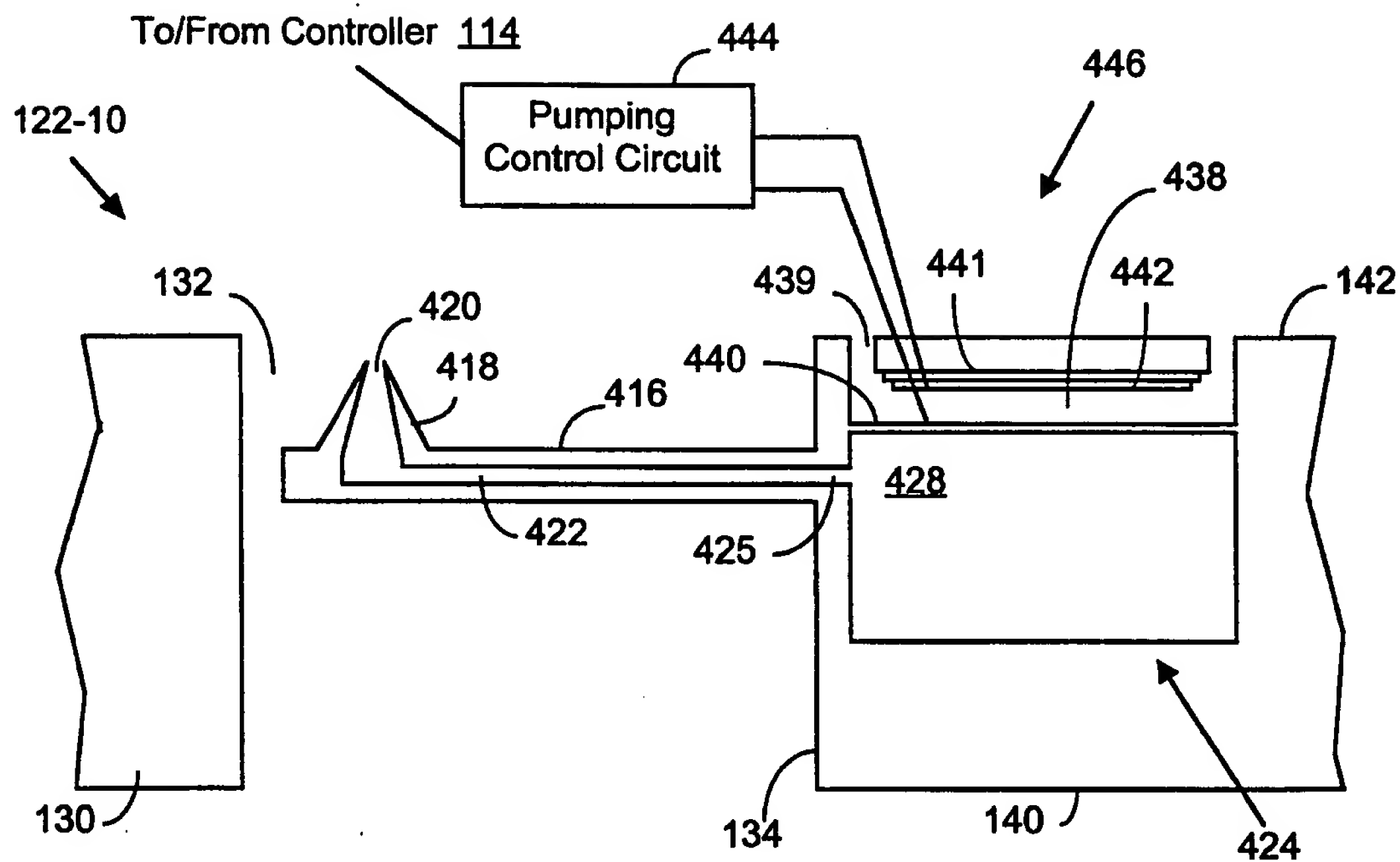


Figure 48

142

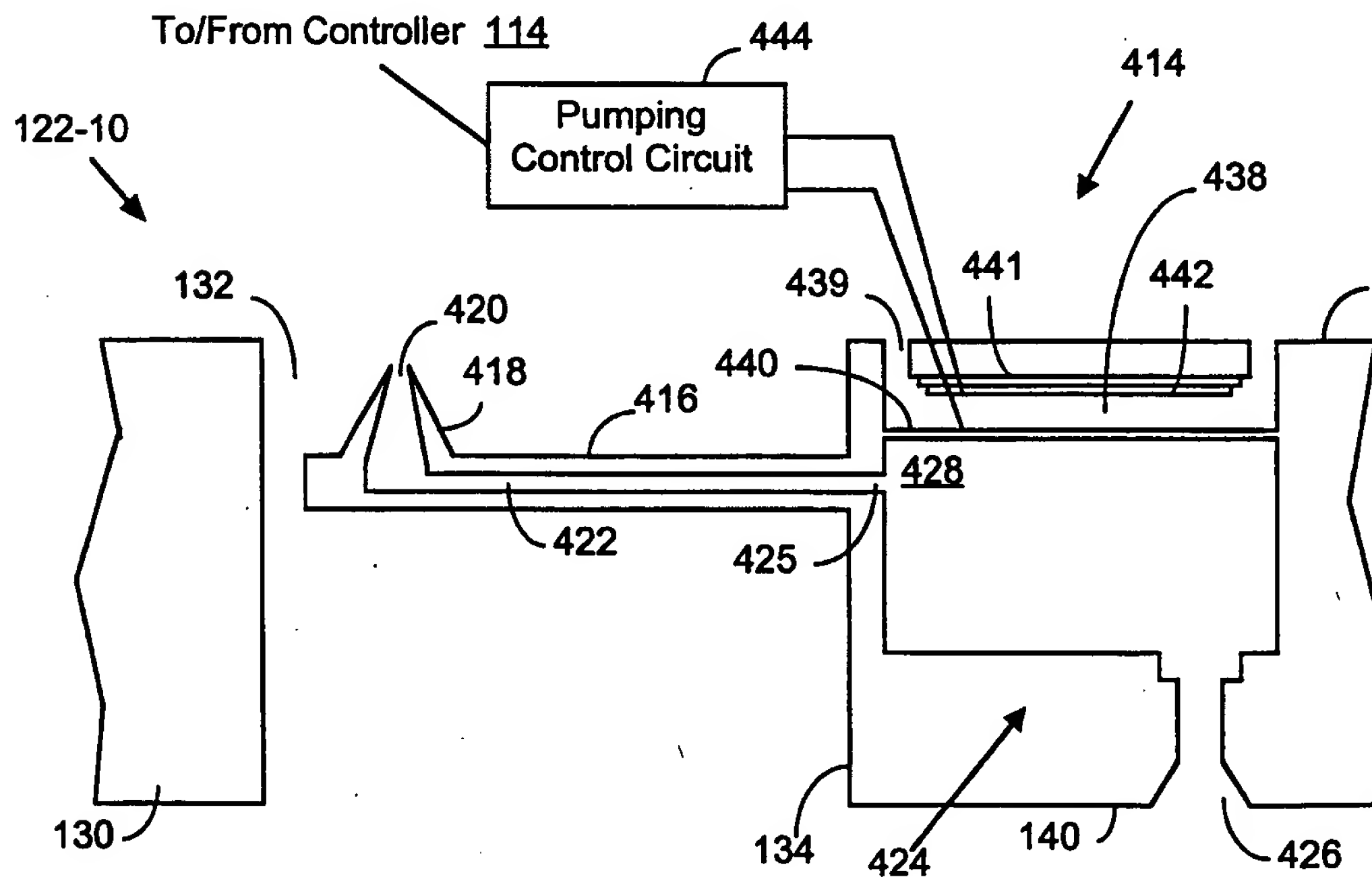


Figure 47

30/58

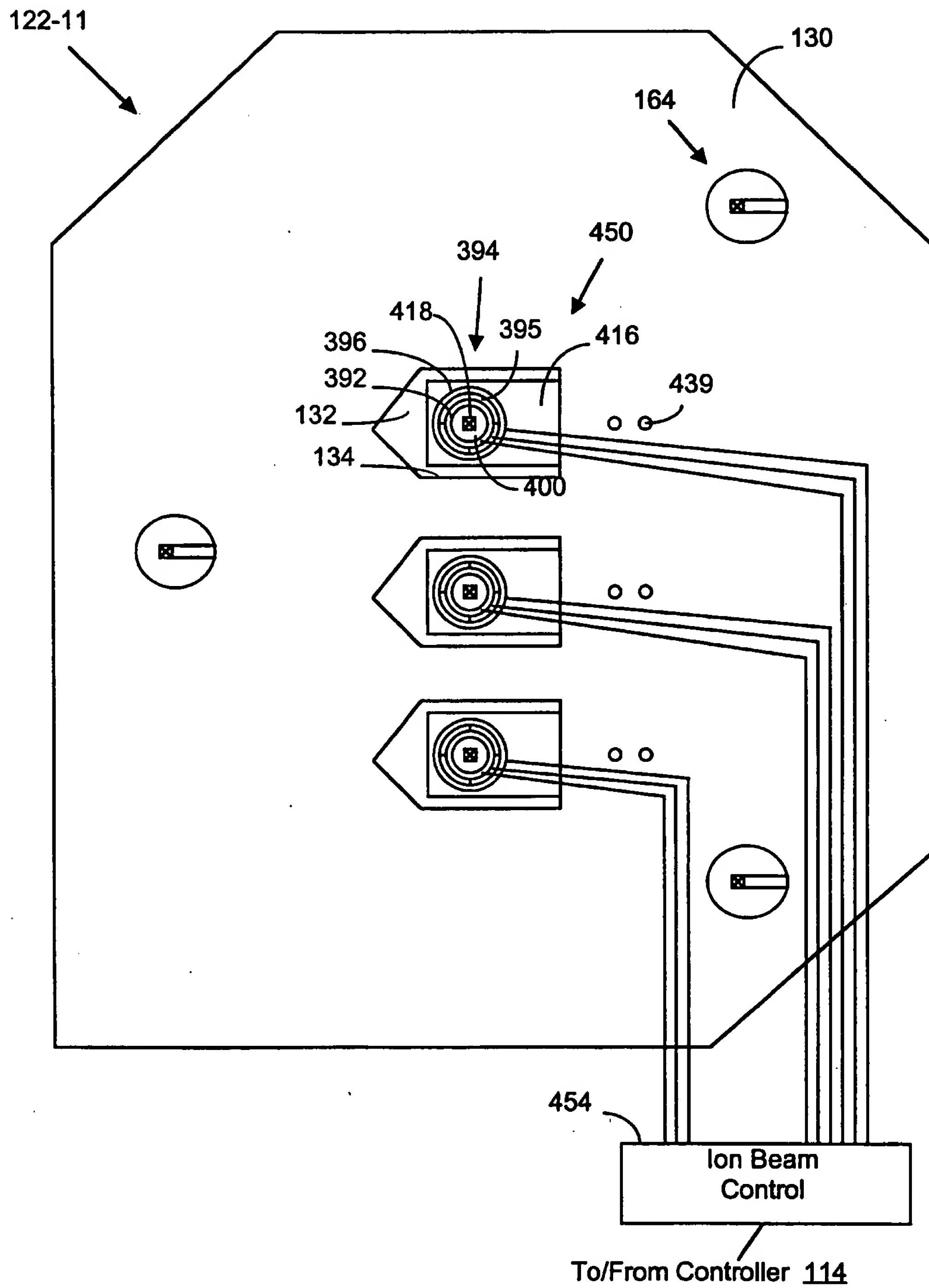


Figure 49

31/58

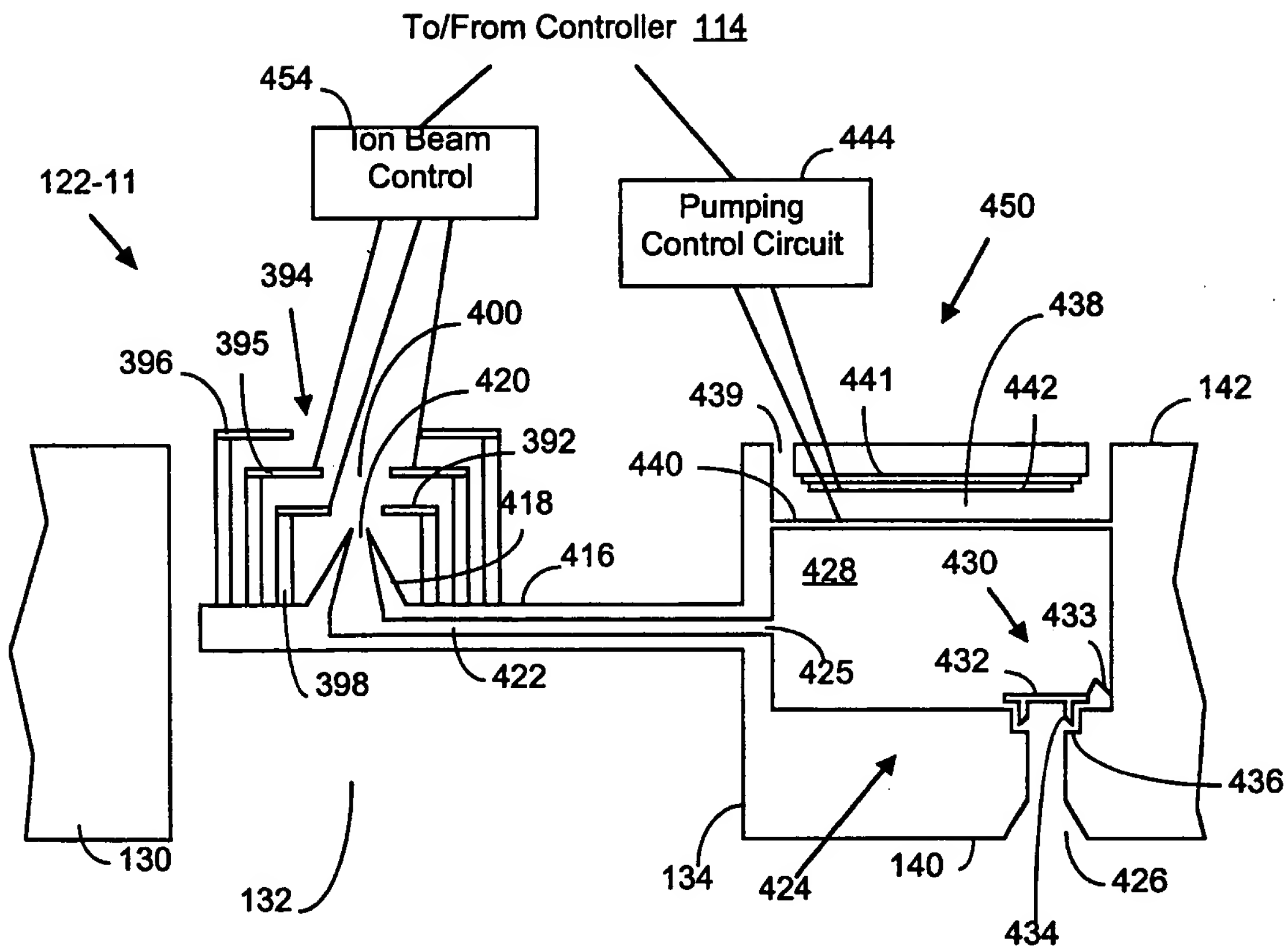


Figure 50

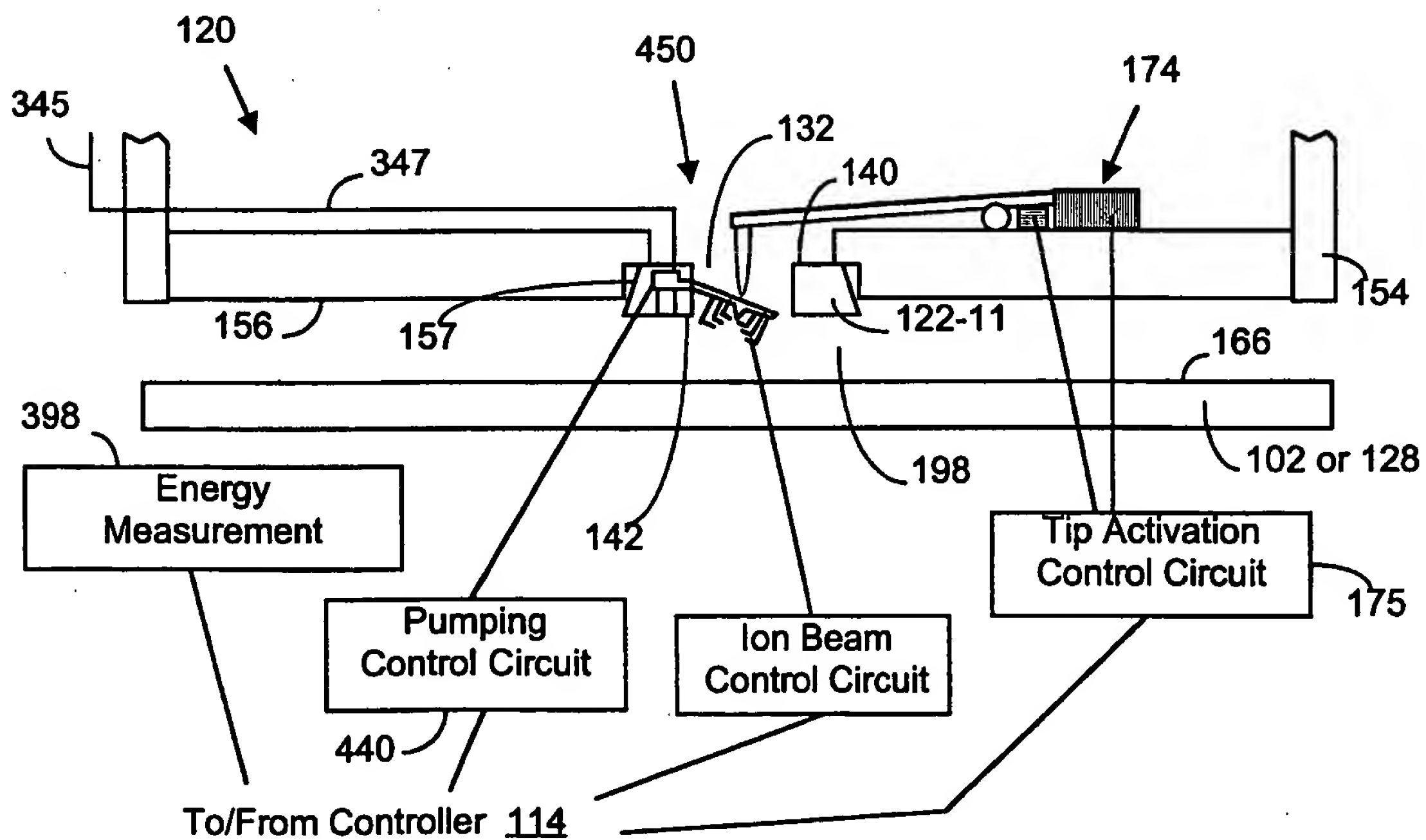


Figure 51

32/58

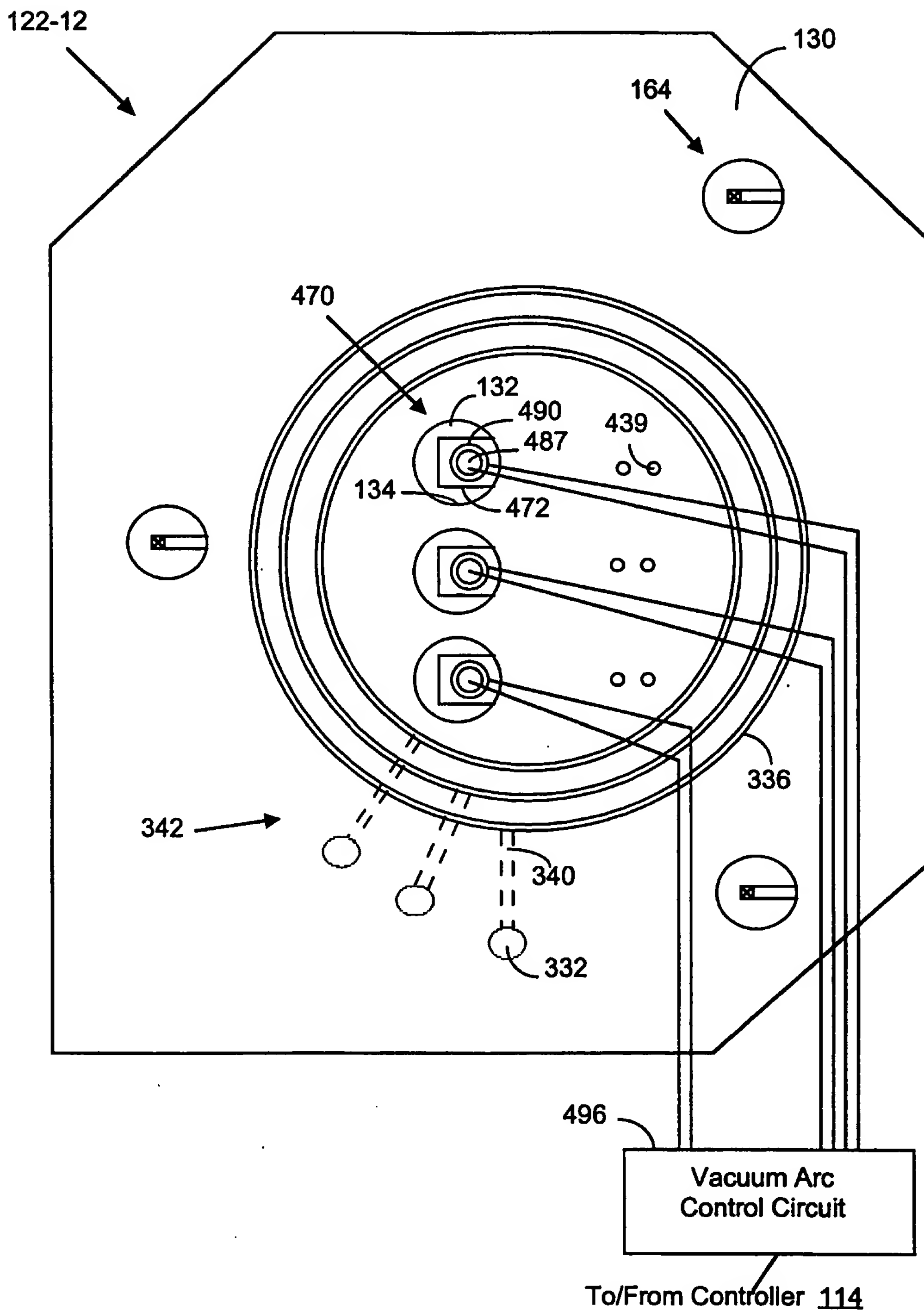


Figure 53

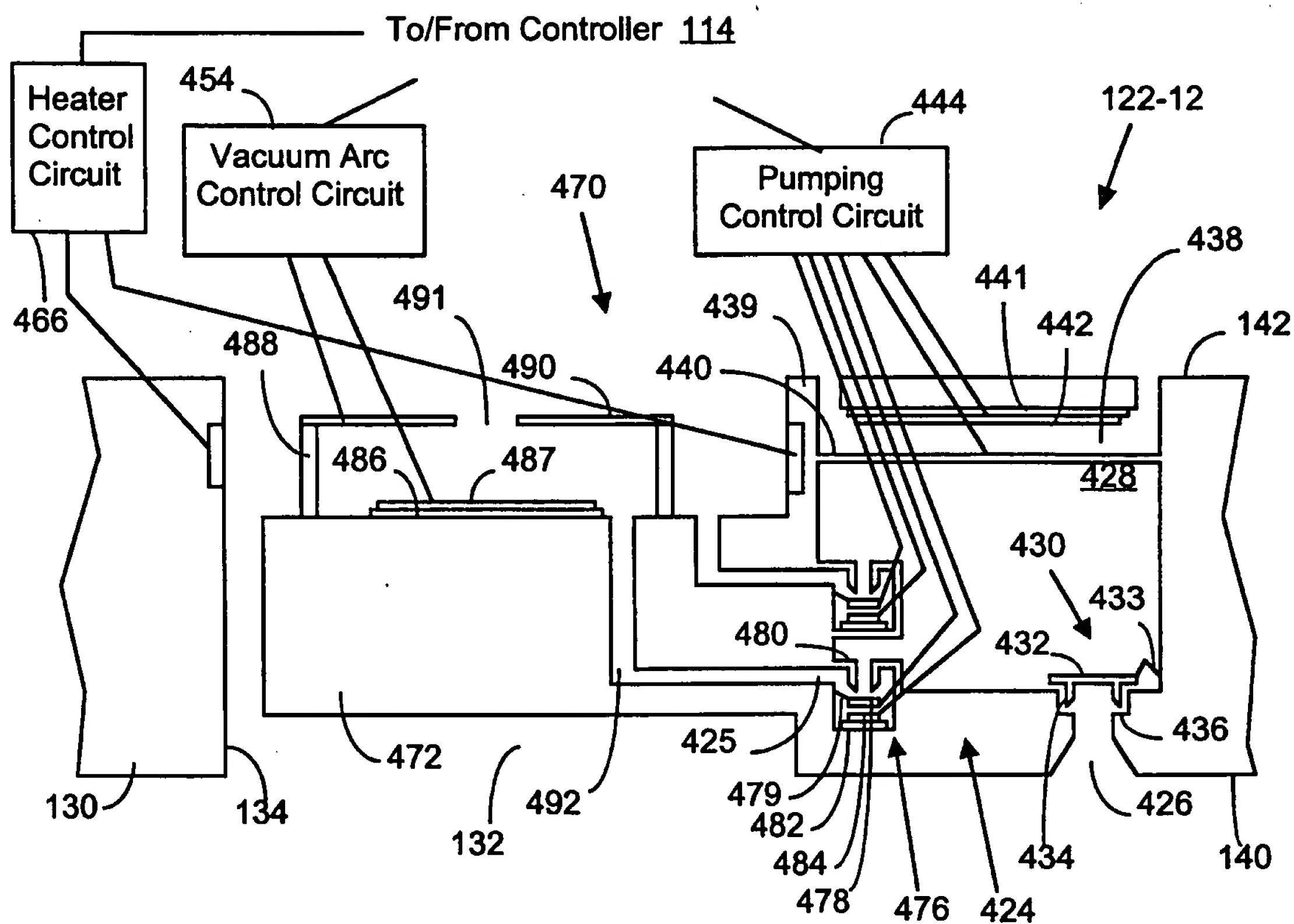


Figure 54

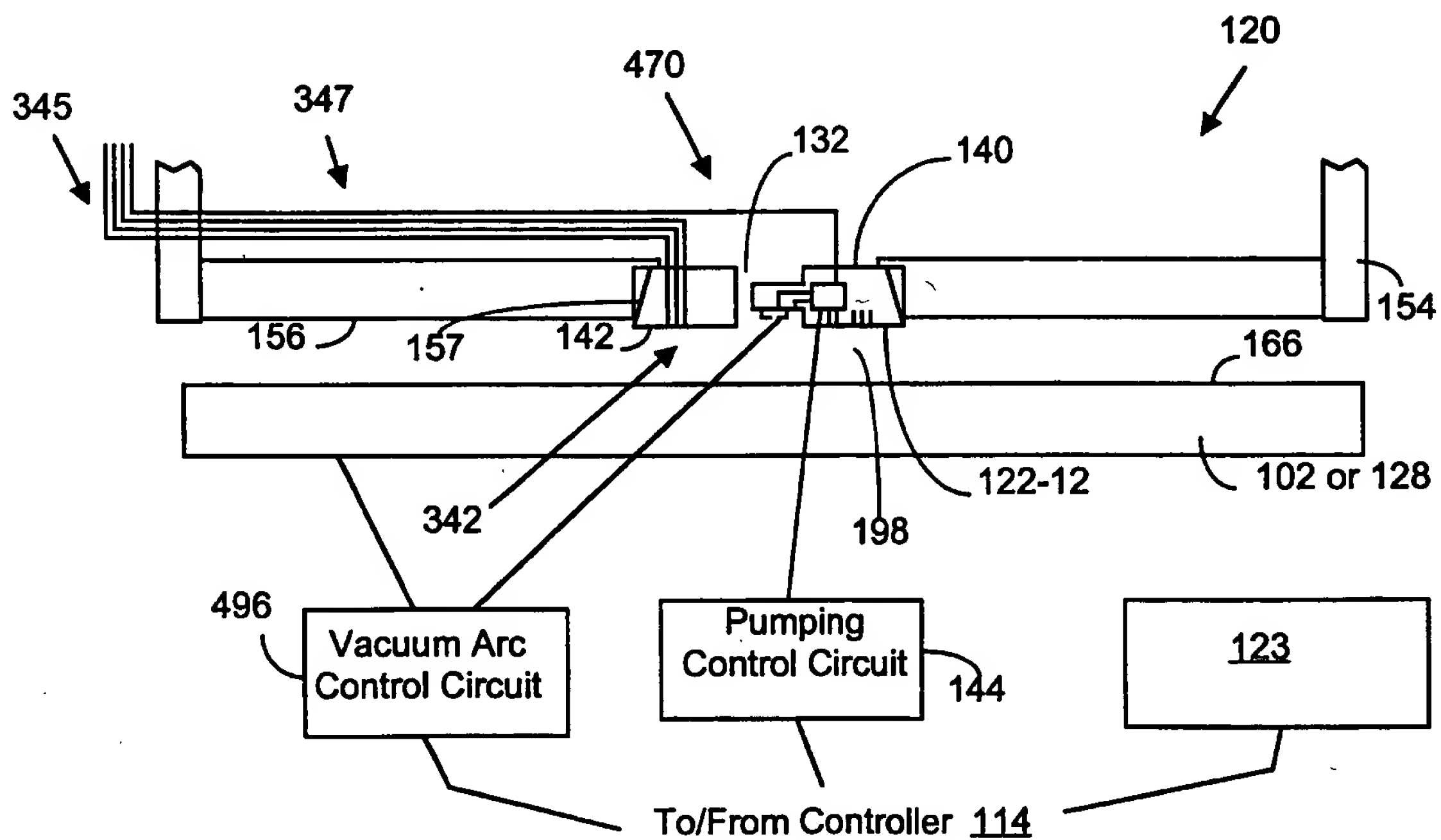


Figure 55

34/58

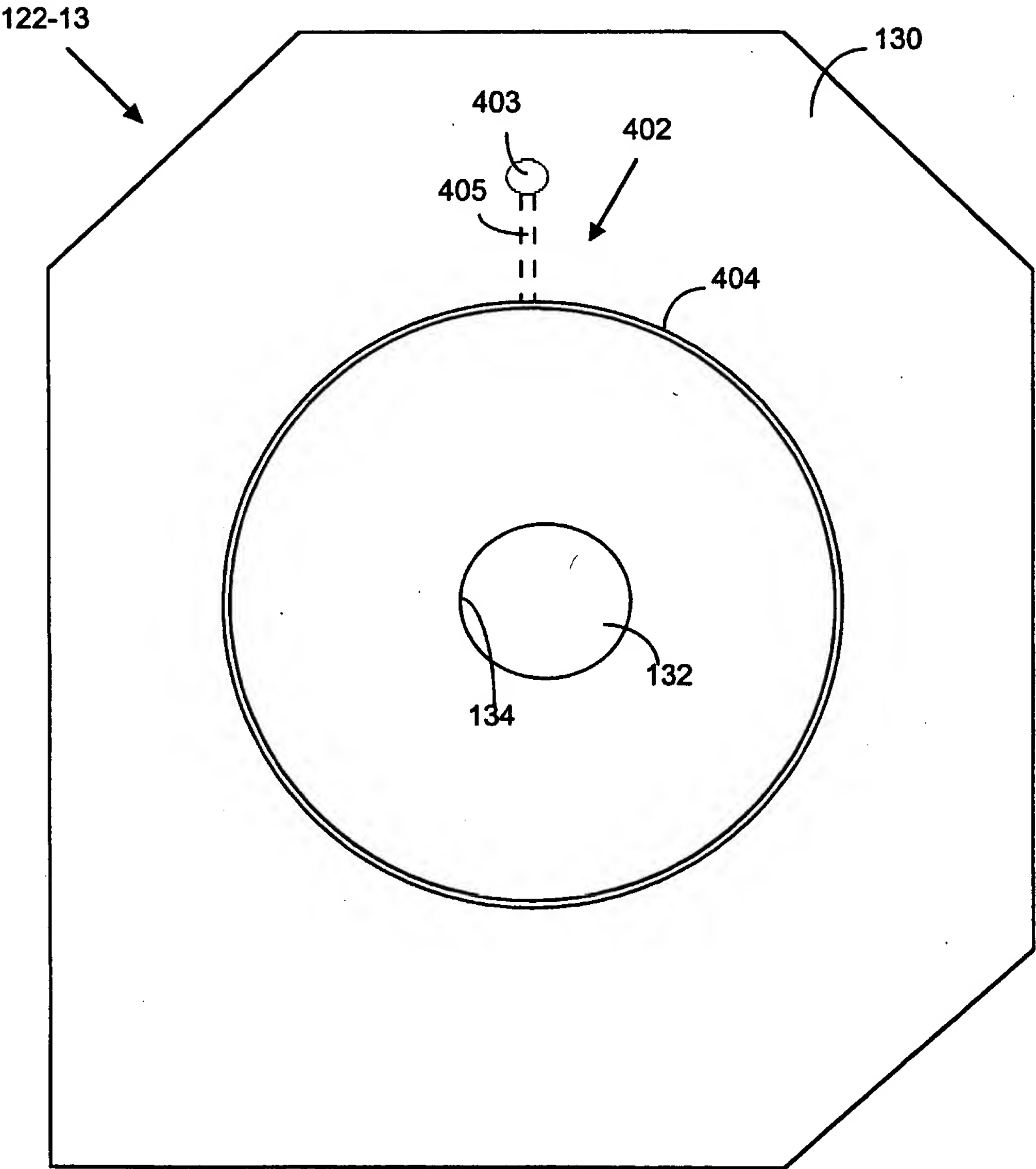


Figure 56